

Catalyst 9400 SVL دادعإ ىلع CPLD ةقورت

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ةمدقملا

دقعملال يقطنملا زاهجلا" رادصل ةقورت ةيفيك لوح ةوطخب ةوطخ عارجا دننتملا اذه فصى
Catalyst 9400 StackWise Virtual Link (SVL) دادعإ ىلع "CPLD) ةجمربلل لباقللا

ةيساسألا تابلطتملا

تابلطتملا

ةدام ىلع ليكشتو StackWise-Virtual setup نم ةيساسألا ةفرعم تنأ ىقلى نأ ىصوي cisco
9400 ةزافح

ةمدختسملا تانوكملا

ةيدامل تانوكملا وجماربل تارادصل ىل دننتملا اذه يف ةدراول تامولعمل دننست
PID:C9404R.

ةيطمنلا تادحول:

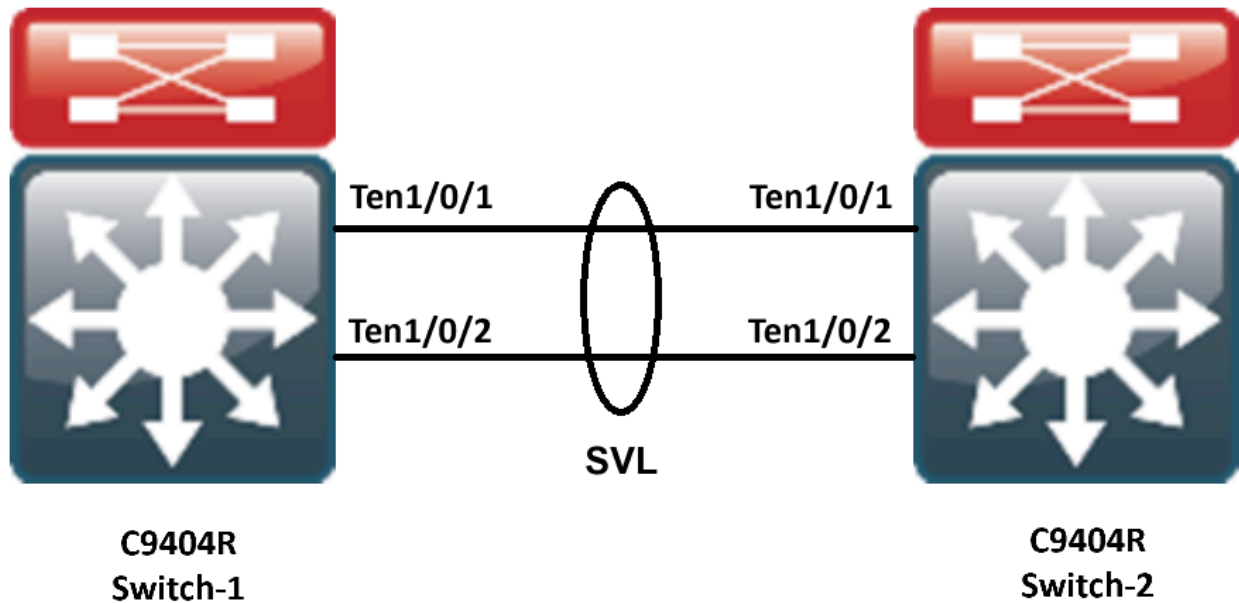
C9400-SUP-1XL

C9400-LC-24XS زارطل

رادصل SW: 17.09.4a

ةصاخ ةيلمعم ةئيب يف ةدوجوملا ةزهجال نم دننتملا اذه يف ةدراول تامولعمل عاشنإ مت
تناك اذإ. (ىضارتفا) حوسمم نيوكتب دننتملا اذه يف ةمدختسملا ةزهجال عيمج تادب
رمأ يال لمتمحمل ريثأتلل كمهف نم دكأتف، ليغشتلا ديق كتكبش

ططخملا



ۆل كشملا

قPLD لآ ۆردانلا تالاحلا ضعب يف ۆغيف ص cisco ios ۆ نسحي امنيف ، 9400 ۆزافح ۆدام يف نم نيفعمل رادصل ل ROMMON رادصل او CPLD نم ققحتلا نكمي . نسحي ال ۆادال نم ۆغيف ص https://www.cisco.com/c/en/us/td/docs/switches/lan/catalyst9400/software/release/17-13/release_notes/ol-17-13-9400/rommon_versions.html link.

ۆصاصق اذه تلحأ

<#root>

9400-1#show platform
Chassis type: C9404R

Switch 1

Slot	Type	State	Insert time (ago)
1	C9400-LC-24XS	ok	00:05:00
1/0	C9400-LC-24XS	ok	00:00:56
2	C9400-SUP-1XL	ok	00:05:00
2/0	C9400-SUP-1XL	ok	00:00:53

ok, active

P1	00:05:00	C9400-PWR-3200AC	ok	00:04:38
P9		C9404-FAN	ok	00:04:37

Slot	CPLD Version	Firmware Version
1	20062105	17.10.1r
2	20062105	17.10.1r

Image Version = Cisco IOS Software [Cupertino], Catalyst L3 Switch Software (CAT9K_IOSXE), Version 17.9
Technical Support: <http://www.cisco.com/techsupport>
Copyright (c) 1986-2023 by Cisco Systems, Inc.
Compiled Fri 20-Oct-23 10:44 by mcpre
BOOT = bootflash:packages.conf;

Peer Processor Information :

Standby Location = Switch 2 >>>>>>>>>Switch 2 is standby

Current Software state = STANDBY HOT
Uptime in current state = 0 minutes
Image Version = Cisco IOS Software [Cupertino], Catalyst L3 Switch Software (CAT9K_IOSXE), Version 17.9
Technical Support: <http://www.cisco.com/techsupport>
Copyright (c) 1986-2023 by Cisco Systems, Inc.
Compiled Fri 20-Oct-23 10:44 by mcpre
BOOT = bootflash:packages.conf;

رمأ اذه ةغيص CPLD لآ تنسح in order to تلمعتسا 2. ةوطخال

<#root>

upgrade hw-programmable cpld filename bootflash: switch {1-2} RP active

<#root>

9400-1#

upgrade hw-programmable cpld filename bootflash: switch 2 R0

Firmware upgrade requires the SVL member to reload. Do you want to proceed?(y/n)y
Use cli 'redundancy reload peer' to upgrade the firmware on peer

9400-1#

redundancy reload peer

Stack is in Half ring setup; Reloading a switch might cause stack split
Reload peer [confirm]
Preparing to reload peer

هتبقرت متي CPLD نأ ىرت نأ كنكمي انه 3. ةوطخال

<#root>

Chassis 2 reloading, reason - Admin reload CLI
May 4 01:34:23.829: %PMAN-5-EXITACTION: R0/0: pvp:

Initializing Hardware.....

System Bootstrap, Version 17.10.1r, RELEASE SOFTWARE (P)
Compiled Tue Aug 2 13:02:51 2022 by rel

Current ROMMON image : Primary
Last reset cause : SoftwareResetTrig
C9400-SUP-1XL platform with 16777216 Kbytes of main memory

Starting System FPGA Upgrade

Programming SPI Primary image is completed.

Authenticating SPI Primary image

IO FPGA image is authenticated successfully.

Programming Header

FPGA HDR file size: 12

Image page count: 1

Verifying programmed header

Verifying programmed header

Programmed header is verified successfully.

!!

Power Cycle is needed to complete System firmware upgrade.
It takes ~7 mins to upgrade firmwre after power cycle starts.

DO NOT DISRUPT AFTER POWER C

Initializing Hardware.....

Initializing Hardware.....

System Bootstrap, Version 17.10.1r, RELEASE SOFTWARE (P)
Compiled Tue Aug 2 13:02:51 2022 by rel

دادعتسالا عضو ي CPLD ة يقرت تمت دق ناك اذا ام دي كأت 4. ةوطخلال

<#root>

F241.24.02-9400-1#show platform

Chassis type: C9404R

Switch 1

Slot	Type	State	Insert time (ago)
------	------	-------	-------------------

1	C9400-LC-24XS	ok	00:29:38
1/0	C9400-LC-24XS	ok	00:25:34

```

2      C9400-SUP-1XL  ok          00:29:38
2/0    C9400-SUP-1XL  ok          00:25:31
R0     ok, active     00:29:38
P1     C9400-PWR-3200AC ok          00:29:16
P9     C9404-FAN      ok          00:29:14

```

```

Slot    CPLD Version    Firmware Version
-----

```

```

1      20062105      17.10.1r
2      20062105      17.10.1r

```

Switch 2

```

Slot    Type          State          Insert time (ago)
-----

```

```

1      C9400-LC-24XS    ok            00:29:35
1/0    C9400-LC-24XS    ok            00:00:11
2      C9400-SUP-1XL    ok            00:29:35
2/0    C9400-SUP-1XL    ok            00:00:10
R0     ok, standby      00:29:35
P1     C9400-PWR-3200AC ok            00:29:13
P9     C9404-FAN        ok            00:29:11

```

```

Slot    CPLD Version    Firmware Version
-----

```

```

1 20062105 17.10.1r >>>>>>>>>>CPLD got upgraded

```

```

2 20062105 17.10.1r >>>>>>>>>>CPLD got upgraded

```

steps: اذه قحالت يغبنني تنأ، تنسح نوكي نأجاتحي طشن حاتفملا نم ةغيص جاتحي CPLD لالنا، ةلجالا يف 5 ةوطخلا

<#root>

1. Do a switchover

```
9400-1#
```

```
redundancy force-switchover
```

```

>>>>>>>>>>Initially Switch 1 is active and Switch 2 is standby.
After switchover it is vise-versa

```

2. Upgrade the CPLD version of previous active [now standby] with the command:

```
9400-1#
```

```
upgrade hw-programmable cpld filename bootflash: switch 1 R0
```

3. Reload the the standby

9400-1#

redundancy reload peer

ةمچرتل هذه لوج

ةللأل تاي نقتل نمة ومة مادختساب دن تسمل اذة Cisco تمةرت
ملاعلاء انء مء مء نمة دختسمل معد و تمة مء دقتل ةر شبل او
امك ةق قء نوك ت نل ةللأل ةمچرت لصف أن ةظحال مء ءرء. ةصاأل مء تءل ب
Cisco ةللخت. فرتمة مچرت مء دقء ةللأل ةل فارتحال ةمچرتل عم لاعل او
ىل إأمءءاد ءوچرلاب ةصوء و تامةرتل هذه ةقء نء اهءل وئس م Cisco
Systems (رفوتم طبارل) ةل صأل ةل ءل ءن إل دن تسمل