Hardware Job Descriptions

- 1. Hardware Engineer I, Hardware Engineer II
- 2. Hardware Engineer I Intern, Hardware Engineer II Intern
- 3. Hardware Engineer I CO-OP, Hardware Engineer II CO-OP

Hardware 1 (grade 4), Hardware 2 (grade 6)

Engineering Possibilities!

We value your innovative ideas and your energy. Cisco provides an environment that allows your creativity to become a reality. Explore the possibilities as you begin your career with us!

SKILLS REQUIRED

The ideal candidate should possess the following attributes:

- Self-motivated and highly ambitious
- Creative problem solving skills and excellent troubleshooting/debugging skills
- Strong written and verbal communication
- Excellent teamwork and people skills
- Ability to manage multiple tasks and work towards long-term goals
- Flexibility and adaptability

Depending on the role, one or more of the following may be required:

- ASIC, FPGA, PCB Design, Analog/RF design, Digital Design, VHDL/Verilog, Spice, Linux/Unix, C++, Java.

EDUCATIONAL BACKGROUND

- Currently enrolled in (or graduated within past 12 months from) a Bachelor's or Master's degree in Computer Science, Electrical Engineering, or related majors such as Math, Physics
- 3.0 GPA or higher is preferred

Positions are available in San Jose, CA, Research Triangle Park, NC, Atlanta, GA, Boxborough, MA, and Richardson, TX. Not all positions are available at all locations. Relocation is available.

Hardware 1 Intern (grade 4), Hardware 2 Intern (grade 6)

Engineering Possibilities!

We value your innovative ideas and your energy. Cisco provides an environment that allows your creativity to become a reality. Explore the possibilities as you begin your career with us!

Interns in the Engineering Organization will spend 10-12 weeks working on projects which add significant value to Cisco Systems, Inc. The Engineering internship program is designed for the participant to gain:

Experience: Engineering interns will be placed into a role based on their area of interest and the business need.

Exposure: Interns will work and interact with senior leadership, managers, cross-functional teams, and other Engineering interns.

The Engineering internship program serves as our primary pipeline for filling full-time positions.

SKILLS REQUIRED

The ideal candidate should possess the following attributes:

- Self-motivated and highly ambitious
- Creative problem solving skills and excellent troubleshooting/debugging skills
- Strong written and verbal communication
- Excellent teamwork and people skills
- Ability to manage multiple tasks and work towards long-term goals
- Flexibility and adaptability

Depending on the role, one or more of the following may be required:

- ASIC, FPGA, PCB Design, Analog/RF design, Digital Design, VHDL/Verilog, Spice, Linux/Unix, C++, Java.

EDUCATIONAL BACKGROUND

- Currently enrolled in (or graduated within past 12 months from) a Bachelor's or Master's degree in Computer Science, Electrical Engineering, or related majors such as Math, Physics
- 3.0 GPA or higher is preferred

Positions are available in San Jose, CA, Research Triangle Park, NC, Atlanta, GA, Boxborough, MA, and Richardson, TX. Not all positions are available at all locations. Relocation is available.

Hardware 1 Co-Op (grade 4), Hardware 2 Co-Op(grade 6)

Engineering Possibilities!

We value your innovative ideas and your energy. Cisco provides an environment that allows your creativity to become a reality. Explore the possibilities as you begin your co-op with us!

Co-Ops in the Engineering Organization will spend 4-6 months working on projects which add significant value to Cisco Systems, Inc. The Engineering Co-Op program is designed for the participant to gain:

Experience: Engineering Co-Ops will be placed into a role based on their area of interest and the business need.

Exposure: Co-Ops will work and interact with senior leadership, managers, cross-functional teams, and other Engineering interns.

The Engineering Co-Ops program serves as our primary pipeline for filling full-time positions.

SKILLS REQUIRED

The ideal candidate should possess the following attributes:

- Self-motivated and highly ambitious
- Creative problem solving skills and excellent troubleshooting/debugging skills
- Strong written and verbal communication
- Excellent teamwork and people skills
- Ability to manage multiple tasks and work towards long-term goals
- Flexibility and adaptability

Depending on the role, one or more of the following may be required:

- ASIC, FPGA, PCB Design, Analog/RF design, Digital Design, VHDL/Verilog, Spice, Linux/Unix, C++, Java.

EDUCATIONAL BACKGROUND

- Currently enrolled in (or graduated within past 12 months from) a Bachelor's or Master's degree in Computer Science, Electrical Engineering, or related majors such as Math, Physics
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