



Open Source Used In ASA 9.17.1

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1.3 lua 5.0.2

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README for release 6b of 27-Mar-1998

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DOCUMENTATION

ROADMAP

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This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
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REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
RELATED SOFTWARE Other stuff you should get.
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TO DO Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:

install.doc How to configure and install the IJG software.
usage.doc Usage instructions for cjpeg, djpeg, jpegtran,
 rdjpgcom, and wrjpgcom.
*.1 Unix-style man pages for programs (same info as usage.doc).
wizard.doc Advanced usage instructions for JPEG wizards only.
change.log Version-to-version change highlights.
Programmer and internal

documentation:

libjpeg.doc How to use the JPEG library in your own programs.
example.c Sample code for calling the JPEG library.
structure.doc Overview of the JPEG library's internal structure.
filelist.doc Road map of IJG files.
coderrules.doc Coding style rules --- please read if you contribute code.

Please read at least the files install.doc and usage.doc. Useful information can also be found in the JPEG FAQ (Frequently Asked Questions) article. See ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or more of the REFERENCES, then looking at the documentation files (in roughly the order listed) before diving into the code.

OVERVIEW

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This package contains C software to implement JPEG image compression and decompression. JPEG (pronounced "jay-peg") is a standardized compression method for full-color and gray-scale images.

JPEG is intended for compressing "real-world" scenes; line drawings, cartoons and other non-realistic images are not its strong suit. JPEG is lossy, meaning that the output image is not exactly identical to the input image. Hence you must not use JPEG if you have to have identical output bits. However, on typical photographic images, very good compression levels can be obtained with no visible change, and remarkably high compression levels are possible if you can tolerate a low-quality image. For more details, see the references, or just experiment with various compression settings.

This software implements JPEG baseline, extended-sequential, and progressive compression processes. Provision is made for supporting all variants of these processes, although some uncommon parameter settings aren't implemented yet. For legal reasons, we are not distributing code for the arithmetic-coding variants of JPEG; see LEGAL ISSUES. We have made no provision for supporting the hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files, plus two sample applications "cjpeg" and "djpeg", which use the library to perform conversion between JPEG and some other popular image file formats. The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included considerable functionality beyond the bare JPEG coding/decoding capability; for example, the color quantization modules are not strictly part of JPEG

decoding, but they are essential for output to colormapped file formats or colormapped displays. These extra functions can be compiled out of the library if not required for a particular application. We have also included "jpegtran", a utility for lossless transcoding between different JPEG processes, and "rdjpgcom" and "wrjpgcom", two simple applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and flexibility, while also making it fast enough to be useful. In particular, the software is not intended to be read as a tutorial on JPEG. (See the REFERENCES section for introductory material.) Rather, it is intended to be reliable, portable, industrial-strength code. We do not claim to have achieved that goal in every aspect of the software, but we strive for it.

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It appears that the arithmetic coding option of the JPEG spec is covered by patents owned by IBM, AT&T, and Mitsubishi. Hence arithmetic coding cannot legally be used without obtaining one or more licenses. For this reason, support for arithmetic coding has been removed from the free JPEG software. (Since arithmetic coding provides only a marginal gain over the unpatented Huffman mode, it is unlikely that very many implementations will support it.) So far as we are aware, there are no patent restrictions on the remaining

code.

The IJG distribution formerly included code to read and write GIF files. To avoid entanglement with the Unisys LZW patent, GIF reading support has been removed altogether, and the GIF writer has been simplified to produce "uncompressed GIFs". This technique does not use the LZW algorithm; the resulting GIF files are larger than usual, but are readable by all standard GIF decoders.

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REFERENCES

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highly recommend reading one or more of these references before trying to understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is Wallace, Gregory K. "The JPEG Still Picture Compression Standard", Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44. (Adjacent articles in that issue discuss MPEG motion picture compression, applications of JPEG, and related topics.) If you don't have the CACM issue handy, a PostScript file containing a revised version of Wallace's article is available at <ftp://ftp.uu.net/graphics/jpeg/wallace.ps.gz>. The file (actually a preprint for an article that appeared in IEEE Trans. Consumer Electronics) omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more

leisurely introduction to JPEG can be found in

"The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...

The best full description of JPEG is the textbook "JPEG Still Image Data Compression Standard" by William B. Pennebaker and Joan L. Mitchell, published by Van Nostrand Reinhold, 1993, ISBN 0-442-01272-1. Price US\$59.95, 638 pp. The book includes the complete text of the ISO JPEG standards (DIS 10918-1

and draft DIS 10918-2). This is by far the most complete exposition of JPEG in existence, and we highly recommend it.

The

JPEG standard itself is not available electronically; you must order a paper copy through ISO or ITU. (Unless you feel a need to own a certified official copy, we recommend buying the Pennebaker and Mitchell book instead; it's much cheaper and includes a great deal of useful explanatory material.)

In the USA, copies of the standard may be ordered from ANSI Sales at (212) 642-4900, or from Global Engineering Documents at (800) 854-7179. (ANSI doesn't take credit card orders, but Global does.) It's not cheap: as of 1992, ANSI was charging \$95 for Part 1 and \$47 for Part 2, plus 7% shipping/handling. The standard is divided into two parts, Part 1 being the actual specification, while Part 2 covers compliance testing methods. Part 1 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 1: Requirements and guidelines" and has document numbers ISO/IEC IS 10918-1, ITU-T T.81. Part 2 is titled "Digital Compression and Coding of Continuous-tone Still

Images, Part 2: Compliance testing" and has document numbers ISO/IEC IS 10918-2, ITU-T T.83.

Some extensions to the original JPEG standard are defined in JPEG Part 3, a newer ISO standard numbered ISO/IEC IS 10918-3 and ITU-T T.84. IJG currently does not support any Part 3 extensions.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, revision 1.02. A copy of the JFIF spec is available from:

Literature Department

C-Cube Microsystems, Inc.

1778 McCarthy Blvd.

Milpitas, CA 95035

phone (408) 944-6300, fax (408) 944-6314

A PostScript version of this document is available by FTP at

<ftp://ftp.uu.net/graphics/jpeg/jfif.ps.gz>. There is also a plain text

version at <ftp://ftp.uu.net/graphics/jpeg/jfif.txt.gz>, but it is missing

the figures.

The TIFF 6.0 file format specification can be obtained by FTP from

<ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz>. The JPEG incorporation

scheme

found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems.

IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6).

Instead, we recommend the JPEG design proposed by TIFF Technical Note #2

(Compression tag 7). Copies of this Note can be obtained from <ftp.sgi.com> or

from <ftp://ftp.uu.net/graphics/jpeg/>. It is expected that the next revision

of the TIFF spec will replace the 6.0 JPEG design with the Note's design.

Although IJG's own code does not support TIFF/JPEG, the free libtiff library

uses our library to implement TIFF/JPEG per the Note. libtiff is available from <ftp://ftp.sgi.com/graphics/tiff/>.

ARCHIVE LOCATIONS

=====

The "official" archive site for this software is <ftp.uu.net> (Internet address 192.48.96.9). The most recent released version can always be found there in directory `graphics/jpeg`. This particular version will be archived as <ftp://ftp.uu.net/graphics/jpeg/jpegsrc.v6b.tar.gz>. If you don't have direct Internet access, UUNET's archives are also available via UUCP; contact help@uunet.uu.net for information on retrieving files that way.

Numerous Internet sites maintain copies of the UUNET files. However, only <ftp.uu.net> is guaranteed to have the latest official version.

You can also obtain this software in DOS-compatible "zip" archive format from the SimTel archives (<ftp://ftp.simtel.net/pub/simtelnet/msdos/graphics/>), or on CompuServe in the Graphics Support forum (GO CIS:GRAPHSUP), library 12 "JPEG Tools". Again, these versions may sometimes lag behind the <ftp.uu.net> release.

The JPEG FAQ (Frequently Asked Questions) article is a useful source of general information about JPEG. It is updated constantly and therefore is not included in this distribution. The FAQ is posted every two weeks to Usenet newsgroups `comp.graphics.misc`, `news.answers`, and other groups. It is available on the World Wide Web at <http://www.faqs.org/faqs/jpeg-faq/> and other `news.answers` archive sites, including the official `news.answers` archive at [rtfm.mit.edu: ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/](ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/). If you don't have Web or FTP access, send e-mail to mail-server@rtfm.mit.edu with body
send `usenet/news.answers/jpeg-faq/part1`
send `usenet/news.answers/jpeg-faq/part2`

RELATED SOFTWARE

=====

Numerous viewing and image manipulation programs now support JPEG. (Quite a few of them use this library to do so.) The JPEG FAQ described above lists some of the more popular free and shareware viewers, and tells where to obtain them on Internet.

If you are on a Unix machine, we highly recommend Jef Poskanzer's free PBPLUS software, which provides many useful operations on PPM-format image

files. In particular, it can convert PPM images to and from a wide range of other formats, thus making cjpeg/djpeg considerably more useful. The latest version is distributed by the NetPBM group, and is available from numerous sites,

notably <ftp://wuarchive.wustl.edu/graphics/graphics/packages/NetPBM/>.

Unfortunately PBPLUS/NETPBM is not nearly as portable as the IJG software is; you are likely to have difficulty making it work on any non-Unix machine.

A different free JPEG implementation, written by the PVRG group at Stanford, is available from <ftp://havefun.stanford.edu/pub/jpeg/>. This program is designed for research and experimentation rather than production use; it is slower, harder to use, and less portable than the IJG code, but it is easier to read and modify. Also, the PVRG code supports lossless JPEG, which we do not. (On the other hand, it doesn't do progressive JPEG.)

FILE FORMAT WARS

=====

Some JPEG programs produce files that are not compatible with our library. The root of the problem is that the ISO JPEG committee failed to specify a concrete file format. Some vendors "filled in the blanks" on their own, creating proprietary formats that no one else could read.

(For example, none of the early commercial JPEG implementations for the Macintosh were able to exchange compressed files.)

The file format we have adopted is called JFIF (see REFERENCES). This format has been agreed to by a number of major commercial JPEG vendors, and it has become the de facto standard. JFIF is a minimal or "low end" representation. We recommend the use of TIFF/JPEG (TIFF revision 6.0 as modified by TIFF Technical Note #2) for "high end" applications that need to record a lot of additional data about an image. TIFF/JPEG is fairly new and not yet widely supported, unfortunately.

The upcoming JPEG Part 3 standard defines a file format called SPIFF. SPIFF is interoperable with JFIF, in the sense that most JFIF decoders should be able to read the most common variant of SPIFF. SPIFF has some technical advantages over JFIF, but its major claim to fame is simply that it is an official standard rather than an informal one. At this point it is unclear whether

SPIFF will supersede JFIF or whether JFIF will remain the de-facto standard. IJG intends to support SPIFF once the standard is frozen, but we have not decided whether it should become our default output format or not. (In any case, our decoder will remain capable of reading JFIF indefinitely.)

Various proprietary file formats incorporating JPEG compression also exist. We have little or no sympathy for the existence of these formats. Indeed,

one of the original reasons for developing this free software was to help force convergence on common, open format standards for JPEG files. Don't use a proprietary file format!

TO DO

=====

The major thrust for v7 will probably be improvement of visual quality. The current method for scaling the quantization tables is known not to be very good at low Q values. We also intend to investigate block boundary smoothing, "poor man's variable quantization", and other means of improving quality-vs-file-size performance without sacrificing compatibility.

In future versions, we are considering supporting some of the upcoming JPEG Part 3 extensions --- principally, variable quantization and the SPIFF file format.

As always, speeding things up is of great interest.

Please send bug reports, offers of help, etc. to jpeg-info@uunet.uu.net.

1.5 jquery 1.7.2

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1.6 zlib 1.2.7

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/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.7, May 2nd, 2012

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The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://tools.ietf.org/html/rfc1950> (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

*/

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1.7 csrp 1.0

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1.8 xerces-j 2.8.0

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1.11 xinetd 2.3.15

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libbz2.dll test program.
```

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```

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1.13 zlib 1.2.5

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/* zlib.h -- interface of the 'zlib' general purpose compression library  
version 1.2.5, April 19th, 2010
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1.14 zlib 1.2.8

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/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.11, January 15th, 2017

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1.15 unzip 6.0

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Additionally, it summarises some exceptions to the general BSD-like copyright found in LICENSE that covers our generic code and most of the system specific ports.

Please read LICENSE first to find out what is allowed to do with Info-ZIP's UnZip code.

There are currently two explicit copyrights on portions of UnZip code (at least, of which Info-ZIP is aware):

Jim Luther's Mac OS File Manager interface code; and Christopher Evans' MacBinaryIII coding code (for the MacOS port).. These copyrights are discussed in more detail below.

All remaining code is now (starting with UnZip version 5.41) covered by the new Info-ZIP license. For details, please read the accompanying file LICENSE. The terms and conditions in this license supersede the copyright conditions of the contributions by Igor Mandrichenko (vms/vms.c), Greg Roelofs (zipinfo.c, new version of unshrink.c), Mike White (Windows DLL code in "windll/*"), Steve P. Miller (Pocket UnZip GUI "wince/*"), and Mark Adler (inflate/explode decompression core routines, previously put into the public domain). All these Info-ZIP contributors (or "primary" authors) have permitted us to replace their copyright notes by the Info-ZIP License.

Frequently Asked Questions regarding (re)distribution of Zip and UnZip are near the end of this file.

There are no known patents on any of the code in UnZip. Unisys claims a patent on LZW encoding and on LZW decoding in an apparatus that performs LZW encoding, but the patent appears to exempt a stand-

alone decoder (as in UnZip's unshrink.c). Unisys has publicly claimed otherwise, but the issue has never been tested in court. Since this point is unclear, unshrinking is not enabled by default. It is the responsibility of the user to make his or her peace with Unisys and its licensing requirements. (unshrink.c may be removed from future releases altogether.)

The original unzip source code has been extensively modified and almost entirely rewritten (changes include random zipfile access rather than sequential; replacement of unimplode() with explode(); replacement of old unshrink() with new (unrelated) unshrink(); replacement of output routines; addition of inflate(), wildcards, filename-mapping, text translation, ...; etc.). As far as we can tell, only the core code of the unreduce method remained substantially similar to Mr. Smith's original source. As of UnZip 5.42, the complete

core code is now covered by the Info-ZIP Licence. Therefore, support for the reduce method has been removed.

The drop of the reduce method should only affect some test archives, reducing was never used in any publically distributed Zip program. For pathologic cases where support for reduced archive entries is needed, the unreduce code copyrighted by Samuel H. Smith is available as a separate distribution (the restricted copyright of this code is cited below in the "historical" section).

The following copyright applies to the Mac OS File Manager interface code (macos/source/macstuff.[ch]), distributed with UnZip 5.4 and later:

```
* MoreFiles
*
* A collection of File Manager and related routines
*
* by Jim Luther (Apple Macintosh Developer Technical Support Emeritus)
* with significant code contributions by Nitin Ganatra
* (Apple Macintosh Developer Technical Support Emeritus)
* Copyright 1992-1998
```

Apple Computer, Inc.

```
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* All rights reserved.
* The Package "More Files" is distributed under the following
* license terms:
*
* "You may incorporate this sample code into your
* applications without restriction, though the
* sample code has been provided "AS IS" and the
* responsibility for its operation is 100% yours.
* However, what you are not permitted to do is to
```

- * redistribute the source as "DSC Sample Code" after
- * having made changes. If you're going to
- * redistribute the source, we require that you make
- * it clear in the source that the code was descended
- * from Apple Sample Code, but that you've made
- * changes."

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The following copyright applies to the Mac OS "macbin3" decoding code (extra field compatibility with ZipIt):

- * MacBinaryIII.h
- *
- * Copyright 1997 Christopher Evans (cevens@poppybank.com)
- *
- * Basic encoding and decoding of Macintosh files to the
- * MacBinary III spec.
- * -----
- * This source is copyrighted by Christopher Evans (cevens@poppybank.com)
- * (available at ftp://ftp.lazerware.com/MacBinaryIII_src_C.sit
- * homepage of Leonard Rosenthol leonardr@netcom.com)

This copyright note does not contain any usage terms. So, we assume that this code is freely reusable until we are proved wrong...

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The following copyright applies to the full-featured unreduce.c (now distributed separately):

- * Copyright 1989 Samuel H. Smith; All rights reserved
- *
- * Do not distribute modified versions without my permission.
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- * Do not use any of this in a commercial product.

Regarding the first stipulation, Mr. Smith was tracked down in southern California some years back [Samuel H. Smith, The Tool Shop; as of mid-

May 1994, (213) 851-9969 (voice), (213) 887-2127(?) (subscription BBS),
71150.2731@compuserve.com]:

"He says that he thought that whoever contacted him understood that
he has no objection to the Info-ZIP group's inclusion of his code.
His primary concern is that it remain freely distributable, he said."

Despite the fact that our "normal" code has been entirely rewritten
and by default
no longer contains any of Mr. Smith's code, Info-ZIP
remains indebted and grateful to him. We hope he finds our contribu-
tions as useful as we have his.

Note that the third and fourth stipulations still apply to any com-
pany that wishes to incorporate the unreduce code into its products;
if you wish to do so, you must contact Mr. Smith directly regarding
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The following copyright applied to most of the VMS code in vms.c,
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- * any purpose whatsoever, subject to the conditions noted in the
- * Frequently Asked Questions section below, plus one additional
- * condition: namely, that my name not be removed from the source
- * code. (Other names may, of course, be added as modifications
- * are made.) Corporate legal staff (like at IBM :-)) who have
- * problems understanding this can contact me through Zip-Bugs...

The following copyright applied to the Windows DLL code (windll/*),
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- * copy, or redistribute this software so long as all of the original
- * files are included, that it is not sold for profit, and that this
- * copyright notice is retained.

The following copyright applied to the Windows
CE GUI port, ``Pocket
UnZip," distributed with UnZip version 5.3 and later:

- * All the source files for Pocket UnZip, except for components
- * written by the Info-ZIP group, are copyrighted 1997 by Steve P.
- * Miller. The product "Pocket UnZip" itself is property of the
- * author and cannot be altered in any way without written consent
- * from Steve P. Miller.

The remaining code was written by many people associated with the
Info-ZIP group, with large contributions from (but not limited to):
Greg Roelofs (overall program logic, ZipInfo, unshrink, filename
mapping/portability, etc.), Mark Adler (inflate, explode, funzip),
Kai Uwe Rommel (OS/2), John Bush and Paul Kienitz (Amiga), Antoine
Verheijen (Macintosh), Hunter Goatley (more VMS), Mike White (Windows
DLLs), Christian Spieler (overall logic, optimization, VMS, etc.) and
others. See the file CONTRIBS in the source distribution for a much
more complete list of contributors.

The decompression core code for the deflate method (inflate.[ch],
explode.c) was originally written by Mark Adler who submitted it
as public domain code.

1.16 zlib 1.2.3

1.16.1 Available under license :

```
/* zlib.h -- interface of the 'zlib' general purpose compression library  
version 1.1.4, March 11th, 2002
```

Copyright (C) 1995-2002 Jean-loup Gailly and Mark Adler

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files ftp://ds.internic.net/rfc/rfc1950.txt (zlib format), rfc1951.txt (deflate format) and rfc1952.txt (gzip format).

```
*/  
/* match.s -- Pentium-optimized version of longest_match()  
* Written for zlib 1.1.2  
* Copyright (C) 1998 Brian Raiter <breadbox@muppetlabs.com>  
*  
* This is free software; you can redistribute it and/or modify it  
* under the terms of the GNU General Public License.  
*/
```

1.17 jboss-logging 3.1.1.GA

1.17.1 Available under license :

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Version 2.1, February 1999

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<signature of Ty Coon>, 1 April 1990
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That's all there is to it!

1.18 net-snmp 5.7.3

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```
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1.21 pam 1.3.0

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1.22 libpcap 1.8.1

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1.23 curl 7.54.1

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1.24 diffutils 3.6

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```
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1.26 libxslt 1.1.32

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1.27 apr-util 1.6.1

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```
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1.28 libmnl 1.0.4

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1.29 sqlite 3.23.1

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1.31 gawk 4.2.1

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the

object code for the work under the terms of Section 6.

Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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a) Accompany

the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

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It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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The

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1.36 esapi 2.1.0.1

1.36.1 Available under license :

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Found in path(s):

* /opt/cola/permits/185936931_1651084248.41/0/esapi-2-1-0-1-jar/META-INF/maven/org.owasp.esapi/esapi/pom.xml

1.37 rsync 3.1.3

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1.38 apache-http-server 2.4.34

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```
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<html xmlns="http://www.w3.org/1999/xhtml" lang="en" xml:lang="en"><head>
<meta content="text/html; charset=ISO-8859-1" http-equiv="Content-Type" />
<!--
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
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<title>The Apache License, Version 2.0 - Apache HTTP Server Version 2.4</title>
<link href="/style/css/manual.css" rel="stylesheet" media="all" type="text/css" title="Main stylesheet" />
<link href="/style/css/manual-loose-100pc.css" rel="alternate stylesheet" media="all" type="text/css" title="No
Sidebar - Default font size" />
<link href="/style/css/manual-print.css" rel="stylesheet" media="print" type="text/css" /><link rel="stylesheet"
type="text/css" href="/style/css/prettify.css"
/>
<script src="/style/scripts/prettify.min.js" type="text/javascript">
</script>
```

```
<link href="/images/favicon.ico" rel="shortcut icon" /></head>
<body id="manual-page" class="no-sidebar"><div id="page-header">
<p class="menu"><a href="/mod/">Modules</a> | <a href="/mod/directives.html">Directives</a> | <a
href="http://wiki.apache.org/httpd/FAQ">FAQ</a> | <a href="/glossary.html">Glossary</a> | <a
href="/sitemap.html">Sitemap</a></p>
<p class="apache">Apache HTTP Server Version 2.4</p>
</div>
<div class="up"><a href="/"></a></div>
<div id="path">
<a href="http://www.apache.org">Apache</a> > <a href="http://httpd.apache.org">HTTP Server</a> > <a
href="http://httpd.apache.org/docs/">Documentation</a> > <a href="/">Version 2.4</a></div><div id="page-
content"><div id="preamble"><h1>The Apache License, Version 2.0</h1>
<div class="toplang">
<p><span>Available Languages:
</span><a href="/en/license.html" title="English">&nbsp;en&nbsp;</a></p>
</div>
```

```
<p class="centered">Apache License<br />
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</div>

</div>

<div class="bottomlang">

<p>Available Languages: en </p>

</div><div class="top"></div><div

class="section"><h2>Comments</h2><div

class="warning">Notice:
This is not a Q&A section. Comments placed here should be pointed towards suggestions on improving the documentation or server, and may be removed again by our moderators if they are either implemented or considered invalid/off-topic. Questions on how to manage the Apache HTTP Server should be directed at either our IRC channel, #httpd, on Freenode, or sent to our mailing lists.</div>

<script type="text/javascript"><!--><![CDATA[</script><!--

var comments_shortcode = 'httpd';

var comments_identifier = 'http://httpd.apache.org/docs/2.4/license.html';

(function(w, d) {

if (w.location.hostname.toLowerCase() == "httpd.apache.org") {

d.write('<div id="comments_thread"></div>');

var s = d.createElement('script');

s.type = 'text/javascript';

s.async = true;

s.src = 'https://comments.apache.org/show_comments.lua?site=' + comments_shortcode + '&page=' +

comments_identifier;

(d.getElementsByTagName('head')[0] || d.getElementsByTagName('body')[0]).appendChild(s);

}

else {

d.write('<div id="comments_thread">Comments are disabled for this page at the moment.</div>');

}

})(window, document);

</script></div><div id="footer">

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<p class="menu">Modules | Directives | FAQ | Glossary | Sitemap</p></div><script type="text/javascript"><!--><![CDATA[</script><!--

if (typeof(prettyPrint) !== 'undefined') {

prettyPrint();

}

</script>

</body></html>

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Content-Language: en

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*/

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```

/*****
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* University of Illinois at Urbana-Champaign
* 605 E. Springfield, Champaign, IL 61820
* httpd@ncsa.uiuc.edu
*
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*
*****/
*
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*
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/* ZeusBench V1.01

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Written by Adam Twiss (adam@zeus.co.uk). March 1996

Thanks to the following people for their input:

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Michael Campanella (campanella@stevms.enet.dec.com)

*/

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1.39 zlib 1.2.11

1.39.1 Available under license :

```
/* zlib.h -- interface of the 'zlib' general purpose compression library  
version 1.2.11, January 15th, 2017
```

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for
Comments) 1950 to 1952 in the files <http://tools.ietf.org/html/rfc1950>
(zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

```
*/
```

1.40 glibc 2.28

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@subheading END OF TERMS AND CONDITIONS

@page

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```
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```
@end
smallexample
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```
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```

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@var{signature of Ty Coon}, 1 April 1990
Ty Coon, President of Vice
@end smallexample
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```

```
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That's all there is to it!

```
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```

```
/* setjmp is implemented in setjmp.S */
```

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1.43 gzip 1.9

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The

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1.44 python 2.7.15

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History of the software

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <https://www.cwi.nl/>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <https://www.cnri.reston.va.us/>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation; see <http://www.zope.com/>). In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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2.0.1	2.0+1.6.1	2001	PSF	yes	
2.1.1	2.1+2.0.1	2001	PSF	yes	
2.1.2	2.1.1	2002	PSF	yes	
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Mersenne Twister

The `:mod:`_random`` module includes code based on a download from <http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/MT2002/emt19937ar.html>. The following are the verbatim comments from the original code::

A C-program
for MT19937, with initialization improved 2002/1/26.
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`
or `init_by_array(init_key, key_length)`.

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Sockets

The `:mod:`socket`` module uses the functions, `:func:`getaddrinfo``, and `:func:`getnameinfo``, which are coded in separate source files from the WIDE Project, <http://www.wide.ad.jp/>. ::

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MD5 message digest algorithm

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L. Peter Deutsch
ghost@aladdin.com

Independent implementation of MD5 (RFC 1321).

This code implements the MD5 Algorithm defined in RFC 1321, whose text is available at

<http://www.ietf.org/rfc/rfc1321.txt>

The code is derived from the text of the RFC, including the test suite (section A.5) but excluding the rest of

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The original and principal author of md5.h is L. Peter Deutsch <ghost@aladdin.com>. Other authors are noted in the change history that follows (in reverse chronological order):

- 2002-04-13 lpd Removed support for non-ANSI compilers; removed references to Ghostscript; clarified derivation from RFC 1321; now handles byte order either statically or dynamically.
- 1999-11-04 lpd Edited comments slightly for automatic TOC extraction.
- 1999-10-18 lpd Fixed typo in header comment (ansi2knr rather than md5); added conditionalization for C++ compilation from Martin Purschke <purschke@bnl.gov>.
- 1999-05-03 lpd Original version.

Asynchronous socket services

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Modified by Jack Jansen, CWI, July 1995:

- Use binascii module to do the actual line-by-line conversion between ascii and binary. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with Python standard

XML Remote Procedure Calls

The `:mod:`xmlrpc.lib`` module contains the following notice::

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Select kqueue

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strtod
and dtoa

The file :file:`Python/dtoa.c`, which supplies C functions dtoa and strtod for conversion of C doubles to and from strings, is derived from the file of the same name by David M. Gay, currently available from <http://www.netlib.org/fp/>. The original file, as retrieved on March 16, 2009, contains the following copyright and licensing notice::

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principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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2.1.1	2.1+2.0.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
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2.1.1	2.1+2.0.1	2001	PSF	yes

2.1.2	2.1.1	2002	PSF	yes	
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Mersenne Twister

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A C-program
for MT19937, with initialization improved 2002/1/26.
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`

or `init_by_array(init_key, key_length)`.

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<http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/emt.html>

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Sockets

The `:mod:`socket`` module uses the
functions, `:func:`getaddrinfo``, and
`:func:`getnameinfo``, which are coded in separate source files from the WIDE
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MD5 message digest algorithm

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L. Peter Deutsch
ghost@aladdin.com

Independent implementation of MD5 (RFC 1321).

This code implements the MD5 Algorithm defined in RFC 1321, whose text is available at

<http://www.ietf.org/rfc/rfc1321.txt>

The code is derived from the text of the RFC, including the test suite (section A.5) but excluding the rest of

Appendix A. It does not include

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The original and principal author of md5.h is L. Peter Deutsch <ghost@aladdin.com>. Other authors are noted in the change history that follows (in reverse chronological order):

2002-04-13 lpd Removed support for non-ANSI compilers; removed references to Ghostscript; clarified derivation from RFC 1321; now handles byte order either statically or dynamically.

1999-11-04 lpd Edited comments slightly for automatic TOC extraction.

1999-10-18 lpd Fixed typo in header comment (ansi2knr rather than md5); added conditionalization for C++ compilation from Martin

Purschke <purschke@bnl.gov>.

1999-05-03 lpd Original version.

Asynchronous socket services

The :mod:`asynchat` and :mod:`asyncore` modules contain the following notice::

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Cookie
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UUencode and UUdecode functions

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Modified by Jack Jansen, CWI, July 1995:

- Use binascii module to do the actual line-by-line conversion between ascii and binary. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with Python standard

XML Remote Procedure Calls

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strtod
and dtoa

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zlib

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1.45 zlib 1.2.3

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```
/* zlib.h -- interface of the 'zlib' general purpose compression library
   version 1.2.11, January 15th, 2017
```

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1.46 net-snmp 5.8

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.gitignore

#

builds/unix/pkg.m4

#

docs/FTL.TXT

docs/GPLv2.TXT

#

include/freetype/internal/fthash.h

```
#
src/base/fthash.c
src/base/md5.c
src/base/md5.h
#
src/bdf/bdf.c
src/bdf/bdf.h
src/bdf/bdfdrivr.c
src/bdf/bdfdrivr.h
src/bdf/bdferror.h
src/bdf/bdflib.c
src/bdf/module.mk
src/bdf/README
src/bdf/rules.mk
#
src/pcf/module.mk
src/pcf/pcf.c
src/pcf/pcf.h
src/pcf/pcfdrivr.c
src/pcf/pcfdrivr.h
src/pcf/pcferror.h
src/pcf/pcfread.c
src/pcf/pcfread.h
src/pcf/pcfutil.c
src/pcf/pcfutil.h
src/pcf/README
src/pcf/rules.mk
#
src/zip/adler32.c
src/zip/infblock.c
src/zip/infblock.h
src/zip/infcodes.c
src/zip/infcodes.h
src/zip/inffixed.h
src/zip/inflate.c
src/zip/inftrees.c
src/zip/inftrees.h
src/zip/infutil.c
src/zip/infutil.h
src/zip/zconf.h
src/zip/zlib.h
src/zip/zutil.c
src/zip/zutil.h
#
src/tools/apinames.c
src/tools/ftrandom/ftrandom.c
#
#
```


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- * `configure.ac`, `Makefile.am`: The original versions were derived from the

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* atf-c/ui.c: The format_paragraph and format_text functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5cff2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

* atf-c++/detail/io.hpp, atf-c++/detail/io.cpp, atf-c++/detail/io_test.cpp: These files were derived from the file_handle, systembuf, pipe and pistream classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

* admin/check-style.sh, admin/check-style-common.awk, admin/check-style-cpp.awk, admin/check-style-shell.awk: These files, except the first one, were first implemented in the Buildtool project. They were later adapted to be part of Boost.Process and, during that process, the shell script was created.

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-->

<!-- Generate ISC copyright comments from Docbook copyright metadata. -->

```
<xsl:stylesheet version="1.0"
```

```
  xmlns:xsl="http://www.w3.org/1999/XSL/Transform"
```

```
  xmlns:xi="http://www.w3.org/2001/XInclude"
```

```
  xmlns:db="http://docbook.org/ns/docbook">
```

```
<xsl:template name="isc.copyright.format">
```

```
  <xsl:param name="text"/>
```

```
  <xsl:value-of select="$isc.copyright.leader"/>
```

```
  <xsl:value-of select="normalize-space(substring-before($text, '&#10;'))"/>
```

```
  <xsl:text>&#10;</xsl:text>
```

```
  <xsl:variable name="rest" select="substring-after($text, '&#10;')"/>
```

```
  <xsl:if test="translate($rest, '&#9;&#32;', '')">
```



```

<xsl:call-template name="isc.copyright.format">
  <xsl:with-param name="text" select="$rest"/>
</xsl:call-template>
</xsl:if>
</xsl:template>

<xsl:variable name="isc.copyright.text">
  <xsl:text>
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    file, You can obtain one at http://mozilla.org/MPL/2.0/.
  </xsl:text>
</xsl:variable>

<xsl:variable name="isc.copyright">
  <xsl:call-template name="isc.copyright.format">
    <xsl:with-param name="text">
      <xsl:for-each select="book/info/copyright | refentry/docinfo/copyright">
        <xsl:text>Copyright (C) </xsl:text>
        <xsl:call-template name="copyright.years">
          <xsl:with-param name="years" select="year"/>
        </xsl:call-template>
        <xsl:text> </xsl:text>
        <xsl:value-of select="holder"/>
        <xsl:value-of select="$isc.copyright.breakline"/>
        <xsl:text>&#10;</xsl:text>
      </xsl:for-each>
      <xsl:value-of
        select="$isc.copyright.text"/>
    </xsl:with-param>
  </xsl:call-template>
</xsl:variable>
</xsl:stylesheet>

```

<!--

- Local variables:

- mode: sgml

- End:

-->

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1.49 cpio 2.12

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Version 3, 29 June 2007

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1.53 libevent 2.1.8

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1.54 parted 3.2

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Java ClassMate library was originally written by Tatu Saloranta (tatu.saloranta@iki.fi)

Other developers who have contributed code are:

* Brian Langel

1.56 dmidencode 3.3

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1.57 kerberos 1.16

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cmd/krb5/kproplog/kproplog.c
cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
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lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
lib/libgss/g_canon_name.c
lib/libgss/g_compare_name.c
lib/libgss/g_context_time.c
lib/libgss/g_delete_sec_context.c
lib/libgss/g_dsp_name.c
lib/libgss/g_dsp_status.c
lib/libgss/g_dup_name.c
lib/libgss/g_exp_sec_context.c
```


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lib/libgss/g_glue.c
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lib/libgss/g_imp_sec_context.c
lib/libgss/g_init_sec_context.c
lib/libgss/g_initialize.c
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lib/libgss/gssd_pname_to_uid.c
uts/common/gssapi/include/gssapi_err_generic.h
uts/common/gssapi/include/mechglueP.h

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  <link rel="search" title="Search" href="search.html" />
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      <h1><a href="index.html">MIT Kerberos Documentation</a></h1>

      <div class="rel">

        <a href="index.html" title="Full Table of Contents"
          accesskey="C">Contents</a> |
        <a href="mitK5features.html" title="MIT Kerberos features"
          accesskey="P">previous</a> |
        <a href="copyright.html" title="Copyright"
          accesskey="N">next</a> |
```

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    accesskey="I">index</a> |
    <a href="search.html" title="Enter search criteria"
    accesskey="S">Search</a> |
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information">feedback</a>
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    </div>
</div>

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    <div class="document">

    <div class="documentwrapper">
    <div class="bodywrapper">
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<p>The implementations of

GSSAPI mechglue in GSSAPI-SPNEGO in

<code class="docutils literal">src/lib/gssapi</code>, including the following files:</p>

<div class="highlight-default"><div class="highlight"><pre>lib<span

class="o">gssapi<span

class="n">genericgssapi_err_generic<span

class="o">et</pre>

libgssapi<span

class="o">mechglue<span

class="n">g_accept_sec_contextc

libgssapi<span

class="o">mechglue<span

class="n">g_acquire_credc

lib<span

class="n">gssapimechglue<span

class="o">g_canon_namec

libgssapi<span

class="o">mechglue<span

class="n">g_compare_namec


```
class="o"/></span><span class="n">spnego</span><span class="o"/></span><span
class="n">gssapiP_spnego</span><span class="o"/></span><span class="n">h</span>
<span class="n">lib</span><span class="o"/></span><span
class="n">gssapi</span><span class="o"/></span><span class="n">spnego</span><span
class="o"/></span><span class="n">spnego_mech</span><span class="o"/></span><span class="n">c</span>
</pre></div>
```

</div>

<p>and the initial implementation of incremental propagation, including the following new or changed files:</p>

```
<div class="highlight-default"><div class="highlight"><pre><span></span><span class="n">include</span><span
class="o"/></span><span class="n">iprop_hdr</span><span class="o"/></span><span class="n">h</span>
<span class="n">kadmin</span><span class="o"/></span><span class="n">server</span><span
class="o"/></span><span class="n">ipropd_svc</span><span class="o"/></span><span class="n">c</span>
<span class="n">lib</span><span class="o"/></span><span class="n">kdb</span><span class="o"/></span><span
class="n">iprop</span><span class="o"/></span><span class="n">x</span>
<span class="n">lib</span><span class="o"/></span><span class="n">kdb</span><span class="o"/></span><span
class="n">kdb_convert</span><span
class="o"/></span><span class="n">c</span>
<span class="n">lib</span><span class="o"/></span><span class="n">kdb</span><span class="o"/></span><span
class="n">kdb_log</span><span class="o"/></span><span class="n">c</span>
<span class="n">lib</span><span class="o"/></span><span class="n">kdb</span><span class="o"/></span><span
class="n">kdb_log</span><span class="o"/></span><span class="n">h</span>
<span class="n">lib</span><span class="o"/></span><span class="n">krb5</span><span
class="o"/></span><span class="n">error_tables</span><span class="o"/></span><span
class="n">kdb5_err</span><span class="o"/></span><span class="n">et</span>
<span class="n">kprop</span><span class="o"/></span><span class="n">kpropd_rpc</span><span
class="o"/></span><span class="n">c</span>
<span class="n">kprop</span><span class="o"/></span><span class="n">kproplog</span><span
class="o"/></span><span class="n">c</span>
</pre></div>
```

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in ``src/lib/gssapi``, including the following files:

.. parsed-literal::

- lib/gssapi/generic/gssapi\_err\_generic.et
- lib/gssapi/mechglue/g\_accept\_sec\_context.c
- lib/gssapi/mechglue/g\_acquire\_cred.c
- lib/gssapi/mechglue/g\_canon\_name.c
- lib/gssapi/mechglue/g\_compare\_name.c
- lib/gssapi/mechglue/g\_context\_time.c
- lib/gssapi/mechglue/g\_delete\_sec\_context.c
- lib/gssapi/mechglue/g\_dsp\_name.c
- lib/gssapi/mechglue/g\_dsp\_status.c
- lib/gssapi/mechglue/g\_dup\_name.c
- lib/gssapi/mechglue/g\_exp\_sec\_context.c
- lib/gssapi/mechglue/g\_export\_name.c
- lib/gssapi/mechglue/g\_glue.c
- lib/gssapi/mechglue/g\_imp\_name.c
- lib/gssapi/mechglue/g\_imp\_sec\_context.c
- lib/gssapi/mechglue/g\_init\_sec\_context.c
- lib/gssapi/mechglue/g\_initialize.c
- lib/gssapi/mechglue/g\_inquire\_context.c
- lib/gssapi/mechglue/g\_inquire\_cred.c
- lib/gssapi/mechglue/g\_inquire\_names.c
- lib/gssapi/mechglue/g\_process\_context.c
- lib/gssapi/mechglue/g\_rel\_buffer.c
- lib/gssapi/mechglue/g\_rel\_cred.c
- lib/gssapi/mechglue/g\_rel\_name.c
- lib/gssapi/mechglue/g\_rel\_oid\_set.c
- lib/gssapi/mechglue/g\_seal.c
- lib/gssapi/mechglue/g\_sign.c
- lib/gssapi/mechglue/g\_store\_cred.c
- lib/gssapi/mechglue/g\_unseal.c
- lib/gssapi/mechglue/g\_userok.c
- lib/gssapi/mechglue/g\_utils.c
- lib/gssapi/mechglue/g\_verify.c

lib/gssapi/mechglue/gssd\_pname\_to\_uid.c  
lib/gssapi/mechglue/mglueP.h  
lib/gssapi/mechglue/oid\_ops.c  
lib/gssapi/spnego/gssapiP\_spnego.h  
lib/gssapi/spnego/spnego\_mech.c

and the initial implementation of incremental propagation, including the following new or changed files:

.. parsed-literal::

include/ipropr\_hdr.h  
kadmin/server/ipropr\_svc.c  
lib/kdb/ipropr.x  
lib/kdb/kdb\_convert.c  
lib/kdb/kdb\_log.c  
lib/kdb/kdb\_log.h  
lib/krb5/error\_tables/kdb5\_err.et  
kprop/kpropd\_rpc.c  
kprop/kproplog.c

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```
/* -*- mode: c; c-basic-offset: 4; indent-tabs-mode: nil -*- */
/*
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Transitional//EN"
"http://www.w3.org/TR/xhtml1/DTD/xhtml1-transitional.dtd">
```

```
<html xmlns="http://www.w3.org/1999/xhtml">
```

```
<head>
```

```
<meta http-equiv="Content-Type" content="text/html; charset=utf-8" />
```

```
<title>Copyright — MIT Kerberos Documentation</title>
```

```
<link rel="stylesheet" href="_static/agogo.css" type="text/css" />
```

```
<link rel="stylesheet" href="_static/pygments.css" type="text/css" />
```

```
<link rel="stylesheet" href="_static/kerb.css" type="text/css" />
```

```
<script type="text/javascript">
```

```
var DOCUMENTATION_OPTIONS = {
```

```
 URL_ROOT: '.',
```

```
 VERSION: '1.17',
```

```
 COLLAPSE_INDEX: false,
```

```
 FILE_SUFFIX: '.html',
```

```
 HAS_SOURCE: true,
```

```
 SOURCELINK_SUFFIX: '.txt'
```

```
};
```

```
</script>
```

```
<script type="text/javascript" src="_static/jquery.js"></script>
```

```
<script type="text/javascript" src="_static/underscore.js"></script>
```

```
<script type="text/javascript" src="_static/doctools.js"></script>
```

```
<link rel="author" title="About these documents" href="about.html" />
```

```
<link rel="index" title="Index" href="genindex.html" />
```

```
<link rel="search" title="Search" href="search.html" />
```

```
<link rel="copyright" title="Copyright" href="#" />
```

```
<link rel="next" title="How to build this documentation from the source" href="build_this.html" />
```

```
<link rel="prev" title="MIT Kerberos License information" href="mitK5license.html" />
```

```
</head>
```

```
<body>
```

```
<div class="header-wrapper">
```

```
<div class="header">
```

```
<h1>MIT Kerberos Documentation</h1>
```

```
<div class="rel">
```

```
<a href="index.html" title="Full Table of Contents"
```

```
 accesskey="C">Contents |
```

```
<a href="mitK5license.html" title="MIT Kerberos License information"
```

```
 accesskey="P">previous |
```

```
<a href="build_this.html" title="How to build this documentation from the
```

```
source"
```

```
 accesskey="N">next |
```

```
<a href="genindex.html" title="General Index"
 accesskey="I">index |
<a href="search.html" title="Enter search criteria"
 accesskey="S">Search |
feedback
</div>
</div>
</div>
```

```
<div class="content-wrapper">
<div class="content">
 <div class="document">
```

```
 <div class="documentwrapper">
 <div class="bodywrapper">
 <div class="body" role="main">
```

```
 <div class="section" id="copyright">
<h1>Copyright</h1>
<p>Copyright 1985-2019 by the Massachusetts Institute of
Technology and its contributors. All rights reserved.</p>
<p>See MIT
Kerberos License information for
additional copyright and license
information.</p>
</div>
```

```
 </div>
</div>
</div>
</div>
 <div class="sidebar">
<h2>On this page</h2>

Copyright

```

```


<h2>Table of contents</h2>

<li class="toctree-l1">For users
<li class="toctree-l1">For administrators
<li class="toctree-l1">For application developers
<li class="toctree-l1">For plugin module
developers
<li class="toctree-l1">Building Kerberos V5
<li class="toctree-l1">Kerberos V5 concepts
```



```
<li class="toctree-l1"><a class="reference internal"
href="formats/index.html">Protocols and file formats
<li class="toctree-l1">MIT Kerberos features
<li class="toctree-l1">How to build this documentation from
the source
<li class="toctree-l1">Contributing to the MIT Kerberos
Documentation
<li class="toctree-l1">Resources

```

```


```

```
<h4>Full Table of Contents</h4>
```

```
<h4>Search</h4>
```

```
<form class="search" action="search.html" method="get">
```

```
<input type="text" name="q" size="18" />
```

```
<input type="submit" value="Go" />
```

```
<input type="hidden" name="check_keywords" value="yes" />
```

```
<input type="hidden" name="area" value="default" />
```

```
</form>
```

```
</div>
```

```
<div class="clearer"></div>
```

```
</div>
```

```
</div>
```

```
<div class="footer-wrapper">
```

```
<div class="footer" >
```

```
<div class="right" ><i>Release: 1.17</i>

```

```
© Copyright 1985-2019, MIT.
```

```
</div>
```

```
<div class="left">
```

```
<a href="index.html" title="Full Table of Contents"
```

```
>Contents |
```

```
<a href="mitK5license.html" title="MIT Kerberos License information"
```

```
>previous |
```

```
<a href="build_this.html" title="How to build this documentation from the source"
```

```
>next |
```

```
<a href="genindex.html" title="General Index"
```

```
>index |
```

```
<a href="search.html" title="Enter search criteria"
```

```
>Search |
```

```
feedback
```

```
</div>
```

```
</div>
```

```
</div>
```

```
</body>
```

```
</html>
```

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in "src/lib/gssapi", including the following files:

lib/gssapi/generic/gssapi\_err\_generic.et  
lib/gssapi/mechglue/g\_accept\_sec\_context.c  
lib/gssapi/mechglue/g\_acquire\_cred.c  
lib/gssapi/mechglue/g\_canon\_name.c  
lib/gssapi/mechglue/g\_compare\_name.c  
lib/gssapi/mechglue/g\_context\_time.c  
lib/gssapi/mechglue/g\_delete\_sec\_context.c  
lib/gssapi/mechglue/g\_dsp\_name.c  
lib/gssapi/mechglue/g\_dsp\_status.c  
lib/gssapi/mechglue/g\_dup\_name.c  
lib/gssapi/mechglue/g\_exp\_sec\_context.c

lib/gssapi/mechglue/g\_export\_name.c  
lib/gssapi/mechglue/g\_glue.c  
lib/gssapi/mechglue/g\_imp\_name.c  
lib/gssapi/mechglue/g\_imp\_sec\_context.c  
lib/gssapi/mechglue/g\_init\_sec\_context.c  
lib/gssapi/mechglue/g\_initialize.c  
lib/gssapi/mechglue/g\_inquire\_context.c  
lib/gssapi/mechglue/g\_inquire\_cred.c  
lib/gssapi/mechglue/g\_inquire\_names.c  
lib/gssapi/mechglue/g\_process\_context.c  
lib/gssapi/mechglue/g\_rel\_buffer.c  
lib/gssapi/mechglue/g\_rel\_cred.c  
lib/gssapi/mechglue/g\_rel\_name.c  
lib/gssapi/mechglue/g\_rel\_oid\_set.c  
lib/gssapi/mechglue/g\_seal.c  
lib/gssapi/mechglue/g\_sign.c  
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lib/gssapi/mechglue/g\_unseal.c  
lib/gssapi/mechglue/g\_userok.c  
lib/gssapi/mechglue/g\_utils.c  
lib/gssapi/mechglue/g\_verify.c  
lib/gssapi/mechglue/gssd\_pname\_to\_uid.c  
lib/gssapi/mechglue/mglueP.h  
lib/gssapi/mechglue/oid\_ops.c  
lib/gssapi/spnego/gssapiP\_spnego.h  
lib/gssapi/spnego/spnego\_mech.c

and the initial implementation of incremental propagation,  
including  
the following new or changed files:

include/iprop\_hdr.h  
kadmin/server/ipropd\_svc.c  
lib/kdb/iprop.x  
lib/kdb/kdb\_convert.c  
lib/kdb/kdb\_log.c  
lib/kdb/kdb\_log.h  
lib/krb5/error\_tables/kdb5\_err.et  
kprop/kpropd\_rpc.c  
kprop/kproplog.c

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import os
import sys
import re

def warn(fname, ln, msg):
 print '%s: %d: %s' % (fname, ln + 1, msg)

def indicates_license(line):
 return 'Copyright' in line or 'COPYRIGHT' in line or 'License' in line

Check a comment for boilerplate violations. Return true if the comment
is a license statement.
def check_comment(comment, fname, ln, code_seen, nonlicense_seen):
 text_seen = False
 is_license = False
 for line in comment:
 if not is_license and indicates_license(line):
 is_license = True
 if text_seen:
 warn(fname, ln, 'License begins after first
line of comment')
 elif code_seen:
 warn(fname, ln, 'License after code')
 elif nonlicense_seen:
 warn(fname, ln, 'License after non-license comments')
 break
 # DB2 licenses start with '/*-' and we don't want to change them.
 if line != " and line != '-':
 text_seen = True
 return is_license

def check_file(lines, fname):
 # Skip emacs mode line if present.
 ln = 0
 if '-*- mode: c;' in lines[ln]:
 ln += 1

 # Check filename comment if present.
 m = re.match(r'^* ([^]*)(- .*)? */', lines[ln])
 if m:
 if m.group(1) != fname:

```



```

 warn(fname, ln, 'Wrong filename in comment')
 ln += 1

Scan for license statements.
in_comment = False
code_seen = False
nonlicense_seen = False
for line in lines[ln:]:
 # Strip out whitespace and comments contained within a line.
 if not in_comment:
 line = re.sub(r'\s*.*?\s*/',
", line)
 line = line.strip()

 if not in_comment and '/*' in line:
 (line, sep, comment_part) = line.partition('/*')
 comment = [comment_part.strip()]
 comment_starts_at = ln
 in_comment = True
 elif in_comment and '*/' not in line:
 comment.append(line.lstrip('*').rstrip())
 elif in_comment:
 (comment_part, sep, line) = line.partition('*/')
 comment.append(comment_part.strip())
 is_license = check_comment(comment, fname, comment_starts_at,
 code_seen, nonlicense_seen)
 nonlicense_seen = nonlicense_seen or not is_license
 in_comment = False
 elif line.strip() != "":
 code_seen = True

 ln += 1

for fname in sys.argv[1:]:
 if fname.startswith('./'):
 fname = fname[2:]
 f = open(fname)
 lines = f.readlines()
 f.close()
 check_file(lines, fname)
/* -*- mode: c; c-basic-offset: 4; indent-tabs-mode: nil -*- */
/*

```

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@end enumerate

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@heading Appendix: How to Apply These Terms to Your New Programs

If you develop a new program, and you want it to be of the greatest possible use to the public, the best way to achieve this is to make it free software which everyone can redistribute and change under these terms.

To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the ``copyright" line and a pointer to where the full notice is found.

@smallexample

@var{one line to give the program's name  
and a brief idea of what it does.}

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@end smallexample

Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

```
@smallexample
Gnomovision version
69, Copyright (C) @var{year} @var{name of author}
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.
@end smallexample
```

The hypothetical commands @samp{show w} and @samp{show c} should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than @samp{show w} and @samp{show c}; they could even be mouse-clicks or menu items---whatever suits your program.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a ``copyright disclaimer" for the program, if necessary. Here is a sample; alter the names:

```
@example
Yoyodyne, Inc., hereby disclaims all copyright interest in the program
`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
@var{signature of Ty Coon}, 1 April 1989
Ty Coon, President of Vice
@end example
```

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The

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# 1.60 libdaemon 0.14

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```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
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## 1.63 gmp 6.1.2

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From strings/apr\_fnmatch.c, include/apr\_fnmatch.h, misc/unix/getopt.c, file\_io/unix/mktemp.c, strings/apr\_strings.c:

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From dso/aix/dso.c:

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From strings/apr\_strnatcmp.c,



include/apr\_strings.h:

strnatcmp.c -- Perform 'natural order' comparisons of strings in C.  
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From  
strings/apr\_sprintf.c:

```
*
* cvt - IEEE floating point formatting routines.
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```

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builds/unix/pkg.m4

docs/FTL.TXT
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include/freetype/internal/fthash.h

src/base/fthash.c
src/base/md5.c
src/base/md5.h

src/bdf/bdf.c
src/bdf/bdf.h
src/bdf/bdfdrivr.c
src/bdf/bdfdrivr.h
src/bdf/bdferror.h
src/bdf/bdflib.c
src/bdf/module.mk
src/bdf/README
```



```
src/bdf/rules.mk
#
src/pcf/module.mk
src/pcf/pcf.c
src/pcf/pcf.h
src/pcf/pcfdrivr.c
src/pcf/pcfdrivr.h
src/pcf/pcferror.h
src/pcf/pcfread.c
src/pcf/pcfread.h
src/pcf/pcfutil.c
src/pcf/pcfutil.h
src/pcf/README
src/pcf/rules.mk
#
src/gzip/adler32.c
src/gzip/infblock.c
src/gzip/infblock.h
src/gzip/infcodes.c
src/gzip/infcodes.h
src/gzip/inffixed.h
src/gzip/inflate.c
src/gzip/inftrees.c
src/gzip/inftrees.h
src/gzip/infutil.c
src/gzip/infutil.h
src/gzip/zconf.h
src/gzip/zlib.h
src/gzip/zutil.c
src/gzip/zutil.h
#
src/tools/apinames.c
src/tools/ftandom/ftandom.c
#
#
EOF
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-- vile: txtmode file-encoding=utf-8

Upstream source <https://invisible-island.net/ncurses/ncurses.html>

This package is used for testing builds of ncurses.

Current ncurses maintainer: Thomas Dickey <[dickey@invisible-island.net](mailto:dickey@invisible-island.net)>

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Files: \*

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Files: alocal.m4 package

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Files: doc/html/NCURSES-Programming-HOWTO.html

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Files: progs/tset.c ncurses/tinfo/read\_termcap.c

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-- vile: txtmode file-encoding=utf-8

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Current ncurses maintainer: Thomas Dickey <[dickey@invisible-island.net](mailto:dickey@invisible-island.net)>

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Files: \*

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-- vile: txtmode file-encoding=utf-8
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```
-- vile:txtmode fc=72
-- $Id: COPYING,v 1.6 2018/01/01 12:00:00 tom Exp $
```

## 1.74 attr 2.4.47

## 1.74.1 Available under license :

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## 1.75 ipmi-tool 1.8.18

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# 1.76 libnettle6 3.4

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```
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```
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```

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## 1.78 gawk 4.2.1

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Because

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```
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## 1.79 coreutils 8.30

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# 1.82 ethtool 4.19

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## 1.83 libcgrouop 0.41

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## 1.85 less 530

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## 1.86 libusb 1.0.22

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```
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```
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## 1.91 tcl 8.6.8

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## 1.92 libcroco 0.6.13

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### History and License

\*\*\*\*\*

#### History of the software

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <https://www.cwi.nl/>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <https://www.cnri.reston.va.us/>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation; see <http://www.zope.com/>). In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Release	Derived from	Year	Owner	GPL compatible?
0.9.0 thru 1.2	n/a	1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
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2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	no

2.1	2.0+1.6.1	2001	PSF	no	
2.0.1	2.0+1.6.1	2001	PSF	yes	
2.1.1	2.1+2.0.1	2001	PSF	yes	
2.1.2	2.1.1	2002	PSF	yes	
2.1.3	2.1.2	2002	PSF	yes	
2.2 and above	2.1.1	2001-now	PSF	yes	

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Mersenne Twister

-----

The `:mod:`_random`` module includes code based on a download from

<http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/MT2002/emt19937ar.html>. The following are the verbatim comments from the original code::

A C-program for MT19937, with initialization improved 2002/1/26.  
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`  
or `init_by_array(init_key, key_length)`.

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<http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/emt.html>  
email: m-mat @ math.sci.hiroshima-u.ac.jp (remove space)

Sockets

-----  
The `:mod:`socket``  
module uses the functions, `:func:`getaddrinfo``, and  
`:func:`getnameinfo``, which are coded in separate source files from the WIDE  
Project, <http://www.wide.ad.jp/>. ::

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Asynchronous socket services  
-----

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Execution tracing

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UUencode and UUdecode functions

-----

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Modified by Jack Jansen, CWI, July 1995:

- Use binascii module to do the actual line-by-line conversion between ascii and binary. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with Python standard

#### XML Remote Procedure Calls

-----

The `:mod:`xmlrpc.client`` module contains the following notice::

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test\_epoll

-----

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Select kqueue

-----

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SipHash24

-----

The file `:file:Python/pyhash.c` contains Marek Majkowski's implementation of Dan Bernstein's SipHash24 algorithm. It contains the following note:

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Original location:

<https://github.com/majek/csiphash/>

Solution inspired by code from:

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djb ([supercop/crypto\\_auth/siphhash24/little2](https://github.com/supercop/crypto_auth/siphhash24/little2))

Jean-Philippe Aumasson

(<https://131002.net/siphhash/siphhash24.c>)

strtod and dtoa

-----

The file :file:`Python/dtoa.c`, which supplies C functions dtoa and strtod for conversion of C doubles to and from strings, is derived from the file of the same name by David M. Gay, currently available from <http://www.netlib.org/fp/>. The original file, as retrieved on March 16, 2009, contains the following copyright and licensing notice::

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cfuhash  
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The implementation of the hash table used by the `:mod:`tracemalloc`` is based on the cfuhash project::

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see <https://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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## 1.96 file 5.34

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```
-makefile-
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Commands to generate dependency files
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)

Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE= -Wl,-rpath,

Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

Shared object suffix
SO = so
Non-shared intermediate object suffix
STATIC_O = ao

Compilation rules
%. $(STATIC_O): $(srcdir)/%.c
```

```

$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c)
$(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.${STATIC_O}: $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

```

### ## Dependency rules

```

%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s^\($*\)\.o[:]*/\1.o $@ : /g\" > $@; \
[-s $@] || rm -f $@'

```

```

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s^\($*\)\.o[:]*/\1.o $@ : /g\" > $@; \
[-s $@] || rm -f $@'

```

### ## Versioned libraries rules

```

%.${SO}.${SO_TARGET_VERSION_MAJOR}: %.${SO}.${SO_TARGET_VERSION}
$(RM) $@ && ln -s ${<F} $@
%.${SO}: %.${SO}.${SO_TARGET_VERSION_MAJOR}
$(RM) $@ && ln -s ${*F}.${SO}.${SO_TARGET_VERSION} $@

```

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## 1.98 protobuf 3.6.1

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- \* Slicing support for repeated scalar fields for the Python API.

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- \* MS Visual Studio error format option.
- \* Detect unordered\_map in stl\_hash.m4.

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- \* gzip/zlib I/O support.

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- \* Fixed warnings about generated constructors not explicitly initializing all fields (only present with certain compiler settings).
- \* Added generation of field number constants.

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- \* Fixed initialization ordering problem in logging code.

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- \* Small patch improving performance of in Python serialization.

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- \* Emacs mode for Protocol Buffers (editors/protobuf-mode.el).

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- \* Added Swap(), SwapElements(), and RemoveLast() to Reflection interface.

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- \* HPUX support.

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- \* Detect whether zlib is new enough in configure script.
- \* Fixes for Solaris 10 32/64-bit confusion.

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- \* Optimize Java serialization code when writing a small message to a stream.
- \* Optimize Java serialization of strings so that UTF-8 encoding happens only once per string per serialization call.
- \* Clean up some Java warnings.
- \* Fix bug with permanent callbacks that delete themselves when run.

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- \* Added CodedInputStream.getTotalBytesRead().

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- \* Fixed m4/acx\_pthread.m4 problem for some Linux distributions.

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- \* Fixed detection of sched\_yield on Solaris.
- \* Added atomicops for Solaris

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- \* Fixed minor IBM x1C compiler build issues
- \* Added atomicops for AIX (POWER)

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## 1.99 acpid 2.0.30

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```
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## 1.103 rrdtool 1.7.0

### 1.103.1 Available under license :

\$Id\$

I would like to thank to following people for helping to bring RRDtool into existence.

Alan Lichty <alan\_lichty with eli.net>

Alan Milligan <alan.milligan@last-bastion.net> Python bindings

Alex van den Bogaerd <alex with ergens.op.het.net> (rrd\_resize.c and more)  
Amos Shapira <amos with gezernet.co.il>  
Andreas Krooomaa <andre with ml.ee>  
Andrew Turner <turner with mint.net> (LAST consolidator)  
Aaron Gallagher <\_ with habnab.it> MEDIAN operator  
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Bernard Fischer <bfischer with syslog.ch> 64bit stuff, --alt-autoscale-max  
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Florian octo Forster <rrdtool nospam.verplant.org> rrd\_restore libxml2 rewrite, deprecated function export, rrdcached  
Fidelis Assis <fidelis pobox.com> lua bindings  
Henrik Storer <henrik with hswm.dk> functions for min/max values of data in graph  
Hermann Hueni <hueni with glue.ch> (SunOS porting)  
Jakob Ilves <jilves with se.oracle.com> HPUX 11  
Jeff R. Allen <jeff.allen with acm.org> (autoconfigure, portability)  
Jeremy Fischer <jeremy with pobox.com> (Makefile changes & RPM builds)  
Jess Couto Fandio  
Joel Becker <jlbec with raleigh.ibm.com> AIX  
Joey Miller <joeym with inficad.com> php3 and php4 bindings  
Jost.Krieger  
<Jost.Krieger with ruhr-uni-bochum.de>  
Kai Siering <kai.siering with mediaways.net>  
Kevin Brintnall <kbrint with rufus.net> bugfixes in and additions to rrdcached, including journaling support  
Larry Leszczynski <larryl with furph.com>  
Mark Plaksin <happy@usg.edu> rrd\_graph\_v  
Matt Chambers <matthew.chambers with vanderbilt.edu> --full-size-mode for rrdgraph  
Melchior Rabe <rrdtool at mrab.de> -- legend position patch  
McCreary mcreary with xoanon.colorado.edu  
Mike Mitchell <mcm with unx.sas.com>  
Mike Slifcak <slif with bellsouth.net> many rrdtool-1.1.x fixes  
Oleg Cherevko <olwi with icyb.kiev.ua>  
Otmr Lendl <O.Lendl with Austria.EU.net> (lots of bugfixes)  
Pablo Sanchez <pablo at blueoakdb.com> (CDEF vs VDEF)  
Patrick Cherry <patrick with bytemark.co.uk>

Paul Joslin <Paul.Joslin with sdrc.com>  
Peter Speck <speck with vitality.dk> eps/svg/pdf file format code in rrdtool-1.x  
Peter Stamfest <peter with stamfest.at> initial multi-thread support  
Peter Breitenlohner <peb with  
mppmu.mpg.de> many patches for rrdtool 1.2.x  
Philippe.Simonet <Philippe.Simonet with swisscom.com> (NT porting)  
Poul-Henning Kamp <phk with freebsd.org> CDEF enhancements  
REIBENSCHUH Alfred <alfred.reibenschuh with it-austria.com> AIX  
Radoslaw Karas <rkaras with tyndall.ie>  
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Roman Hoogant <rhoogant with ee.ethz.ch>  
Ronan Mullally <ronan in 4L.ie>  
Roger J. Meier <roger.meier in terreactive.ch> (arbitrary linelength in rrdtool)  
Russ Wright <rwwright with home.com>  
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Selena M Brewington <smbrewin with ichips.intel.com> add\_ds  
Shane O'Donnell <shaneo with opennms.org>  
Simon Leinen <simon with switch.ch>  
Steen Linden <Steen.Linden with ebone.net>  
Sebastian Harl <sh at tokkee.org> debian packaging, rrdcached fixes and enhancements  
Stefan Ludewig <Stefan.Ludewig at exitgames.com> 1.3 WIN32 Port  
Stefan Mueller <s.mueller with computer.org> HP-UX 11  
Steve Harris <steveh with wesley.com.au>  
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Thomas Gutzler <thomas.gutzler with gmail.com> dashed lines  
Tom Crawley <Tom.Crawley with hi.riotinto.com.au> (GCC&HP configuration)  
Travis Brown <tebrown with csh.rit.edu>  
Tuc <ttsg with ttsg.com>  
Ulf Lilleengen <lulf with pvv.ntnu.no> Python binding for 'rrdtool first'  
Ulrich Schilling <schilling with netz.uni-essen.de> AIX  
Wim Heirman <wim.heirman@elis.ugent.be> --units=si option  
Wolfgang Schrimm <wschrimm with uni-hd.de> xport function  
Wrolf Courtney <wrolf with wrolf.net> (HP-UX)  
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Alexander Lucke (lucke with dns-net.de) of DNS:NET Internet  
Services (www.dns-net.de) <http://rrdtool.org>  
Hedley Simons <heds@metahusky.net>  
Nicola Worthington <nicolaw@cpan.org>  
Wegmann, Christof <Christof.Wegmann@exitgames.com> 1.3/trunk win32 port

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2006-Jan-27

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docs/FTL.TXT
docs/GPLv2.TXT
#
include/freetype/internal/fthash.h
#
src/base/fthash.c
src/base/md5.c
src/base/md5.h
#
src/bdf/bdf.c
src/bdf/bdf.h
src/bdf/bdfdrivr.c
src/bdf/bdfdrivr.h
src/bdf/bdferror.h
src/bdf/bdflib.c
src/bdf/module.mk
src/bdf/README
src/bdf/rules.mk
#
src/pcf/module.mk
src/pcf/pcf.c
src/pcf/pcf.h
src/pcf/pcfdrivr.c
src/pcf/pcfdrivr.h
src/pcf/pcferror.h
src/pcf/pcfread.c
src/pcf/pcfread.h
src/pcf/pcfutil.c
src/pcf/pcfutil.h
src/pcf/README
src/pcf/rules.mk
#
src/gzip/adler32.c
src/gzip/infblock.c
src/gzip/infblock.h
src/gzip/infcodes.c
src/gzip/infcodes.h
src/gzip/inffixed.h
src/gzip/inflate.c
src/gzip/inftrees.c
src/gzip/inftrees.h
src/gzip/infutil.c
src/gzip/infutil.h
src/gzip/zconf.h
src/gzip/zlib.h
src/gzip/zutil.c
src/gzip/zutil.h
```

```
#
src/tools/apinames.c
src/tools/ftrandom/ftrandom.c
#
#
EOF
```

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Mathias Koch (mkoch - at - idesis.de 7 : email to boost-owner@lists.boost.org Sep 2007 13:20:09 +0200)

--- end ---

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This is an attempt to acknowledge early contributions to the garbage collector. Later contributions should instead be mentioned in README.changes.

HISTORY -

Early versions of this collector were developed as a part of research projects supported in part by the National Science Foundation and the Defense Advance Research Projects Agency.

The garbage collector originated as part of the run-time system for the Russell programming language implementation. The first version of the garbage collector was written primarily by Al Demers. It was then refined and mostly rewritten, primarily by Hans-J. Boehm, at Cornell U., the University of Washington, Rice University (where it was first used for C and assembly code), Xerox PARC, SGI, and HP Labs. However, significant contributions have also been made by many others.

Some other contributors:

More recent contributors are mentioned in the modification history in README.changes. My apologies for any omissions.

The SPARC specific code was originally contributed by Mark Weiser. The Encore Multimax modifications were supplied by Kevin Kenny (kenny@m.cs.uiuc.edu). The adaptation to the IBM PC/RT is largely due to Vernon Lee, on machines made available to Rice by IBM. Much of the HP specific code and a number of good suggestions for improving the generic code are due to Walter Underwood. Robert Brazile (brazile@diamond.bbn.com) originally supplied the ULTRIX code. Al Dosser (dosser@src.dec.com) and Regis Cridlig (Regis.Cridlig@cl.cam.ac.uk) subsequently provided updates and information on variation between ULTRIX systems. Parag Patel (parag@netcom.com) supplied the A/UX code. Jesper Peterson(jep@mtiame.mtia.oz.au), Michel Schinz, and Martin Tauchmann (martintauchmann@bigfoot.com) supplied the Amiga port. Thomas Funke (thf@zelator.in-berlin.de(?)) and Brian D.Carlstrom (bdc@clark.lcs.mit.edu) supplied the NeXT ports. Douglas Steel (doug@wg.icl.co.uk) provided ICL DRS6000 code. Bill Janssen (janssen@parc.xerox.com) supplied the SunOS dynamic loader specific code. Manuel Serrano (serrano@cornas.inria.fr) supplied linux and Sony News specific code. Al Dosser provided Alpha/OSF/1 code. He and Dave Detlefs(detlefs@src.dec.com) also provided several generic bug fixes. Alistair G. Crooks(agg@uts.amdahl.com) supplied the NetBSD and 386BSD ports. Jeffrey Hsu (hsu@soda.berkeley.edu) provided the FreeBSD port. Brent Benson (brent@jade.ssd.csd.harris.com) ported the collector to a Motorola 88K processor running CX/UX (Harris NightHawk). Ari Huttunen (Ari.Huttunen@hut.fi) generalized the OS/2 port to nonIBM development environments (a nontrivial task). Patrick Beard (beard@cs.ucdavis.edu) provided the initial MacOS port. David Chase, then at Olivetti Research, suggested several improvements. Scott Schwartz (schwartz@groucho.cse.psu.edu) supplied some of the code to save and print call stacks for leak detection on a SPARC. Jesse Hull and John Ellis supplied the C++ interface code. Zhong Shao performed much of the experimentation that led to the current typed allocation facility. (His dynamic type inference code hasn't made it into the released version of the collector, yet.)

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<body>

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Copyright Test

</h3>

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<p class="copyright">

2018 Joe Blow, John Coe

</p>

<p class="copyright">

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</p>

</body>

</html>

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<!DOCTYPE article PUBLIC "-//Boost//DTD BoostBook XML V1.0//EN"

```
"http://www.boost.org/tools/boostbook/dtd/boostbook.dtd">
<article id="copyright_test" last-revision="DEBUG MODE Date: 2000/12/20 12:00:00 $"
xmlns:xi="http://www.w3.org/2001/XInclude">
<title>Copyright Test</title>
<articleinfo>
 <copyright>
 <year>1963</year> <year>1964</year> <year>1965</year> <holder>Jane Doe</holder>
 </copyright>
 <copyright>
 <year>2018</year> <holder>Joe Blow, John Coe</holder>
 </copyright>
 <copyright>
 <year>1977</year> <year>1985</year> <holder>Someone else</holder>
 </copyright>
</articleinfo>
</article>
```

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// Parts of Boost.Geometry are redesigned from Geodan's Geographic Library
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////

```
[#mpl]
MPL Support, <boost/mp11/mpl.hpp>
:toc:
:toc-title:
:idprefix:
```

The header `<boost/mp11/mpl.hpp>`, when included, defines the  
necessary support infrastructure for `mp_list` and `std::tuple`  
to be valid link:../../../../libs/mpl[MPL] sequences.

NOTE: `mpl.hpp` is not included by `<boost/mp11.hpp>`.

## 1.109 libidn 1.35

### 1.109.1 Available under license :

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## 1.112 libpng 1.6.37

### 1.112.1 Available under license :

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pnm2png / png2pnm --- conversion from PBM/PGM/PPM-file to PNG-file

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## 1.116 busybox 1.29.2

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# 1.117 guava 12.0.1

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# 1.118 python-setuptools 39.0.1

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## 1.122 perl 5.24.4

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abstract: 'Build and install Perl modules'

author:

- 'Ken Williams <kwilliams@cpan.org>'

- "Development questions, bug reports, and patches should be sent to the\nModule-Build mailing list at <module-build@perl.org>."

build\_requires:

File::Temp: 0.15

Test::Harness: 3.16

Test::More: 0.49

generated\_by: 'Module::Build version 0.3608'

license: gpl

meta-spec:

url: <http://module-build.sourceforge.net/META-spec-v1.4.html>  
version: 1.4  
name: Module-Build  
resources:  
MailingList: <mailto:module-build@perl.org>  
license: <http://dev.perl.org/licenses/>  
repository: <http://github.com/dagolden/module-build/>  
version: 3

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Julian Seward, [jseward@bzip.org](mailto:jseward@bzip.org)

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```
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```

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```
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```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

```
That's all there is to it!
#!perl
```

```
=head1 NAME
```

```
copyright.t
```

```
=head1 DESCRIPTION
```

Tests that the latest copyright years in the top-level README file and the `C<perl -v>` output match each other.

If the test fails, update at least one of README and perl.c so that they match reality.

Optionally you can pass the `C<--now>` option to check they are at the current year. This isn't checked by default, so that it doesn't fail for people working on older releases. It should be run before making a new release.

```
=cut
```

```
use strict;
use Config;
```

```

BEGIN { require './test.pl' }

if ($Config{usecrosscompile}) {
 skip_all("Not all files are available during cross-compilation");
}

my ($opt) = @ARGV;

my $readme_year = readme_year();
my $v_year = v_year();

Check that both copyright dates are up-to-date, but only if requested, so
that tests still pass for people intentionally working on older versions:
if ($opt eq '--now')
{
 my $current_year = (gmtime)[5] + 1900;
 is $v_year, $current_year, 'perl -v copyright includes current
year';
 is $readme_year, $current_year, 'README copyright includes current year';
}

Otherwise simply check that the two copyright dates match each other:
else
{
 is $readme_year, $v_year, 'README and perl -v copyright dates match';
}

done_testing;

sub readme_year
returns the latest copyright year from the top-level README file
{

 open my $readme, '<', './README' or die "Opening README failed: $!";

 # The copyright message is the first paragraph:
 local $/ = "";
 my $copyright_msg = <$readme>;

 my ($year) = $copyright_msg =~ /\b(\d{4,})/s
 or die "Year not found in README copyright message '$copyright_msg'";

 $year;
}

sub v_year

```

```
returns the latest copyright year shown in perl -v
{

my $output = runperl switches => ['-v'];
my ($year) = $output =~ /copyright 1987.*\b(\d{4,})/i
 or die "Copyright statement not found in perl -v output '$output'";

$year;
}
```

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```
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`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

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```
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## 1.124 log-rotate 3.14.0

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```
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```

```
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## 1.126 dosfs-tools 4.1

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Version 3, 29 June 2007

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## 1.128 libunwind 1.2-rc1

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The licenses for most software are designed to take away your  
freedom to share and change it. By contrast, the GNU General Public

Licenses are intended to guarantee your freedom to share and change free software--to make sure the software is free for all its users.

This license, the Lesser General Public License, applies to some specially designated software packages--typically libraries--of the Free Software Foundation and other authors who decide to use it. You can use it too, but we suggest you first think carefully about whether this license or the ordinary General Public License is the better strategy to use in any particular case, based on the explanations below.

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To protect your rights, we need to make restrictions that forbid distributors to deny you these rights or to ask you to surrender these rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link other code with the library, you must provide complete object files to the recipients, so that they can relink them with the library after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

We protect your rights with a two-step method: (1) we copyright the library, and (2) we offer you this license, which gives you legal permission to copy, distribute and/or modify the library.

To protect each distributor, we want to make it very clear that there is no warranty for the free library. Also, if the library is modified by someone else and passed on, the recipients should know that what they have is not the original version, so that the original author's reputation will not be affected by problems that might be introduced by others.

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Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot

effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is

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The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

^L

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charge to all third parties under  
the terms of this License.
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table of data to be supplied by an application program that uses  
the facility, other than as an argument passed when the facility  
is invoked, then you must make a good faith effort to ensure that,  
in the event an application does not supply such function or  
table, the facility still operates, and performs whatever part of  
its purpose remains meaningful.

(For example, a function in a library to compute square roots has  
a purpose that is entirely well-defined independent of the  
application. Therefore, Subsection 2d requires that any  
application-supplied function or table used by this function must  
be optional: if the application does not supply it, the square  
root function must still compute square roots.)

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identifiable sections of that work are not derived from the Library,  
and  
can be reasonably considered independent and separate works in  
themselves, then this License, and its terms, do not apply to those  
sections when you distribute them as separate works. But when you  
distribute the same sections as part of a whole which is a work based



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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or

linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

^L

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2)

will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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a) Accompany the combined library with a copy of the same work based on the Library, uncombined

with any other library

facilities. This must be distributed under the terms of the Sections above.

b) Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

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To apply these terms, attach the following notices to the library.  
It is safest to attach them to the start of each source  
file to most  
effectively convey the exclusion of warranty; and each file should  
have at least the "copyright" line and a pointer to where the full  
notice is found.

<one line to give the library's name and a brief idea of what it  
does.>

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You should have received a copy of the GNU Lesser General Public  
License along with this library; if not, write to the Free Software  
Foundation, Inc., 51 Franklin Street, Fifth Floor,  
Boston, MA 02110-1301 USA

Also add information on how to contact you by electronic and paper  
mail.

You should also get your employer (if you work as a programmer) or  
your  
school, if any, to sign a "copyright disclaimer" for the library, if  
necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the  
library `Frob' (a library for tweaking knobs) written by James  
Random Hacker.

<signature of Ty Coon>, 1 April 1990  
Ty Coon, President of Vice

That's all there is to it!

-----  
EFFECTIVE OCT 2008, LICENSE IS BEING CHANGED TO LGPL-2.1 (though not reflected  
in released code until Nov 2009 - slow release cycle...)  
-----

Discussion thread from mailing list archive, with approval from everyone actively involved or holding original licensing rights included.

[Cracklib-devel] cracklib license

From: Mike Frysinger <vapier@ge...> - 2007-10-02 01:16

Attachments: Message as HTML

looks like 2.8.11 is out and marked as "GPL-2" ... releasing libraries unde=  
r=20

GPL-2 is not desirable at all ... this is why the LGPL-2.1 exists  
=2Dmike

Re: [Cracklib-devel] cracklib license

From: Neulinger, Nathan <nneul@um...> - 2007-10-02 01:18

I understand that, and you're welcome to bring it up with Alec directly and see if he wants to relicense his code as LGPL... but at this point, it was enough to just get it consistent and documented as to what it was released under. This wasn't actually a license change, just a clarification of the licensing that was already in place.=20

-- Nathan

=20

-----  
Nathan Neulinger EMail: nneul@um...

University of Missouri - Rolla Phone: (573) 341-6679

UMR Information Technology Fax: (573) 341-4216

> -----Original Message-----

> From: cracklib-devel-bounces@li...

> [mailto:cracklib-devel-bounces@li...] On Behalf Of

> Mike Frysinger

> Sent: Monday, October 01, 2007 8:15 PM

> To: cracklib-devel@li...

> Subject: [Cracklib-devel] cracklib license

>=20

> looks like 2.8.11 is out and marked as "GPL-2" ... releasing

> libraries under

> GPL-2 is not desirable at all ... this is why the LGPL-2.1 exists

> -mike

Re: [Cracklib-devel] cracklib license

From: Mike Frysinger <vapier@ge...> - 2007-10-02 01:33

Attachments: Message as HTML



On Monday 01 October 2007, Neulinger, Nathan

wrote:

> I understand that, and you're welcome to bring it up with Alec directly  
> and see if he wants to relicense his code as LGPL... but at this point,  
> it was enough to just get it consistent and documented as to what it was  
> released under. This wasn't actually a license change, just a  
> clarification of the licensing that was already in place.

the original license (before moving to sourceforge -- aka, 2.7) was not=20  
GPL-2 ... it was a modified artistic license ... i didnt notice the license=  
=20  
change until it was mentioned in the latest notes.

unlike the old license, GPL-2 prevents people from using cracklib unless th=  
eir=20  
applications are also GPL-2 which imo is just wrong. it isnt the place of =  
a=20  
library to dictact to application writes what license they should be using.=  
=20  
thus LGPL-2.1 enters to fill this void.  
=20Dmike

Re: [Cracklib-devel]

cracklib license

From: Neulinger, Nathan <nneul@um...> - 2007-10-02 01:46

Seems like the ideal thing here would be for you and the other distro  
maintainers to get together with Alec in a conversation and come to a  
decision as to what licensing scheme y'all want. I haven't really done  
much other than cleaning up the packaging and patches and a small bit of  
additional code, so whatever licensing y'all come up with is fine by me.

-- Nathan

=20

-----  
Nathan Neulinger EMail: nneul@um...

University of Missouri - Rolla Phone: (573) 341-6679

UMR Information Technology Fax: (573) 341-4216

> -----Original Message-----

> From: cracklib-devel-bounces@li...

> [mailto:cracklib-devel-bounces@li...] On Behalf Of

> Mike Frysinger

> Sent: Monday, October 01, 2007 8:33 PM

> To: Neulinger, Nathan

> Cc: cracklib-devel@li...; Alec Muffett

> Subject: Re: [Cracklib-devel] cracklib license

>=20

> On Monday 01 October 2007, Neulinger, Nathan wrote:  
>> I understand that, and you're welcome to bring it up with Alec  
> directly  
>> and see if he wants to relicense his code as LGPL... but at this  
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> place of a  
> library  
to dictact to application writes what license they should  
> be using.  
> thus LGPL-2.1 enters to fill this void.  
> -mike

Re: [Cracklib-devel] cracklib license

From: Alec Muffett <alecm@cr...> - 2007-10-02 08:57

> Seems like the ideal thing here would be for you and the other distro  
> maintainers to get together with Alec in a conversation and come to a  
> decision as to what licensing scheme y'all want. I haven't really done  
> much other than cleaning up the packaging and patches and a small  
> bit of  
> additional code, so whatever licensing y'all come up with is fine  
> by me.

I am sympathetic. Guys, what do you reckon?

What I am hearing so far is that LGPL makes sense, since it can be  
linked with any code, not just GPL...

-a

Re: [Cracklib-devel] cracklib license

From: Devin Reade <gdr@gn...> - 2007-10-02 15:04

I would like to see it under LGPL as well. I

think it is in everyone's

best interests to have as secure systems as possible, and I think tainting it via GPL will just make it less likely that the library gets used, and will not usually cause companies/developers to GPL the dependent code (where it is not already GPL).

I like GPL, I use it when I can, but I don't think that it's the correct license in this situation.

Devin

--

If it's sinful, it's more fun.

Re: [Cracklib-devel] cracklib license

From: Nalin Dahyabhai <nalin@re...> - 2008-01-28 16:32

On Tue, Oct 02, 2007 at 09:57:31AM +0100, Alec Muffett wrote:

>> Seems like the ideal thing here would be for you and the other distro  
>> maintainers to get together with Alec in a conversation and come to a  
>> decision as to what licensing scheme y'all want. I haven't really done  
>> much other than cleaning up the packaging and patches and a small  
>> bit of  
>> additional

code, so whatever licensing y'all come up with is fine

>> by me.

>

> I am sympathetic. Guys, what do you reckon?

>

> What I am hearing so far is that LGPL makes sense, since it can be

> linked with any code, not just GPL...

My apologies for not chiming in in anything resembling a reasonable timeframe.

I'd also suggest the LGPL, for the reason you noted above. Alternately, GPLv2 with the option of using the library under a later version of the GPL would permit applications which were released under version 3 of the GPL to use the library, too, which would be sufficient for the packages which are included in Fedora. FWIW, I'd personally lean toward LGPL.

In any case, I thank you both for working on sorting this out.

Cheers,

Nalin

Re: [Cracklib-devel] cracklib license

From: Mike Frysinger <vapier@ge...> - 2008-10-05 21:27

Attachments: Message as HTML

On Monday 28 January 2008, Nalin Dahyabhai wrote:

> On Tue, Oct 02, 2007 at 09:57:31AM +0100, Alec Muffett wrote:

>>> Seems like the ideal thing here would be for you and the other distro  
>>> maintainers to get together with Alec in a conversation and come to a  
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>

> In any case, I thank you both for working on sorting this out.

looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make  
the change now ?

-mike

Re: [Cracklib-devel] cracklib license

From: Alec Muffett <alecm@cr...> - 2008-10-05 23:18

>> In any case, I thank you both for working on sorting this out.

>

> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make  
> the change now ?

yes. go for it. thanks++

-a

Re: [Cracklib-devel] cracklib license

From: Mike Frysinger <vapier@ge...> - 2008-10-25 22:34

Attachments: Message as HTML

On Sunday 05 October

2008, Alec Muffett wrote:

> >> In any case, I thank you both for working on sorting this out.

> >

> > looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we

> > make the change now ?

>

> yes. go for it. thanks++

Nathan Neulinger is the only one who can actually make said change ...

-mike

-----  
BELOW IS ORIGINAL LICENSING DISCUSSION RE CHANGING TO GPL from Artistic.  
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CrackLib was originally licensed with a variant of the Artistic license. In the interests of wider acceptance and more modern licensing, it was switched with the original author's blessing to GPL v2.

This approval was carried out in email discussions in 2005, and has been reconfirmed as of 2007-10-01 with the following email from Alec Muffett.

The below email references nneul@umr.edu address, as that is the address that was used at the time. For any future emails regarding this, please use nneul@neulinger.org.

-----  
From

alecm@crypticide.com Mon Oct 1 12:26:03 2007

Received: from umr-exproto2.cc.umr.edu ([131.151.0.192]) by UMR-CMAIL1.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);

Mon, 1 Oct 2007 12:26:03 -0500

Received: from scansrv2.srv.mst.edu ([131.151.1.114]) by umr-exproto2.cc.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);

Mon, 1 Oct 2007 12:26:02 -0500

Received: (qmail 8022 invoked from network); 1 Oct 2007 16:59:55 -0000

Received: from smtp1.srv.mst.edu (131.151.1.43)

by scanin-ipvs.cc.umr.edu with SMTP; 1 Oct 2007 16:59:55 -0000

Received: from spunkymail-mx8.g.dreamhost.com (mx1.spunky.mail.dreamhost.com [208.97.132.47])  
by smtp1.srv.mst.edu (8.13.1/8.13.1) with ESMTP id 191Gxtpr020623  
for <nneul@umr.edu>; Mon, 1 Oct 2007 11:59:55 -0500  
Received: from rutherford.zen.co.uk (rutherford.zen.co.uk [212.23.3.142])  
by spunkymail-mx8.g.dreamhost.com (Postfix) with ESMTP id 2C7734D311  
for <nneul@neulinger.org>; Mon, 1 Oct 2007 09:59:50 -0700 (PDT)  
Received: from  
[82.68.43.14] (helo=[192.168.1.3])  
by rutherford.zen.co.uk with esmtp (Exim 4.50)  
id 1IcOcX-0004Qt-6L  
for nneul@neulinger.org; Mon, 01 Oct 2007 16:59:49 +0000  
Mime-Version: 1.0 (Apple Message framework v752.2)  
In-Reply-To: <1b1b3fd80710010908k11dac0afp1f2dd471059ff9a4@mail.gmail.com>  
References: <1190922867.3457.147.camel@localhost.localdomain>  
<EC90713277D2BE41B7110CCD74E235CEF44F38@UMR-CMAIL1.umr.edu>  
<1b1b3fd80710010908k11dac0afp1f2dd471059ff9a4@mail.gmail.com>  
Content-Type: text/plain; charset=US-ASCII; delsp=yes; format=flowed  
Message-Id: <117A1264-F6DC-4E25-B0DD-56FBFE6E9F@crypticide.com>  
Content-Transfer-Encoding: 7bit  
From: Alec Muffett <alecm@crypticide.com>  
Subject: Re: cracklib license  
Date: Mon, 1 Oct 2007 17:59:46 +0100  
To: Nathan Neulinger <nneul@neulinger.org>  
X-Mailer: Apple Mail (2.752.2)  
X-Originating-Rutherford-IP: [82.68.43.14]  
Return-Path: alecm@crypticide.com  
X-OriginalArrivalTime: 01 Oct 2007 17:26:03.0008 (UTC) FILETIME=[2420C000:01C80450]  
Status: RO  
Content-Length:  
585  
Lines: 21

>  
> ----- Forwarded message -----  
> From: Neulinger, Nathan <nneul@umr.edu>  
> Date: Sep 27, 2007 2:58 PM  
> Subject: RE: cracklib license  
> To: alecm@crypto.dircon.co.uk  
>  
> Any chance you could write me a self-contained email stating clearly  
> that the license is being changed to GPL, so I could include that  
> email  
> in the repository and clean up the repository/tarballs? I have all the  
> original discussion, but something succinct and self contained  
> would be  
> ideal.

The license for my code in the Cracklib distribution is henceforth GPL.

Happy now? :-)

-a

# 1.133 beecrypt 4.2.1

## 1.133.1 Available under license :

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Version 2, June 1991

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```

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```
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```

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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That's all there is to it!  
I would like to thank the following people (in alphabetical order):

- Seth Arnold, for contributing to the documentation.
- Daniel Black, (former) maintainer of the Gentoo GNU/Linux BeeCrypt package.
- Jan-Rudolph Bhrmann, for helping me get started on the 64-bit multi-precision integer library.
- Luca Filipozzi, (former) maintainer/packager of BeeCrypt for Debian GNU/Linux.
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- Anibal Monsalve Salazar, (current) maintainer/packager of BeeCrypt for Debian GNU/Linux.
- Jon Sturgeon, bug hunter extraordinaire.

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- ARM Ltd, for donating a copy of "ARM Architecture Reference Manual".
- HP/Compaq, for their testdrive program, which gave me the opportunity to test and BeeCrypt on many new platforms.
- FSF France, for providing me with access to the GCC Compile Farm.
- SourceForge, for their excellent open source development platform.

Last but not least: thanks to everyone who provided bits of information, reported bugs, provided feedback, or works on including BeeCrypt in any other distros.

If I've missed anyone, it's due to oversight. Drop me a line and I'll rectify the situation as quickly as possible.

## 1.134 Im-sensors 3.4.0

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Dynamic chip feature detection (a.k.a. generic chip support) in libsensors and sensors.
- \* Mark M. Hoffman  
Many improvements to the libsensors configuration file scanner.
- \* Jean Delvare  
New libsensors API, and migration of sensors and sensord thereto.  
Many optimizations in libsensors and sensors.  
Configuration file converter.  
Rewrite of sensors-detect.

Support for multiple configuration files in libsensors.

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```
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# 1.137 wireless-tools 30.pre9

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## 1.138 dhcp 4.4.1

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\* `configure.ac`, `Makefile.am`: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.

Author: Julio Merino <jmmv@users.sourceforge.net>

\* `atf-c/ui.c`: The `format_paragraph` and `format_text` functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5cff2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

\* `atf-c++/detail/io.hpp`, `atf-c++/detail/io.cpp`, `atf-c++/detail/io_test.cpp`: These files were derived from the `file_handle`, `systembuf`, `pipe` and `pistream` classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

\* `admin/check-style.sh`,

admin/check-style-common.awk,  
admin/check-style-cpp.awk, admin/check-style-shell.awk: These files,  
except the first one, were first implemented in the Buildtool project.  
They were later adapted to be part of Boost.Process and, during that  
process, the shell script was created.

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vim: filetype=text:textwidth=75:expandtab:shiftwidth=2:softtabstop=2  
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-->

<!-- \$Id\$ -->

<!-- Generate ISC copyright comments from Docbook copyright metadata. -->

<xsl:stylesheet version="1.0"

xmlns:xsl="http://www.w3.org/1999/XSL/Transform"

xmlns:xi="http://www.w3.org/2001/XInclude"

xmlns:db="http://docbook.org/ns/docbook">

<xsl:template name="isc.copyright.format">

<xsl:param name="text"/>

<xsl:value-of select="\$isc.copyright.leader"/>

<xsl:value-of select="normalize-space(substring-before(\$text, '&#10;'))"/>

<xsl:text>&#10;</xsl:text>

<xsl:variable name="rest" select="substring-after(\$text, '&#10;')"/>

<xsl:if test="translate(\$rest, '&#9;&#32;', '')">

<xsl:call-template name="isc.copyright.format">

<xsl:with-param

name="text" select="\$rest"/>

</xsl:call-template>

</xsl:if>

</xsl:template>

<xsl:variable name="isc.copyright.text">

<xsl:text>

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</xsl:text>

</xsl:variable>

<xsl:variable name="isc.copyright">

<xsl:call-template name="isc.copyright.format">

<xsl:with-param name="text">

<xsl:for-each select="book/info/copyright | refentry/docinfo/copyright">

<xsl:text>Copyright (C) </xsl:text>



```

<xsl:call-template name="copyright.years">
 <xsl:with-param name="years" select="year"/>
</xsl:call-template>
<xsl:text> </xsl:text>
<xsl:value-of select="holder"/>
 <xsl:value-of select="$isc.copyright.breakline"/>
<xsl:text>#10;</xsl:text>
</xsl:for-each>
<xsl:value-of select="$isc.copyright.text"/>
 </xsl:with-param>
</xsl:call-template>
</xsl:variable>
</xsl:stylesheet>

<!--
- Local variables:
- mode: sgml
- End:
-->
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## 1.139 bridgeutils 1.6

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the version number 2.1.]

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To protect each distributor, we want to make it very clear that there is no warranty for the free library. Also, if the library is modified by someone else and passed on, the recipients should know that what they have is not the original version, so that the original author's reputation will not be affected by problems that might be introduced by others.

Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the

free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control

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- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any

application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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That's all there is to it!

# 1.141 libcroco 0.6.12

## 1.141.1 Available under license :

```
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```

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### Preamble

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To protect your rights, we need to make restrictions that forbid anyone to deny you these rights or to ask you to surrender the rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library, or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

Our method of protecting your rights has two steps: (1) copyright the library, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the library.

Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This

license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because

of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must

be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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# 1.142 rpm 4.14.2

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```

```
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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves,

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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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\* `configure.ac`, `Makefile.am`: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.

Author: Julio Merino <jmmv@users.sourceforge.net>

\* `atf-c/ui.c`: The `format_paragraph` and `format_text` functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5cff2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

\* `atf-c++/detail/io.hpp`, `atf-c++/detail/io.cpp`, `atf-c++/detail/io_test.cpp`: These files were derived from the `file_handle`, `systembuf`, `pipe` and `pistream` classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>



\* admin/check-style.sh,  
admin/check-style-common.awk,  
admin/check-style-cpp.awk, admin/check-style-shell.awk: These files,  
except the first one, were first implemented in the Buildtool project.  
They were later adapted to be part of Boost.Process and, during that  
process, the shell script was created.

Author: Julio Merino <jmmv84@gmail.com>

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-->

<!-- \$Id\$ -->

<!-- Generate ISC copyright comments from Docbook copyright metadata. -->

<xsl:stylesheet version="1.0"

xmlns:xsl="http://www.w3.org/1999/XSL/Transform"

xmlns:xi="http://www.w3.org/2001/XInclude"

xmlns:db="http://docbook.org/ns/docbook">

<xsl:template name="isc.copyright.format">

<xsl:param name="text"/>

<xsl:value-of select="\$isc.copyright.leader"/>

<xsl:value-of select="normalize-space(substring-before(\$text, '&#10;'))"/>

<xsl:text>&#10;</xsl:text>

<xsl:variable name="rest" select="substring-after(\$text, '&#10;')"/>

<xsl:if test="translate(\$rest, '&#9;&#32;', '')">

<xsl:call-template name="isc.copyright.format">

<xsl:with-param

name="text" select="\$rest"/>

</xsl:call-template>

</xsl:if>

</xsl:template>

<xsl:variable name="isc.copyright.text">

<xsl:text>

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</xsl:text>

</xsl:variable>

<xsl:variable name="isc.copyright">

<xsl:call-template name="isc.copyright.format">

<xsl:with-param name="text">

```
<xsl:for-each select="book/info/copyright | refentry/docinfo/copyright">
 <xsl:text>Copyright (C) </xsl:text>
 <xsl:call-template name="copyright.years">
 <xsl:with-param name="years" select="year"/>
 </xsl:call-template>
 <xsl:text> </xsl:text>
 <xsl:value-of select="holder"/>
 <xsl:value-of select="$isc.copyright.breakline"/>
 <xsl:text>
</xsl:text>
</xsl:for-each>
<xsl:value-of select="$isc.copyright.text"/>
 </xsl:with-param>
 </xsl:call-template>
</xsl:variable>
</xsl:stylesheet>
```

<!--

- Local variables:

- mode: sgml

- End:

-->

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## 1.149 i2c-tools 4.0

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include/bsd/vis.h  
man/bitstring.3bsd  
man/errc.3bsd  
man/explicit\_bzero.3bsd  
man/fgetln.3bsd  
man/fgetwln.3bsd  
man/fpurge.3bsd  
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man/wcsncpy.3bsd  
src/getbsize.c  
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man/getpeereid.3bsd  
man/pidfile.3bsd  
src/expand\_number.c  
src/hash/sha512.h  
src/hash/sha512c.c  
src/pidfile.c

src/reallocf.c

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src/fparsefn.c

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man/readpassphrase.3bsd  
man/strncpy.3bsd  
man/strtonum.3bsd  
src/arc4random.c  
src/arc4random\_linux.h  
src/arc4random\_openbsd.h  
src/arc4random\_uniform.c  
src/arc4random\_unix.h  
src/arc4random\_win.h  
src/closefrom.c  
src/getentropy\_aix.c  
src/getentropy\_bsd.c  
src/getentropy\_hpux.c  
src/getentropy\_hurd.c

src/getentropy\_linux.c  
src/getentropy\_osx.c  
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src/hash/md5.c

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src/chacha\_private.h

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## 1.151 Ivm2 2.02.180

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```
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```

<signature of Ty Coon>, 1 April 1989

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```
* -*- Mode: c; c-basic-offset: 4; indent-tabs-mode: t; tab-width: 8; -*- */
/* cairo - a vector graphics library with display and print output
*
```

```
* Copyright © 2004 Red Hat, Inc
```

```
*
```

```
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```

```
*
```

```
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```

```
*
```

```
* The Original Code is the cairo graphics library.
```

```
*
```

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* The Initial Developer of the Original Code is University of Southern
* California.
```

```
*
```

```
* Contributor(s):
```

```
* Kristian Høgsberg <krh@redhat.com>
```

```
* Carl Worth <cworth@cworth.org>
```

```
*/
```

```
/* cairo-fdr - a 'flight data recorder', a black box, for cairo
```

```
*
```



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## 1.154 icu 62

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Commands to generate dependency files
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)

Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE= -Wl,-rpath,

Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

Shared object suffix
SO = so

Non-shared intermediate object suffix
STATIC_O = ao

Compilation
rules
%. $(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%. $(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<
```

```

Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s^\($*\)\.o[:]*^1.o $@ : /g\" > $@; \
[-s $@] || rm -f $@'

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s^\($*\)\.o[:]*^1.o $@ : /g\" > $@; \
[-s $@] || rm -f $@'

Versioned libraries rules

%.${SO}.${SO_TARGET_VERSION_MAJOR}: %.${SO}.${SO_TARGET_VERSION}
$(RM) $@ && ln -s ${<F} $@
%.${SO}: %.${SO}.${SO_TARGET_VERSION_MAJOR}
$(RM) $@ && ln -s ${*F}.${SO}.${SO_TARGET_VERSION} $@

Bind internal references

LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

Dependencies [i.e. map files] for the final library
BIR_DEPS=

Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

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```

#### 4. Burmese Word Break Dictionary Data (burmesedict.txt)

```
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```

```
#
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```

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```

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```
#
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## 1.155.1 Available under license :

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Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

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This is the Debian GNU/Linux prepackaged version of the static EXT2  
file system consistency checker (e2fsck.static). The EXT2 utilities  
were written by Theodore Ts'o <tytso@mit.edu> and Remy Card  
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```

This is a Makefile stub which handles the creation of BSD shared
libraries.

In order to use this stub, the following makefile variables must be defined.

BSDLIB_VERSION = 1.0
BSDLIB_IMAGE = libce
BSDLIB_MYDIR = et
BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
```

```
all:: image
```

real-subdirs:: Makefile

@echo " MKDIR pic"

@mkdir -p pic

BSD\_LIB = \$(BSDLIB\_IMAGE).so.\$(BSDLIB\_VERSION)

BSDLIB\_PIC\_FLAG = -fpic

image: \$(BSD\_LIB)

\$(BSD\_LIB): \$(OBJS)

(cd pic; ld -Bshareable -o \$(BSD\_LIB) \$(LDFLAGS\_SHLIB) \$(OBJS))

\$(MV) pic/\$(BSD\_LIB) .

\$(RM) -f ../\$(BSD\_LIB)

(cd ..; \$(LN) \$(LINK\_BUILD\_FLAGS) \

`echo \$(my\_dir) | sed -e 's;lib/;;' /\$(BSD\_LIB) \$(BSD\_LIB))

install-shlibs install:: \$(BSD\_LIB)

@echo " INSTALL\_PROGRAM \$(BSDLIB\_INSTALL\_DIR)/\$(BSD\_LIB)"

@\$(INSTALL\_PROGRAM) \$(BSD\_LIB) \

\$(DESTDIR)\$ (BSDLIB\_INSTALL\_DIR)/\$(BSD\_LIB)

@-\$(LDCONFIG)

install-strip: install

install-shlibs-strip:: install-shlibs

uninstall-shlibs uninstall::

\$(RM) -f \$(DESTDIR)\$ (BSDLIB\_INSTALL\_DIR)/\$(BSD\_LIB)

clean::

\$(RM)

-rf pic

\$(RM) -f \$(BSD\_LIB)

\$(RM) -f ../\$(BSD\_LIB)

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Index: tdbsa/tdb.c

=====

--- tdbsa.orig/tdb.c

+++ tdbsa/tdb.c

@@ -4,11 +4,11 @@ Rev: 23371

Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)

\*/

/\*

- Unix SMB/CIFS implementation.
- + trivial database library - standalone version

- trivial database library - private includes

-

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Gadi Oxman, August 1995

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```

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Theodore Ts'o  
23-June-2007

---

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Version 2, June 1991

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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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base64.cpp and base64.h

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L. Peter Deutsch  
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\*\*\*\*\* UTF8 Validation logic (utf8\_validation.hpp) \*\*\*\*\*

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## 1.159 nfs-utils 2.3.1

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## 1.161 iputils 20180629

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## 1.164 libidn 2.0.5

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Any executables

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# 1.168 libdevmapper 2.02.180

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Upstream-Name: rdma-core

Upstream-Contact: Doug Ledford <dledford@redhat.com>,

Leon Romanovsky <Leon@kernel.org>

Source: <https://github.com/linux-rdma/rdma-core>

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Any

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Version 2, June 1991

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* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
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0.4.4/pyasn1/codec/native/decoder.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/codec/cer/__main__.py
*
/opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/tests/type/test_opentype.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/type/namedtype.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/compat/integer.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/type/useful.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/native/encoder.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/error.py
* /opt/ws_local/PERMITS_SQL/1063016395_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/ber/eoo.py
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```

ASN.1 library for Python

=====

```

.. toctree::
 :maxdepth: 1

```

Abstract Syntax Notation One (ASN.1)

[http://en.wikipedia.org/wiki/Abstract\\_Syntax\\_Notation\\_1x](http://en.wikipedia.org/wiki/Abstract_Syntax_Notation_1x)) is a technology for exchanging structured data in a universally understood, hardware agnostic way. Many industrial, security and telephony applications heavily rely on ASN.1.

The `pyasn1` <https://pypi.org/project/pyasn1/> library implements ASN.1 support in pure-Python.

What is ASN.1

-----

ASN.1 is a large, arguably over-engineered and extremely old data modelling and serialisation tool. It is probably among the first serialisation protocols in the history of computer science and technology.

ASN.1 started its life over 30 years ago as a serialisation mechanism for the first electronic mail (known as X.400). Later on it was split off the e-mail application and become a stand-alone tech still being actively supported by its designers and widely used in industry and technology.

Since then ASN.1 is sort of haunted by its relations with the OSI model -- the first, unsuccessful, version of the Internet. You can read many interesting discussions <https://news.ycombinator.com/item?id=8871453> on that topic.

In the following years, generations of software engineers tackled the serialisation problem many times. We can see that in Google's `ProtoBuffers` <https://developers.google.com/protocol-buffers/> or `FlatBuffers` <https://google.github.io/flatbuffers/>, for example. Interestingly, many new takes on binary protocol design do not depart far from ASN.1 from technical perspective. It's more of a matter of striking a balance between processing overhead, wire format overhead and human readability.

Looking at what ASN.1 has to offer, it has three loosely coupled parts:

- \* Data types: the standard introduces a collection of basic data types (integers, bits, strings, arrays and records) that can be used for describing arbitrarily complex, nested data structures.
- \* Serialisation protocols: the above data structures could be converted into a series of octets for storage or transmission over the wire as well as recovered back into their structured form. The system is fully agnostic to hardware architectures differences.
- \* Schema language: ASN.1 data structures could be described in terms of a schema language for ASN.1 compiler to turn it into platform-specific implementation.

## ASN.1 applications

-----

Being an old and generally successful standard, ASN.1 is widely adopted for many uses. To give you an example, these technologies use ASN.1 for their data exchange needs:

- \* Signaling standards for the public switched telephone network (SS7 family)
- \* Network management standards (SNMP, CMIP)
- \* Directory standards (X.500 family, LDAP)
- \* Public Key Infrastructure standards (X.509, etc.)
- \* PBX control (CSTA)
- \* IP-based Videoconferencing (H.323 family)
- \* Biometrics (BIP, CBEFF, ACBio)
- \* Intelligent transportation (SAE J2735)
- \* Cellular telephony (GSM, GPRS/EDGE, UMTS, LTE)

## ASN.1 gotchas

-----

Apparently, ASN.1 is hard to implement properly. Quality open-source ASN.1 tools are rare, but ad-hoc implementations are numerous. Judging from the `statistics <http://cve.mitre.org/cgi-bin/cvekey.cgi?keyword=ASN.1>` on discovered security vulnerabilities, many people have implemented ASN.1 parsers and oftentimes fell victim to its edge cases.

On the bright side, ASN.1 has been around for a long time, it is well understood and security reviewed.

## Documentation

-----

```
.. toctree::
 :maxdepth: 2

 /pyasn1/contents
```

## Use case

-----

```
.. toctree::
 :maxdepth: 2

 /example-use-case
```

## Download & Install



-----  
.. toctree::  
:maxdepth: 2

/download

## Changes

-----

All changes and release history is maintained in changelog. There you could also download the latest unreleased pyasn1 tarball containing the latest fixes and improvements.

..  
toctree::  
:maxdepth: 1

/changelog

## License

-----

The PyASN1 software is distributed under 2-clause BSD License.

.. toctree::  
:maxdepth: 2

/license

## Getting help

-----

Please, file your `issues` <<https://github.com/etingof/pyasn1/issues>>`\_` and `PRs` <<https://github.com/etingof/pyasn1/pulls>>`\_` at GitHub. Alternatively, you could ask for help at `Stack Overflow` <<http://stackoverflow.com/questions/tagged/pyasn1>>`\_` or search `pyasn1-users` <<https://lists.sourceforge.net/lists/listinfo/pyasn1-users>>`\_` mailing list archive.

## Books on ASN.1

-----

The pyasn1 implementation is largely based on reading up the following awesome books:

\* `ASN.1 - Communication between heterogeneous systems` <<http://www.oss.com/asn1/dubuisson.html>>`\_` by

Olivier Dubuisson

\* `ASN.1 Complete <<http://www.oss.com/asn1/resources/books-whitepapers-pubs/larmouth-asn1-book.pdf>>`\_ by Prof John Larmouth

Here you can get the official standards which is hard to read:

\* `ITU standards

<<http://www.itu.int/ITU-T/studygroups/com17/languages/X.680-X.693-0207w.zip>>`\_

On the other end of the readability spectrum, here is a quick and sweet write up:

\* `A Layman's Guide to a Subset of ASN.1, BER, and DER <<ftp://ftp.rsasecurity.com/pub/pkcs/ascii/layman.asc>>`\_ by Burton S. Kaliski

If you are working with ASN.1, we'd highly recommend reading a proper book on the subject.

Found in path(s):

\* /opt/ws\_local/PERMITS\_SQL/1063016395\_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/docs/source/contents.rst

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ASN.1 library for Python

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[![PyPI](<https://img.shields.io/pypi/v/pyasn1.svg?maxAge=2592000>)](<https://pypi.org/project/pyasn1>)  
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[![GitHub license](<https://img.shields.io/badge/license-BSD-blue.svg>)](<https://raw.githubusercontent.com/etingof/pyasn1/master/LICENSE.txt>)

This is a free and open source implementation of ASN.1 types and codecs as a Python package. It has been first written to support particular protocol (SNMP) but then generalized to be suitable for a wide range of protocols based on

[ASN.1 specification]([https://www.itu.int/rec/dologin\\_pub.asp?lang=e&id=T-REC-X.208-198811-W!!PDF-E&type=items](https://www.itu.int/rec/dologin_pub.asp?lang=e&id=T-REC-X.208-198811-W!!PDF-E&type=items)).

Features

-----

- \* Generic implementation of ASN.1 types (X.208)
- \* Standards compliant BER/CER/DER codecs
- \* Dumps/loads ASN.1 structures from Python types
- \* 100% Python, works with Python 2.4 up to Python 3.7
- \* MT-safe

\* Contributed ASN.1 compiler [Asn1ate](<https://github.com/kimgr/asn1ate>)

## Why using pyasn1

-----

ASN.1 solves the data serialisation problem. This solution was designed long ago by the wise Ancients. Back then, they did not have the luxury of wasting bits. That is why ASN.1 is designed to serialise data structures of unbounded complexity into something compact and efficient when it comes to processing the data.

That probably explains why many network protocols and file formats still rely on the 30+ years old technology. Including a number of high-profile Internet protocols and file formats.

Quite a number of books cover the topic of ASN.1.

[Communication between heterogeneous systems](<http://www.oss.com/asn1/dubuisson.html>)

by

Olivier Dubuisson is one of those high quality books freely available on the Internet.

The pyasn1 package is designed to help Python programmers tackling network protocols and file formats at the comfort of their Python prompt. The tool struggles to capture all aspects of a rather complicated ASN.1 system and to represent it on the Python terms.

## How to use pyasn1

-----

With pyasn1 you can build Python objects from ASN.1 data structures. For example, the following ASN.1 data structure:

```
```bash
Record ::= SEQUENCE {
  id    INTEGER,
  room  [0] INTEGER OPTIONAL,
  house [1] INTEGER DEFAULT 0
}
```
```

Could be expressed in pyasn1 like this:

```
```python
class Record(Sequence):
    componentType = NamedTypes(
        NamedType('id', Integer()),
        OptionalNamedType(
```

```

        'room', Integer().subtype(
            implicitTag=Tag(tagClassContext, tagFormatSimple, 0)
        )
    ),
    DefaultedNamedType(
        'house',
        Integer(0).subtype(
            implicitTag=Tag(tagClassContext, tagFormatSimple, 1)
        )
    )
)
)
)
...

```

It is in the spirit of ASN.1 to take abstract data description and turn it into a programming language specific form. Once you have your ASN.1 data structure expressed in Python, you can use it along the lines of similar Python type (e.g. ASN.1 `SET` is similar to Python `dict`, `SET OF` to `list`):

```

```python
>>> record = Record()
>>> record['id'] = 123
>>> record['room'] = 321
>>> str(record)
Record:
id=123
room=321
>>>
```

```

Part of the power of ASN.1 comes from its serialisation features. You can serialise your data structure and send it over the network.

```

```python
>>> from pyasn1.codec.der.encoder import encode
>>> substrate = encode(record)
>>> hexdump(substrate)
0000: 30 07 02 01 7B 80 02 01 41
```

```

Conversely, you can turn serialised ASN.1 content, as received from network or read from a file, into a Python object which you can introspect, modify, encode and send back.

```

```python
>>> from pyasn1.codec.der.decoder import decode
>>> received_record, rest_of_substrate = decode(substrate, asn1Spec=Record())

```

```

>>>
>>> for field in received_record:
>>> print('{} is {}'.format(field, received_record[field]))
id is 123
room is 321
house is 0
>>>
>>> record == received_record
True
>>> received_record.update(room=123)
>>> substrate = encode(received_record)
>>> hexdump(substrate)
00000: 30 06 02 01 7B 80 01 7B
...

```

The pyasn1 classes struggle to emulate their Python prototypes (e.g. int, list, dict etc.). But ASN.1 types exhibit more complicated behaviour. To make life easier for a Pythonista, they can turn their pyasn1 classes into Python built-ins:

```

```python
>>> from pyasn1.codec.native.encoder import encode
>>> encode(record)
{'id': 123, 'room': 321, 'house': 0}
...

```

Or vice-versa -- you can initialize an ASN.1 structure from a tree of Python objects:

```

```python
>>> from pyasn1.codec.native.decoder import decode
>>> record = decode({'id':
123, 'room': 321, 'house': 0}, asn1Spec=Record())
>>> str(record)
Record:
id=123
room=321
>>>
...

```

With ASN.1 design, serialisation codecs are decoupled from data objects, so you could turn every single ASN.1 object into many different serialised forms. As of this moment, pyasn1 supports BER, DER, CER and Python built-ins codecs. The extremely compact PER encoding is expected to be introduced in the upcoming pyasn1 release.

More information on pyasn1 APIs can be found in the [documentation](<http://snmplabs.com/pyasn1/>),

compiled ASN.1 modules for different protocols and file formats  
could be found in the pyasn1-modules  
[repo](<https://github.com/etingof/pyasn1-modules>).

How to get pyasn1

-----

The pyasn1 package is distributed under terms and conditions of 2-clause  
BSD [license](<http://snmplabs.com/pyasn1/license.html>). Source code is freely  
available as a GitHub [repo](<https://github.com/etingof/pyasn1>).

You could `pip install pyasn1` or download it from [PyPI](<https://pypi.org/project/pyasn1>).

If

something does not work as expected,  
[open an issue](<https://github.com/etingof/pyasn1/issues>) at GitHub or  
post your question [on Stack Overflow](<https://stackoverflow.com/questions/ask>)  
or try browsing pyasn1  
[mailing list archives](<https://sourceforge.net/p/pyasn1/mailman/pyasn1-users/>).

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\* /opt/ws\_local/PERMITS\_SQL/1063016395\_1592949447.38/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/README.md

# 1.180 pycparser 2.18

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pycparser -- A C parser in Python

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    maintenance

44. [48]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de>  
    monitoring/trap scripts, statistics file handling
45. [49]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
46. [50]Wilfredo Snchez <wsanchez@apple.com> added support for NetInfo
47. [51]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
48. [52]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of  
    space on the stuff in the html/pic/ subdirectory
49. [53]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
50. [54]Michael Shields <shields@tembel.org> USNO clock driver
51. [55]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
52. [56]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure  
    makeover, various other bits (see the ChangeLog)
53. [57]Kenneth Stone <ken@sdd.hp.com> HP-UX port
54. [58]Ajit Thyagarajan <ajit@ee.udel.edu> IP multicast/anycast support
55. [59]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp> TRAK  
clock driver
56. [60]Brian Utterback <brian.utterback@oracle.com> General codebase,  
    Solaris issues
57. [61]Loganaden Velvindron <loganaden@gmail.com> Sandboxing  
    (libseccomp) support
58. [62]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic  
    TrueTime clock driver
59. [63]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and  
    validated HTML documents according to the HTML DTD

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## References

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```
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```



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## 1.183 ipmi-tool 1.8.18

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## 1.184 kerberos 1.16.1

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cmd/krb5/ipropl/ipropl.x
cmd/krb5/ipropl/ipropl_hdr.h
cmd/krb5/kadmin/server/ipropl_svc.c
cmd/krb5/kproplog/kproplog.c
cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
lib/libgss/g_canon_name.c
lib/libgss/g_compare_name.c
lib/libgss/g_context_time.c
lib/libgss/g_delete_sec_context.c
```

lib/libgss/g\_dsp\_name.c  
lib/libgss/g\_dsp\_status.c  
lib/libgss/g\_dup\_name.c  
lib/libgss/g\_exp\_sec\_context.c  
lib/libgss/g\_export\_name.c  
lib/libgss/g\_glue.c  
lib/libgss/g\_imp\_name.c  
lib/libgss/g\_imp\_sec\_context.c  
lib/libgss/g\_init\_sec\_context.c  
lib/libgss/g\_initialize.c  
lib/libgss/g\_inquire\_context.c  
lib/libgss/g\_inquire\_cred.c  
lib/libgss/g\_inquire\_names.c  
lib/libgss/g\_process\_context.c  
lib/libgss/g\_rel\_buffer.c  
lib/libgss/g\_rel\_cred.c  
lib/libgss/g\_rel\_name.c  
lib/libgss/g\_rel\_oid\_set.c  
lib/libgss/g\_seal.c  
lib/libgss/g\_sign.c  
lib/libgss/g\_store\_cred.c  
lib/libgss/g\_unseal.c  
lib/libgss/g\_userok.c  
lib/libgss/g\_utils.c  
lib/libgss/g\_verify.c  
lib/libgss/gssd\_pname\_to\_uid.c  
uts/common/gssapi/include/gssapi\_err\_generic.h  
uts/common/gssapi/include/mechglueP.h

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```
/* -*- mode: c; c-basic-offset: 4; indent-tabs-mode: nil -*- */
```

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/* tests/shlib/t_loader.c */
```

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/*
```

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in "src/lib/gssapi", including the following files:

lib/gssapi/generic/gssapi\_err\_generic.et  
lib/gssapi/mechglue/g\_accept\_sec\_context.c  
lib/gssapi/mechglue/g\_acquire\_cred.c  
lib/gssapi/mechglue/g\_canon\_name.c  
lib/gssapi/mechglue/g\_compare\_name.c  
lib/gssapi/mechglue/g\_context\_time.c  
lib/gssapi/mechglue/g\_delete\_sec\_context.c  
lib/gssapi/mechglue/g\_dsp\_name.c  
lib/gssapi/mechglue/g\_dsp\_status.c

lib/gssapi/mechglue/g\_dup\_name.c  
lib/gssapi/mechglue/g\_exp\_sec\_context.c  
lib/gssapi/mechglue/g\_export\_name.c  
lib/gssapi/mechglue/g\_glue.c  
lib/gssapi/mechglue/g\_imp\_name.c  
lib/gssapi/mechglue/g\_imp\_sec\_context.c  
lib/gssapi/mechglue/g\_init\_sec\_context.c  
lib/gssapi/mechglue/g\_initialize.c  
lib/gssapi/mechglue/g\_inquire\_context.c  
lib/gssapi/mechglue/g\_inquire\_cred.c  
lib/gssapi/mechglue/g\_inquire\_names.c  
lib/gssapi/mechglue/g\_process\_context.c  
lib/gssapi/mechglue/g\_rel\_buffer.c  
lib/gssapi/mechglue/g\_rel\_cred.c  
lib/gssapi/mechglue/g\_rel\_name.c  
lib/gssapi/mechglue/g\_rel\_oid\_set.c  
lib/gssapi/mechglue/g\_seal.c  
lib/gssapi/mechglue/g\_sign.c  
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lib/gssapi/mechglue/g\_unseal.c  
lib/gssapi/mechglue/g\_userok.c  
lib/gssapi/mechglue/g\_utils.c  
lib/gssapi/mechglue/g\_verify.c  
lib/gssapi/mechglue/gssd\_pname\_to\_uid.c  
lib/gssapi/mechglue/mglueP.h  
lib/gssapi/mechglue/oid\_ops.c  
lib/gssapi/spnego/gssapiP\_spnego.h  
lib/gssapi/spnego/spnego\_mech.c

and the initial implementation of incremental propagation,  
including  
the following new or changed files:

include/iprop\_hdr.h  
kadmin/server/ipropd\_svc.c  
lib/kdb/iprop.x  
lib/kdb/kdb\_convert.c  
lib/kdb/kdb\_log.c  
lib/kdb/kdb\_log.h  
lib/krb5/error\_tables/kdb5\_err.et  
kprop/kpropd\_rpc.c  
kprop/kproplog.c

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# 1.185 syslog-ng 3.16.1

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\* Detect unordered\_map in stl\_hash.m4.

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\* gzip/zlib I/O support.

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\* Fixed warnings about generated constructors not explicitly initializing all fields (only present with certain compiler settings).  
\* Added generation of field number constants.

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\* Small patch improving performance of in Python serialization.

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\* Emacs mode for Protocol Buffers (editors/protobuf-mode.el).

Scott Stafford <scott.stafford@gmail.com>  
\* Added Swap(), SwapElements(), and RemoveLast() to Reflection interface.

Alexander Melnikov <alm@sibmail.ru>

- \* HPUX support.

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- \* Detect whether zlib is new enough in configure script.
- \* Fixes for Solaris 10 32/64-bit confusion.

Evan Jones <evanj@mit.edu>

- \* Optimize Java serialization code when writing a small message to a stream.
- \* Optimize Java serialization of strings so that UTF-8 encoding happens only once per string per serialization call.
- \* Clean up some Java warnings.
- \* Fix bug with permanent callbacks that delete themselves when run.

Michael Kucharski <m.kucharski@gmail.com>

- \* Added CodedInputStream.getTotalBytesRead().

Kacper Kowalik <xarthisius.kk@gmail.com>

- \* Fixed m4/acx\_pthread.m4 problem for some Linux distributions.

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- \* Fixed detection of sched\_yield on Solaris.
- \* Added atomicops for Solaris

Andrew Paprocki <andrew@ishiboo.com>

- \* Fixed minor IBM xLC compiler build issues
- \* Added atomicops for AIX (POWER)

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\*

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## 1.192 traceroute 2.1.0-r0

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```
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```

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```
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```

```
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/\* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)

\* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)

\*

\* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.

\* <http://www.hypermall.com/>

\* 10/1/97 - commented out CFG\_PHYIE bit - we don't care when the PHY

\* interrupts us (except possibly for removal/insertion of the cable?)

\* 10/4/97 - began heavy inline documentation of the code. Corrected typos

\* and spelling mistakes.

\* 10/5/97 - added code to handle PHY interrupts, disable PHY on

\* loss of link, and correctly re-enable PHY when link is

\* re-established. (put back CFG\_PHYIE)

\*

\* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.

\*

\* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997  
\*  
\* Linux driver for the IDT77201 NICStAR PCI ATM controller.  
\* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;  
\* see init\_nicstar() for PHY initialization to change this. This driver  
\* expects the Linux ATM stack to support scatter-gather  
lists  
\* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.  
\*  
\* Implementing minimal-copy of received data:  
\* IDT always receives data into a small buffer, then large buffers  
\* as needed. This means that data must always be copied to create  
\* the linear buffer needed by most non-ATM protocol stacks (e.g. IP)  
\* Fix is simple: make large buffers large enough to hold entire  
\* SDU, and leave <small\_buffer\_data> bytes empty at the start. Then  
\* copy small buffer contents to head of large buffer.  
\* Trick is to avoid fragmenting Linux, due to need for a lot of large  
\* buffers. This is done by 2 things:  
\* 1) skb->destructor / skb->atm.recycle\_buffer  
\* combined, allow nicstar\_free\_rx\_skb to be called to  
\* recycle large data buffers  
\* 2) skb\_clone of received buffers  
\* See nicstar\_free\_rx\_skb and linearize\_buffer for implementation  
\* details.  
\*  
\*  
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\*  
\* M. Welsh, 6 July 1996  
\*  
\*  
\*/

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

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```
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*/
```

Found in path(s):

```
* /opt/cola/permits/1102738801_1610534739.81/0/multipart-parser-c-master-3-zip/multipart-parser-c-master/multipart_parser.c
* /opt/cola/permits/1102738801_1610534739.81/0/multipart-parser-c-master-3-zip/multipart-parser-c-master/multipart_parser.h
```



# 1.200 net-snmp 5.8.0

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\* Constructor.

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\* @param

exchange

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\*/

Found in path(s):

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/engine/resource/MethodAnnotationInfo.java

\*

/opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/engine/util/StringUtils.java

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/representation/DigesterRepresentation.java

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/representation/AppendableRepresentation.java

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/engine/local/ZipClientHelper.java

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*/
/** The confidentiality. */
/**
 * Constructor.
 *
 * @param schemeName
 * The scheme name.
 * @param name
 * The unique name.
 * @param description
 * The description.
 * @param defaultPort
 * The default port.
 * @param confidential
 * The confidentiality.
 */
/**
 * Indicates if the protocol guarantees the confidentiality of the messages
 * exchanged, for example via a SSL-secured connection.
 *
 * @return True if the protocol is confidential.
 */

```

Found in path(s):

```
* /opt/cola/permits/1122075426_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/Protocol.java
```

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- \* <https://restlet.talend.com/>
- \*
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- \*/

Found in path(s):

- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/DispositionReader.java
- \*
- /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/ExpectationWriter.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/util/DateUtils.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/util/SystemUtils.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/resource/Put.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/local/Entity.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/DateWriter.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/service/DecoderService.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/routing/Validator.java
- \*
- /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/representation/ReaderRepresentation.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/routing/Template.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/application/Decoder.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/application/CorsFilter.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/PreferenceWriter.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/ssl/SsIUtils.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/representation/CharacterRepresentation.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/StringWriter.java
- \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/CompositeHelper.java
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/opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/security/Authenticator.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/header/ChallengeRequestReader.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/service/ConverterService.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/Client.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/io/ReadableSelectionChannel.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/util/ChildContext.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/application/Conneg.java  
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 jar/org/restlet/engine/header/TagWriter.java  
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 jar/org/restlet/engine/resource/VariantInfo.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/resource/Result.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/application/StatusInfo.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/Tag.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/routing/Router.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/io/WriterOutputStream.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/Context.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/util/ImmutableDate.java  
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 /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/Range.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/Dimension.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/representation/StreamRepresentation.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/routing/VirtualHost.java  
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 jar/org/restlet/engine/util/WrapperScheduledExecutorService.java  
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 jar/org/restlet/engine/component/ComponentClientDispatcher.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/local/LocalClientHelper.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/Expectation.java  
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 jar/org/restlet/engine/io/ReaderInputStream.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/header/HeaderConstants.java

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/local/ClapClientHelper.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/representation/ReadableRepresentation.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/Application.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/service/ConnectorService.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/representation/OutputRepresentation.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/local/FileEntity.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/connector/HttpServerHelper.java

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/Response.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/io/UnclosableOutputStream.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/security/AuthenticatorHelper.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/CookieWriter.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/CacheDirective.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/CookieSettingWriter.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/util/SelectionRegistration.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/security/Enroler.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/ContentTypeReader.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-

jar/org/restlet/engine/header/RangeWriter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/security/AuthenticatorUtils.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/component/ServerRouter.java  
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\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/connector/ConnectorHelper.java  
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\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/adapter/ServerAdapter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/ClientInfo.java  
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/opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/CharacterSet.java  
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\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/RecipientInfoWriter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/Edition.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/Reference.java

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/util/ServerList.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/util/RouteList.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/Engine.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/RecipientInfo.java  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
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 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/application/RangeRepresentation.java  
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 jar/org/restlet/engine/connector/HttpProtocolHelper.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/header/CookieSettingReader.java  
 \*  
 /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/security/Authorizer.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/representation/StringRepresentation.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/Language.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/ssl/WrapperSslServerSocketFactory.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/routing/Redirector.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/security/HttpBasicHelper.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/util/ListUtils.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/util/FormUtils.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/connector/ConnectionClosingRepresentation.java  
 \*  
 /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/io/NbChannelOutputStream.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/engine/ssl/WrapperSslSocketFactory.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
 jar/org/restlet/service/MetadataService.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/Cookie.java  
 \* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/util/ReadingListener.java

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/HeaderUtils.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/resource/Options.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/resource/Get.java  
\*  
/opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/resource/ClientInvocationHandler.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/CacheDirectiveWriter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/connector/URLConnectionCall.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/ssl/DefaultSslContext.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/log/LogFilter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/header/EncodingWriter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/component/ComponentHelper.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/util/MapResolver.java  
\*  
/opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/data/ServerInfo.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/converter/DefaultConverter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/service/EncoderService.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/adapter/ClientAdapter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/log/LoggingThreadFactory.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/log/DefaultAccessLogFormatter.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/service/ConnegService.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/util/SetUtils.java  
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/\*\*

\* Indicates if the call came over a confidential channel  
such as an  
\* SSL-secured connection.  
\*  
\* @return True if the call came over a confidential channel.  
\*/

Found in path(s):

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/util/WrapperRequest.java  
\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/util/WrapperResponse.java

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```
*/
/** Indicates if the call is confidential. */
/**
 * Indicates
if the confidentiality of the call is ensured (ex: via SSL).
 *
 * @return True if the confidentiality of the call is ensured (ex: via SSL).
 */
/**
 * Indicates if the confidentiality of the call is ensured (ex: via SSL).
 *
 * @param confidential
 * True if the confidentiality of the call is ensured (ex: via
 * SSL).
 */
```

Found in path(s):

```
*/opt/cola/permits/1122075426_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/engine/adapters/Call.java
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```

```
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 */
/**
 * Optimized public-domain implementation of a Java alphanumeric
 * sort.
 * <p>
 */
```

\* This implementation uses a single comparison pass over the characters in a  
\* CharSequence, and returns as soon as a differing character is found, unless  
\* the difference occurs in a series of numeric characters, in which case that  
\* series is followed to its end. Numeric series of equal length are compared  
\* numerically, that is, according to the most significant (leftmost) differing  
\* digit. Series of unequal length are compared by their length.

\* <p>

\*

\* This implementation appears to be 2-5 times faster than alphanumeric  
\* comparators based based on substring analysis, with a lighter memory  
\* footprint.

\* <p>

\*

\* This alphanumeric comparator has approximately 20%-50% the performance of the  
\* lexical String.compareTo() operation. Character sequences without numeric  
\* data are compared more quickly.

\* <p>

\*

\* Dedicated to the public domain by the original author:

\* <http://creativecommons.org/licenses/publicdomain/>

\*

\* @author Rob Heittman,

\* <a href="http://www.solertium.com">Solertium

\* Corporation</a>

\*/

Found in path(s):

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/engine/util/AlphaNumericComparator.java

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*
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*/
/**
* Application service capable of running and scheduling
* tasks asynchronously.
* The service instance returned will not invoke the runnable task in the
* current thread.

*

* In addition to allowing pooling, this method will ensure that the threads
* executing the tasks will have the thread local variables copied from the
* calling thread. This will ensure that call to static methods like
* { @link Application#getCurrent()} still work.

*

* Also, note that this executor service will be shared among all Restlets and
* Resources that are part of your context. In general this context corresponds
* to a parent Application's context. If you want to have your own service
* instance, you can use the { @link TaskService#wrap(ScheduledExecutorService)}
* method to ensure that thread local variables are correctly set.
*
* @author Jerome Louvel
* @author Doug Lea (docs of ExecutorService in public domain)
* @author Tim Peierls
*/
```

Found in path(s):

```
* /opt/cola/permits/1122075426_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/service/TaskService.java
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```

```
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```

```

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*/
/**
 * Sets the reference comparator based on the more friendly
 "Alphanum
 * Algorithm" created by David Koelle. The internal implementation used is
 * based on an optimized public domain implementation provided by Rob
 * Heittman from the Solertium Corporation.
 *
 * @see The original
 * Alphanum Algorithm from David Koelle
 * @see #setComparator(Comparator)
 */

```

Found in path(s):

```

* /opt/cola/permits/1122075426_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/resource/Directory.java
No license file was found, but licenses were detected in source scan.

```

```

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*/
/**
 * Implemented based on the { @link Protocol#isConfidential() }

```

method for the

```
* request's protocol returned by {@link #getProtocol()};
*/
```

Found in path(s):

```
* /opt/cola/permits/1122075426_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/Request.java
```

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```
/**
```

```
* Copyright 2005-2020 Talend
```

```
*
```

```
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```

```
*
```

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```
*
```

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* You can obtain a copy of the EPL 1.0 license at
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* http://www.opensource.org/licenses/eclipse-1.0
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```
*
```

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*
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```

```
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```

```
*
```

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```

```
*/
```

```
/**
```

```
* Indicates if this service is acting in HTTP or HTTPS
```

```
mode.
```

```
*
```

```
* @param confidential
```

```
* True if this service is acting in HTTP or HTTPS mode.
```

```
*/
```

Found in path(s):

```
* /opt/cola/permits/1122075426_1629910343.42/0/org-restlet-2-4-3-sources-
```

```
jar/org/restlet/engine/connector/NetServerHelper.java
```

No license file was found, but licenses were detected in source scan.

```
/**
```

```
* Copyright 2005-2020 Talend
```

```
*
```

```
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Found in path(s):

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/engine/adapters/ServerCall.java

\*

/opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-  
jar/org/restlet/engine/resource/AnnotationUtils.java

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```
*
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*/
/**
 * Indicates if the message was or will be exchanged
 confidentially, for
 * example via a SSL-secured connection.
 *
 * @return True if the message is confidential.
 * @see Request#isConfidential()
 */
```

Found in path(s):

```
*/opt/cola/permits/1122075426_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/resource/Resource.java
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```

```
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 */
/**
 * Indicates if the message was or will be exchanged
 confidentially, for
 * example via a SSL-secured connection.
 *
 * @return True if the message is confidential.
 */
```

Found in path(s):

\* /opt/cola/permits/1122075426\_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/Message.java

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/\*\*

\* Resolves a name into a value. By default, the { @link #createResolver(Map)}

\* static method can adapt a Java map into a resolver. Another useful method is

\* { @link #createResolver(Request, Response)}, which can expose a Restlet call

\* into a compact data model, with the following variables:

\*

\* <table>

\* <tr>

\* <th>Model property</th>

\* <th>Variable name</th>

\* <th>Content type</th>

\* </tr>

\* <tr>

\* <td>request.confidential</td>

\* <td>c</td>

\* <td>boolean (true|false)</td>

\* </tr>

\* <tr>

\* <td>request.clientInfo.address</td>

\* <td>cia</td>

\* <td>String</td>

\* </tr>

```

* <tr>
* <td>request.clientInfo.upstreamAddress</td>
* <td>ciua</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.clientInfo.agent</td>
* <td>cig</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.challengeResponse.identifier</td>
* <td>cri</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.challengeResponse.scheme</td>
* <td>crs</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.date</td>
* <td>d</td>
* <td>Date (HTTP format)</td>
* </tr>
* <tr>
* <td>request.entity.characterSet</td>
* <td>ecs</td>
* <td>String</td>
* </tr>
* <tr>
* <td>response.entity.characterSet</td>
* <td>ECS</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.entity.encoding</td>
* <td>ee</td>
* <td>String</td>
* </tr>
* <tr>
* <td>response.entity.encoding</td>
* <td>EE</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.entity.expirationDate</td>
* <td>eed</td>

```

```

* <td>Date (HTTP format)</td>
* </tr>
* <tr>
* <td>response.entity.expirationDate</td>
* <td>EED</td>
* <td>Date (HTTP format)</td>
* </tr>
* <tr>
* <td>request.entity.language</td>
* <td>el</td>
* <td>String</td>
* </tr>
* <tr>
* <td>response.entity.language</td>
* <td>EL</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.entity.modificationDate</td>
* <td>emd</td>
* <td>Date (HTTP format)</td>
* </tr>
* <tr>
* <td>response.entity.modificationDate</td>
* <td>EMD</td>
* <td>Date (HTTP format)</td>
* </tr>
* <tr>
* <td>request.entity.mediaType</td>
* <td>emt</td>
* <td>String</td>
* </tr>
* <tr>
* <td>response.entity.mediaType</td>
* <td>EMT</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.entity.size</td>
* <td>es</td>
* <td>Integer</td>
* </tr>
* <tr>
* <td>response.entity.size</td>
* <td>ES</td>
* <td>Integer</td>
* </tr>
* <tr>

```



```

* <td>request.entity.tag</td>
* <td>et</td>
* <td>String</td>
* </tr>
* <tr>
* <td>response.entity.tag</td>
* <td>ET</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.referrerRef</td>
* <td>f*</td>
* <td>Reference (see table below variable name sub-parts)</td>
* </tr>
* <tr>
* <td>request.hostRef</td>
* <td>h*</td>
* <td>Reference (see table below variable name sub-parts)</td>
* </tr>
* <tr>
* <td>request.method</td>
* <td>m</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.rootRef</td>
* <td>o*</td>
* <td>Reference (see table below variable name sub-parts)</td>
* </tr>
* <tr>
* <td>request.protocol</td>
* <td>p</td>
* <td>String</td>
* </tr>
* <tr>
* <td>request.resourceRef</td>
* <td>r*</td>
* <td>Reference (see table below variable name sub-parts)</td>
* </tr>
* <tr>
* <td>response.redirectRef</td>
* <td>R*</td>
* <td>Reference (see table below variable name sub-parts)</td>
* </tr>
* <tr>
* <td>response.status</td>
* <td>S</td>
* <td>Integer</td>

```

```

* </tr>
* <tr>
* <td>response.serverInfo.address</td>
* <td>SIA</td>
* <td>String</td>
* </tr>
* <tr>
* <td>response.serverInfo.agent</td>
* <td>SIG</td>
* <td>String</td>
* </tr>
* <tr>
* <td>response.serverInfo.port</td>
* <td>SIP</td>
* <td>Integer</td>
* </tr>
* </table>
*

*

```

\* Below is the list of name sub-parts, for Reference variables, that can  
 \* replace the asterix in the variable names above:<br>

```

*

*
* <table>
* <tr>
* <th>Reference property</th>
* <th>Sub-part name</th>
* <th>Content type</th>
* </tr>
* <tr>
* <td>authority</td>
* <td>a</td>
* <td>String</td>
* </tr>
* <tr>
* <td>baseRef</td>
* <td>b*</td>
* <td>Reference</td>
* </tr>
* <tr>
* <td>targetRef</td>
* <td>t*</td>
* <td>Reference</td>
* </tr>
* <tr>
* <td>relativePart</td>
* <td>e</td>
* <td>String</td>

```

```

* </tr>
* <tr>
* <td>fragment</td>
* <td>f</td>
* <td>String</td>
* </tr>
* <tr>
* <td>hostIdentifier</td>
* <td>h</td>
* <td>String</td>
* </tr>
* <tr>
* <td>identifier</td>
* <td>i</td>
* <td>String</td>
* </tr>
* <tr>
* <td>path</td>
* <td>p</td>
* <td>String</td>
* </tr>
* <tr>
* <td>query</td>
* <td>q</td>
* <td>String</td>
* </tr>
* <tr>
* <td>remainingPart</td>
* <td>r</td>
* <td>String</td>
* </tr>
* </table>
*
* @author Jerome Louvel
*/

```

Found in path(s):

```

* /opt/cola/permits/1122075426_1629910343.42/0/org-restlet-2-4-3-sources-jar/org/restlet/util/Resolver.java

```

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\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/castor/CastorEnumTypeDeserializerFactory.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/wsdl/toJava/NamespaceSelector.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/castor/CastorEnumTypeSerializer.java  
\*  
/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/wsdl/toJava/FactoryProperty.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/castor/CastorEnumTypeSerializerFactory.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/SimpleListSerializerFactory.java  
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jar/org/apache/axis/encoding/ser/castor/CastorSerializer.java
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jar/org/apache/axis/deployment/wsdd/WSDDConstants.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/ArraySerializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/ArraySerializerFactory.java  
\*

/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/deployment/wsdd/WSDDService.java

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\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/SimpleListSerializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/castor/CastorEnumTypeDeserializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/ser/SimpleListDeserializer.java  
\*

/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/MixedContentType.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/SimpleListDeserializerFactory.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/AnyContentType.java  
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\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-

jar/org/apache/axis/deployment/wsdd/WSDDArrayMapping.java  
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDBeanMapping.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/javax/xml/rpc/server/ServletEndpointContext.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/TypeMapping.java
*
```



/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/Handler.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/handler/HandlerChain.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/PlainTextDataSource.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/HexSerializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/InputStreamBody.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/symbolTable/BackslashUtil.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/enum/Style.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/symbolTable/SymTabEntry.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/VectorDeserializerFactory.java  
\*  
/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/providers/WSDComProvider.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/FactoryFinder.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/PlainTextDataHandlerDeserializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/EnumSerializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/handler/soap/SOAPMessageContext.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/configuration/XMLStringProvider.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPPart.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/security/servlet/ServletSecurityProvider.java  
\*  
/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/security/servlet/ServletAuthenticatedUser.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/constants/Style.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/TypeMappingImpl.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/MapSerializerFactory.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/HexBinary.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/MapDeserializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/ArrayDeserializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/gen/NoopFactory.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/VectorDeserializer.java  
\*

```

/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/script/BSF.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/types/NonPositiveInteger.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/toJava/JavaInterfaceWriter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/http/HTTPConstants.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/uuid/UUIDGenFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/MapDeserializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/session/SimpleSession.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPElement.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/local/LocalResponder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/handlers/JAXRPCHandler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/JAFDataHandlerDeserializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/encoding/AbstractXMLEncoder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/VectorSerializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utills/Token.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/symbolTable/CollectionTE.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/security/simple/SimpleSecurityProvider.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/holders/LongHolder.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/toJava/JavaBeanWriter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/UnsignedByte.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/TimeSerializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/attachments/OctetStreamDataSource.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/client/AxisClientProxy.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/NMToken.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/mail/MailConstants.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/encoding/Deserializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/SerializationContext.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/handlers/SimpleSessionHandler.java

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\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/http/AxisHttpSession.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/DeserializerImpl.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/providers/ComProvider.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/Version.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/cache/JavaClass.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/local/LocalTransport.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/Detail.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/net/DefaultHTTPTransportClientProperties.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/JavaBeanFaultWriter.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/WSDDUndeployment.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/Message.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPElementFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/TimeSerializerFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/security/simple/SimpleAuthenticatedUser.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/JavaServiceWriter.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/RPCHeaderParam.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/server/ParamList.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/FaultableHandler.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/providers/WSDDJavaRPCProvider.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/holders/CalendarHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/compiler/AbstractCompiler.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/handler/HandlerInfo.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/constants/Enum.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/symbolTable/ElementDecl.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/JavaServiceIfaceWriter.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/AxisServiceConfig.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/MimeMultipartHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/BoundaryDelimitedStream.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/session/Session.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utills/cache/MethodCache.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPException.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utills/Options.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/EnumDeserializerFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/Attachments.java  
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 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/handlers/HandlerInfoChainFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/symbolTable/BindingEntry.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/client/async/Status.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/NegativeInteger.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/EnumDeserializer.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/DimeBodyPart.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/client/Stub.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utills/IDKey.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/ElementSerializerFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/DocumentDeserializer.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/constants/Use.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/HexSerializerFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/holders/FloatWrapperHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utills/BeanUtils.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/schema/SchemaVersion2000.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/ManagedMemoryDataSource.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/i18n/MessageBundle.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/collections/LRUMap.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/CalendarSerializer.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/compiler/Jikes.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/holders/DoubleHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/net/BooleanHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-

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jar/org/apache/axis/wsdl/symbolTable/ContainedAttribute.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/NodeImpl.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPConstants.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/Base64DeserializerFactory.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Schema.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/Admin.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/PositiveInteger.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDProvider.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/Base64.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/SOAPPart.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/javax/xml/rpc/holders/BigIntegerHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/FactoryFinder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/symbolTable/UndefinedType.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/holders/BooleanHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/providers/WSDDBsfProvider.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/fromJava/Namespace.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/UnsignedInt.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/client/async/AsyncCall.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/NodeListImpl.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/uuid/SimpleUUIDGen.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/client/ServiceFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/SerializerFactory.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPFaultElement.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/QNameSerializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/utils/DefaultEntityResolver.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/Service.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/TypeMappingRegistryImpl.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/net/SunFakeTrustSocketFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/local/Handler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/compiler/Compiler.java
*

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/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/mail/MailTransport.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPMessage.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/http/SocketInputStream.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/Text.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDRequestFlow.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/encoding/Serializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/Call.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/toJava/JavaImplWriter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/monitor/SOAPMonitorConstants.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/configuration/NullProvider.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/encoding/UTF16Encoder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/BeanSerializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/symbolTable/BaseType.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/handler/Handler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/net/IBMJSSESocketFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/image/MerlinIO.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/toJava/JavaServiceImplWriter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/toJava/JavaDefinitionWriter.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/holders/FloatHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/Stub.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utills/WSDLUtils.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/NCName.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPFault.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/encoding/EncodedByteArray.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/message/NamedNodeMapImpl.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/SimpleDeserializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/message/SAX2EventRecorder.java
*

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/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/NonNegativeInteger.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/schema/SchemaVersion1999.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/DateSerializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/WSDDHandler.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/DateDeserializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/UnsignedShort.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/JAFDataHandlerSerializerFactory.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/SkeletonImpl.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/http/QSMethodHandler.java  
\*  
/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/client/async/AsyncResult.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/ElementDeserializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/http/SimpleAxisWorker.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/handlers/http/HTTPAuthHandler.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/EnvelopeHandler.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/soap/SOAPFaultException.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/i18n/Messages.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/description/ParameterDesc.java  
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/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/symbolTable/TypeEntry.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/CalendarDeserializerFactory.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/providers/BasicProvider.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/image/JDK13IO.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ConstructorTarget.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/http/FilterPrintWriter.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/OctetStream.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-

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jar/org/apache/axis/encoding/ser/DateSerializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/message/SOAPEnvelope.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/SimpleSerializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/TimeDeserializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/messaging/Endpoint.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/ArrayUtil.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/MessageFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/message/SOAPFaultDetailsBuilder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/handlers/DebugHandler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDTransport.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/message/MessageWithAttachments.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/symbolTable/Undefined.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/net/DefaultHTTPSTransportClientProperties.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/security/AuthenticatedUser.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/MimeUtils.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/Emitter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/QNameSerializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/ServiceException.java
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/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDElement.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/toJava/JavaTypeWriter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/fromJava/Emitter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/ImageDataHandlerSerializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/toJava/JavaHolderWriter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/uuid/FastUUIDGen.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/attachments/DynamicContentDataHandler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-

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jar/org/apache/axis/deployment/wsdd/WSDDGlobalConfiguration.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPHeader.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDJAXRPCHandlerInfo.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/toJava/JavaClassWriter.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/DetailEntry.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/PlainTextDataHandlerSerializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/server/ServiceLifecycle.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/schema/SchemaVersion.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/MethodTarget.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/gen/WSDL2.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/attachments/DimeMultiPart.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/http/CommonsHTTPSender.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/XMLType.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/symbolTable/CollectionType.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDResponseFlow.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/wsdl/symbolTable/PortEntry.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/NoEndPointException.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/MessageContext.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/AxisFault.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/providers/BSFProvider.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/handlers/BasicHandler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/handlers/LogHandler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/attachments/DimeDelimitedInputStream.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDTypeMappingContainer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/mail/MailServer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/IDRefs.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/client/Call.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/utils/FieldPropertyDescriptor.java
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/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/attachments/MultiPartDimeInputStream.java

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\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/wsdl/symbolTable/DefinedElement.java  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/SimpleType.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/SOAPFault.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/MimeHeader.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/NSStack.java  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/Utils/tcpmon.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/Java2WSDL.java  
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 jar/org/apache/axis/configuration/FileProvider.java  
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 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/ServiceFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/ParameterMode.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/encoding/ser/castor/CastorDeserializer.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/Utils/DOM2Writer.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/BodyBuilder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/providers/java/JavaProvider.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/JavaTestCaseWriter.java  
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 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Language.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/encoding/DefaultXMLEncoder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/i18n/ProjectResourceBundle.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/symbolTable/SymbolTable.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/SourceHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/providers/java/EJBProvider.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/DuplicateFileException.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/holders/BigDecimalHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/InternalException.java  
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 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/description/FaultDesc.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/symbolTable/MessageEntry.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/IDResolver.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/http/ChunkedInputStream.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/uuid/UUIDGen.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/mail/Handler.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/symbolTable/UndefinedElement.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/SimpleTargetedChain.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/Skeleton.java  
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 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/HexDeserializerFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/Utils/JavaUtils.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/handlers/EchoHandler.java  
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jar/javax/xml/rpc/encoding/SerializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/image/ImageIOFactory.java
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/attachments/MultiPartInputStream.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utills/ClassUtils.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Entity.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPFactory.java
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utills/StringUtils.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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jar/org/apache/axis/encoding/ser/BeanDeserializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/RPCElement.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/NamespaceConstants.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/SourceDataHandlerSerializer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/utils/LockableHashtable.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/messaging/URLEndpoint.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/gen/Generator.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/handlers/SimpleAuthorizationHandler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/java/JavaSender.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/Base64SerializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/server/Transport.java
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/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/Callback.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/mail/MailSender.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/NMTokens.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/providers/WSDDJavaRMIPProvider.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/MimeHeaders.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/handlers/HandlerChainImpl.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/AttachmentUtils.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/Name.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/CLOption.java  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/monitor/SOAPMonitorService.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Token.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/WSDDOperation.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/namespace/QName.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/providers/java/RMIPProvider.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/http/HTTPTransport.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/WSDDTargetedChain.java  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/encoding/UTF8Encoder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/RPCParam.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/strategies/InvocationStrategy.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/urls/URLHashSet.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Entities.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/MapSerializer.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/net/JSSocketFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/Deserializer.java  
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 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/JavaFaultWriter.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-

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jar/org/apache/axis/wsdl/symbolTable/UndefinedDelegate.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/ImageHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/server/AxisServer.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/Target.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPConnection.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/TeeOutputStream.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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jar/org/apache/axis/encoding/ser/BaseDeserializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/DefaultJAXRPC11TypeMappingImpl.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/constants/Scope.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/holders/ByteHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
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jar/org/apache/axis/encoding/DefaultSOAPEncodingTypeMappingImpl.java
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jar/org/apache/axis/encoding/ser/QNameDeserializer.java
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 jar/org/apache/axis/deployment/wsdd/WSDTypeMapping.java  
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 jar/org/apache/axis/encoding/AttributeSerializationContextImpl.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/rpc/handler/MessageContext.java  
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 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/JavaBeanHelperWriter.java  
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 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/bytecode/ParamNameExtractor.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/WSDDChain.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/server/DefaultAxisServerFactory.java  
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\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/TypeMappingRegistry.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/handlers/soap/SOAPService.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/DeserializerFactory.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/wsdl/toJava/JavaUndeployWriter.java

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/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/WSDDDocument.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/UnsignedLong.java

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\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/local/LocalSender.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/javax/xml/soap/SOAPBody.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Notation.java

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/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/CLUUtil.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/threadpool/ThreadPool.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/attachments/AttachmentsImpl.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-

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jar/org/apache/axis/encoding/ser/BaseSerializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/configuration/SimpleProvider.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/message/EnvelopeBuilder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/i18n/RB.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/handlers/SOAPMonitorHandler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/net/TransportClientProperties.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/transport/http/QSWSDLHandler.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDParameter.java
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  - \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/client/AxisClient.java
  - \*
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  - \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utis/XMLUtils.java
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  - \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utis/ByteArray.java
  - \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/DeserializationContext.java
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\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/management/jmx/DeploymentAdministrator.java  
\*  
/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/management/jmx/WSDDTransportWrapper.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/management/jmx/DeploymentQueryMBean.java  
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* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/deployment/wsdd/WSDDJAXRPCHandlerInfoChain.java
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jar/org/apache/axis/transport/jms/JMSURLConnection.java  
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jar/org/apache/axis/components/jms/JNDIVendorAdapter.java  
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jar/org/apache/axis/transport/jms/JMSConnector.java  
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jar/org/apache/axis/transport/jms/TopicConnector.java  
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jar/org/apache/axis/components/jms/JMSVendorAdapter.java  
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jar/org/apache/axis/transport/jms/JMSEndpoint.java  
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jar/org/apache/axis/transport/jms/JMSConstants.java  
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jar/org/apache/axis/deployment/wsdd/WSDDNonFatalException.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/encoding/SimpleValueSerializer.java  
\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
jar/org/apache/axis/description/ServiceDesc.java  
\*

/opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/holders/UnsignedByteHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/utils/bytecode/ChainedParamReader.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/soap/SOAPConnectionImpl.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/holders/DurationHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/utils/JWSClassLoader.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/description/OperationDesc.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/WSDDEngineConfiguration.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/configuration/EngineConfigurationFactoryFinder.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/components/net/SecureSocketFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/URIHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/holders/YearMonthHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/description/JavaServiceDesc.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/TimeHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/encoding/ser/castor/CastorSerializerFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/TokenHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/holders/UnsignedLongHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/components/net/SocketFactory.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/transport/http/ServletEndpointContextImpl.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Year.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/configuration/EngineConfigurationFactoryDefault.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/configuration/ServletEngineConfigurationFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/components/net/CommonsHTTPClientPropertiesFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/SOAPHeader.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/Detail.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/message/SOAPFaultElement.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-  
 jar/org/apache/axis/description/AttributeDesc.java

\* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/MonthHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/NonNegativeIntegerHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/configuration/BasicClientConfig.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/YearHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/DateHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/PositiveIntegerHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/AxisServiceConfigImpl.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/deployment/wsdd/WSDDFault.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/description/FieldDesc.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/MonthDay.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/collections/SequencedHashMap.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/components/net/SocketFactoryFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/configuration/EngineConfigurationFactoryServlet.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/soap/SOAPConnectionFactoryImpl.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/encoding/ser/SimpleSerializerFactory.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/soap/SOAPConstants.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/Text.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/EngineConfiguration.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/DayHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/OctetStreamHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/MonthDayHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/Utils/SessionUtils.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/ConfigurationException.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/UnsignedShortHolder.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/holders/HexBinaryHolder.java  
 \*  
 /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/soap/SOAP11Constants.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/transport/http/AxisServletBase.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/SOAPBody.java  
 \* /opt/cola/permits/1122085880\_1611198607.29/0/axis-1-4-sources-



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jar/org/apache/axis/holders/NonPositiveIntegerHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/soap/SOAP12Constants.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/holders/NegativeIntegerHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/message/SOAPFaultReasonBuilder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/soap/MessageFactoryImpl.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/soap/SOAPFactoryImpl.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Month.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/encoding/ser/castor/CastorDeserializerFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/utils/bytecode/ClassReader.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/holders/UnsignedIntHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/holders/NormalizedStringHolder.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/YearMonth.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/utils/bytecode/ParamReader.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/configuration/BasicServerConfig.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/components/net/TransportClientPropertiesFactory.java
*
/opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Day.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/description/TypeDesc.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/types/Time.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/EngineConfigurationFactory.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/description/ElementDesc.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-jar/org/apache/axis/message/DetailEntry.java
* /opt/cola/permits/1122085880_1611198607.29/0/axis-1-4-sources-
jar/org/apache/axis/configuration/DefaultEngineConfigurationFactory.java
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```
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```

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# 1.220 unzip 6.0

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There are currently two explicit copyrights on portions of UnZip code (at least, of which Info-ZIP is aware):

Jim Luther's Mac OS File Manager interface code; and Christopher Evans' MacBinaryIII coding code (for the MacOS port).. These copyrights are discussed in more detail below.

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There are no known patents on any of the code in UnZip. Unisys claims a patent on LZW encoding and on LZW decoding in an apparatus that performs LZW encoding, but the patent appears to exempt a stand-alone decoder (as in UnZip's unshrink.c). Unisys has publicly claimed otherwise, but the issue has never been tested in court. Since this point is unclear, unshrinking is not enabled by default. It is the responsibility of the user to make his or her peace with Unisys and its licensing requirements. (unshrink.c may be removed from future releases altogether.)

---

The original unzip source code has been extensively modified and almost entirely rewritten (changes include random zipfile access

rather than sequential; replacement of unimplode() with explode(); replacement of old unshrink() with new (unrelated) unshrink(); replacement of output routines; addition of inflate(), wildcards, filename-mapping, text translation, ...; etc.). As far as we can tell, only the core code of the unreduce method remained substantially similar to Mr. Smith's original source. As of UnZip

5.42, the complete

core code is now covered by the Info-ZIP Licence. Therefore, support for the reduce method has been removed.

The drop of the reduce method should only affect some test archives, reducing was never used in any publically distributed Zip program.

For pathologic cases where support for reduced archive entries is needed, the unreduce code copyrighted by Samuel H. Smith is available as a separate distribution (the restricted copyright of this code is cited below in the "historical" section).

The following copyright applies to the Mac OS File Manager interface code (macos/source/macstuff.[ch]), distributed with UnZip 5.4 and later:

```
* MoreFiles
*
* A collection of File Manager and related routines
*
* by Jim Luther (Apple Macintosh Developer Technical Support Emeritus)
* with significant code contributions by Nitin Ganatra
* (Apple Macintosh Developer Technical Support Emeritus)
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```
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*
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*
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* MacBinary III spec.
* -----
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```

Regarding the first stipulation, Mr. Smith was tracked down in southern  
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May 1994, (213) 851-9969 (voice), (213) 887-2127(?) (subscription BBS),  
71150.2731@compuserve.com]:

"He says that he thought that whoever contacted him understood that  
he has no objection to the Info-ZIP group's inclusion of his code.  
His primary concern is that it remain freely distributable, he said."

Despite the fact that our "normal" code has been entirely rewritten  
and by default

no longer contains any of Mr. Smith's code, Info-ZIP  
remains indebted and grateful to him. We hope he finds our contribu-  
tions as useful as we have his.

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-----

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Schwarze, Ingo  
Scott, Dougal

Sieger, Nick  
Simon, Thor Lancelot  
Slemko, Marc  
Smith, Andy  
Sobrado, Igor  
Soulen, Steven  
Spangler, Aaron  
Spradling, Cloyce D.  
Spradling, Michael  
Stier, Matthew  
Stoeckmann, Tobias  
Street, Russell  
Stritzky, Tilo  
Stroucken, Michael  
Tarrall, Robert  
Thomas, Matthew  
Todd, Giles  
Toft, Martin  
Torek, Chris  
Tucker, Darren  
Uhl, Robert  
Uzel, Petr  
Valery, Reznic  
Van Dinter, Theo  
Venckus, Martynas  
de Vries, Maarten  
Wagner, Klaus  
Walsh, Dan  
Warburton, John  
Webb, Kirk  
Wetzel, Timm  
Wieringen, Marco van  
Wilk, Jakub  
Winiger, Gary  
Wood, David  
Zacarias, Gustavo  
Zolnowsky, John

The following people have worked to translate sudo into other languages as part of the Translation Project, see <https://translationproject.org> for more details.

Albuquerque, Pedro



Blttermann, Mario  
Bogusz, Jakub  
Buo-ren, Lin  
Casagrande, Milo  
Castro, Felipe  
Cho, Seong-ho  
Chornoivan, Yuri  
Diguez, Francisco  
Fontenelle, Rafael  
Garca-Fontes, Walter  
Gezer, Volkan  
Hamasaki, Takeshi  
Hamming, Peter  
Hansen, Joe  
Hantrais, Frdric  
Hein, Jochen  
Hufthammer, Karl Ove  
Jerovek, Damir  
Karvonen, Jorma  
Kazik, Duan  
Kelemen, Gbor  
Keeci, Mehmet  
Koir, Klemen  
Kozlov, Yuri  
Kramer, Jakob  
Krznar, Tomislav  
Marchal, Frdric  
Margeviius, Algimantas  
Maryanov, Pavel  
Florentina Muat  
Nurmi, Lauri  
Nikoli, Miroslav  
Nylander, Daniel  
Pan, Yi-Jyun  
Psa, Petr  
Puente, Enol  
Putanec, Boidar  
Qun, Trn Ngc  
Rasmussen, Sebastian  
Regueiro, Leandro  
Sarer, zgr  
Sendn, Abel  
erbnescu, Daniel  
Sikrom, ka  
Spingos, Dimitris  
Taniguchi, Yasuaki  
Tomat, Fbio  
r, Balzs

Uranga, Mikel Olasagasti  
Vorotnikov, Artem  
Wang, Wylmer  
Yang, Boyuan

The following people designed the artwork used on the sudo website:

Shield logo: Badger, Trent  
Sandwich logo: Stillman, Mark

## 1.224 asm 5.0.3

### 1.224.1 Available under license :

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Found in path(s):

\* /opt/cola/permits/1135840046\_1613613019.19/0/asm-5-0-3-sources-7-jar/org/objectweb/asm/TypePath.java  
\* /opt/cola/permits/1135840046\_1613613019.19/0/asm-5-0-3-sources-7-jar/org/objectweb/asm/TypeReference.java

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/\*\*

\* Creates a new {@link AnalyzerAdapter}. *Subclasses must not use this*

\* constructor*.* Instead, they must use the

\* {@link #AnalyzerAdapter(int, String, int, String, String, MethodVisitor)}

\* version.

\*

\* @param owner

\*

the owner's class name.

\* @param access

\* the method's access flags (see {@link Opcodes}).

\* @param name

\* the method's name.

\* @param desc

```
* the method's descriptor (see { @link Type Type}).
* @param mv
* the method visitor to which this adapter delegates calls. May
* be <tt>null</tt>.
* @throws IllegalStateException
* If a subclass calls this constructor.
*/
```

Found in path(s):

```
*/opt/cola/permits/1135840046_1613613019.19/0/asm-5-0-3-sources-7-
jar/org/objectweb/asm/commons/AnalyzerAdapter.java
```

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```

```
*/
```

```
/**
```

```
* Creates a new { @link GeneratorAdapter}. <i>Subclasses must not use this
```

```
* constructor</i>. Instead, they must use the
```

```
* { @link #GeneratorAdapter(int, MethodVisitor, int, String, String)}
```

```
* version.
```

```

*
* @param mv
* the
method visitor to which this adapter delegates calls.
* @param access
* the method's access flags (see { @link Opcodes}).
* @param name
* the method's name.
* @param desc
* the method's descriptor (see { @link Type Type}).
* @throws IllegalStateException
* If a subclass calls this constructor.
*/

```

Found in path(s):

```

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```

```

*/
/**
 * Constructs a new {@link LocalVariableAnnotationNode}. <i>Subclasses must
 * not use this constructor</i>. Instead, they must use the
 * {@link #LocalVariableAnnotationNode(int, TypePath, LabelNode[], LabelNode[], int[], String)}
 * version.
 *
 * @param typeRef
 * a reference to the annotated type. See {@link TypeReference}.
 * @param typePath
 * the path to the annotated type argument, wildcard bound, array
 * element type, or static inner type within 'typeRef'. May be
 * <tt>null</tt> if the annotation targets 'typeRef' as a whole.
 * @param start
 * the first instructions corresponding to the continuous ranges
 * that make the scope of this local variable (inclusive).
 * @param end
 * the last instructions corresponding to the continuous ranges
 * that make the scope of this local variable (exclusive). This
 * array must have the same size as the 'start' array.
 * @param index
 * the local variable's index in each range. This array must have
 * the same size as the 'start' array.
 * @param desc
 * the class descriptor of
the annotation class.
 */

```

Found in path(s):

```

/opt/cola/permits/1135840046_1613613019.19/0/asm-5-0-3-sources-7-
jar/org/objectweb/asm/tree/LocalVariableAnnotationNode.java

```

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\*/

/\*\*

\* Creates a new JSRInliner. *<i>Subclasses must not use this  
\* constructor</i>*. Instead, they must use the  
\* `{@link #JSRInlinerAdapter(int, MethodVisitor, int, String, String, String, String[])}`  
\* version.

\*

\* @param mv

\*

the `MethodVisitor` to send the resulting inlined

\* method code to (use `null` for none).

\* @param access

\* the method's access flags (see `{@link Opcodes}`). This  
\* parameter also indicates if the method is synthetic and/or  
\* deprecated.

\* @param name

\* the method's name.

\* @param desc

\* the method's descriptor (see `{@link Type}`).

\* @param signature

\* the method's signature. May be `null`.

\* @param exceptions

\* the internal names of the method's exception classes (see  
\* `{@link Type#getInternalName() getInternalName()}`). May be  
\* `null`.

\* @throws IllegalStateException

\* If a subclass calls this constructor.

\*/

Found in path(s):

\* /opt/cola/permits/1135840046\_1613613019.19/0/asm-5-0-3-sources-7-  
jar/org/objectweb/asm/commons/JSRInlinerAdapter.java

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\* /opt/cola/permits/1135840046\_1613613019.19/0/asm-5-0-3-sources-7-jar/org/objectweb/asm/tree/package.html  
\* /opt/cola/permits/1135840046\_1613613019.19/0/asm-5-0-3-sources-7-  
jar/org/objectweb/asm/signature/package.html

\*  
/opt/cola/permits/1135840046\_1613613019.19/0/asm-5-0-3-sources-7-  
jar/org/objectweb/asm/tree/analysis/package.html  
\* /opt/cola/permits/1135840046\_1613613019.19/0/asm-5-0-3-sources-7-  
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\*/

/\*\*

\* Constructs a new { @link ClassNode}. <i>Subclasses must not use this  
\* constructor</i>. Instead, they must use the { @link #ClassNode(int)}  
\* version.

\*

\* @throws IllegalStateException

\* If a subclass calls this constructor.

\*/

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\* Constructs a new {@link Textifier}. *Subclasses must not use this*

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\* version.

\*

\* @throws IllegalStateException

\* If a subclass calls this constructor.

\*/

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* /opt/cola/permits/1135840046_1613613019.19/0/asm-5-0-3-sources-7-
jar/org/objectweb/asm/xml/SAXClassAdapter.java
* /opt/cola/permits/1135840046_1613613019.19/0/asm-5-0-3-sources-7-
jar/org/objectweb/asm/xml/SAXAdapter.java
*
/opt/cola/permits/1135840046_1613613019.19/0/asm-5-0-3-sources-7-
jar/org/objectweb/asm/xml/SAXCodeAdapter.java
* /opt/cola/permits/1135840046_1613613019.19/0/asm-5-0-3-sources-7-
jar/org/objectweb/asm/xml/ASMContentHandler.java
* /opt/cola/permits/1135840046_1613613019.19/0/asm-5-0-3-sources-7-
jar/org/objectweb/asm/xml/SAXFieldAdapter.java
* /opt/cola/permits/1135840046_1613613019.19/0/asm-5-0-3-sources-7-
jar/org/objectweb/asm/xml/SAXAnnotationAdapter.java
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\* /opt/cola/permits/1135840046\_1613613019.19/0/asm-5-0-3-sources-7-jar/org/objectweb/asm/util/Textifiable.java

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The of the start-stop-daemon

- \* A rewrite
- of the original Debian's start-stop-daemon Perl script
- \* in C (faster - it is executed many times during system startup).
- \*
- \* Written by Marek Michalkiewicz <[marekm@i17linuxb.ists.pwr.wroc.pl](mailto:marekm@i17linuxb.ists.pwr.wroc.pl)>,
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## 1.227 shared-mime-info 1.1

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## The Unicode Standard, Unicode Character Database, Version 13.0.0

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Mesa 3-D  
graphics library

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include/GL/glxext.h
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## 1.232 openssl 1.1.1j

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## 1.234 rng-tools 6.11

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@vskip 20pt plus 1fil
@end macro
```

```
@macro copyrightstart{ }
@end macro
```

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@macro copyrightend{ }
@end macro
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@comment node-name, next, previous, up

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@table

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@item GSS-API CFX, SPNEGO, naming extensions, API extensions.

@item KCM credential cache.

@item HDB LDAP backend.

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AES in libcrypto

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rijndael-alg-fst.c

@version 3.0 (December 2000)

Optimised ANSI C code for the Rijndael cipher (now AES)

@author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>  
@author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>  
@author Paulo Barreto <paulo.barreto@terra.com.br>

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Thanks to: Dan Hoey for his excellent Initial and Inverse permutation code; Jim Gillogly & Phil Karn for the DES key schedule code; Dennis Ferguson, Eric Young and Dana How for comparing notes; and Ray Lau, for humouring me on.

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# 1.241 acl 2.2.52-r0

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Version 2, June 1991

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## 1.243 tdb 1.44.3

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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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This is the Debian GNU/Linux prepackaged version of the static EXT2 file system consistency checker (e2fsck.static). The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

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```

This is a Makefile stub which handles the creation of BSD shared
libraries.

In order to use this stub, the following makefile variables must be defined.

BSDLIB_VERSION = 1.0
BSDLIB_IMAGE = libce
BSDLIB_MYDIR = et
BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
```

```
all:: image
```

```
real-subdirs:: Makefile
@echo " MKDIR pic"
@mkdir -p pic
```

```
BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic
```

```
image: $(BSD_LIB)
```

```
$(BSD_LIB): $(OBJS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LD_FLAGS_SHLIB) $(OBJS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) \
`echo $(my_dir) | sed -e 's;lib/;;'/'$(BSD_LIB) $(BSD_LIB))
```

```
install-shlibs install:: $(BSD_LIB)
@echo " INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$(INSTALL_PROGRAM) $(BSD_LIB) \
$(DESTDIR)$ (BSDLIB_INSTALL_DIR)/$(BSD_LIB)
@-$(LDCONFIG)
```

install-strip: install

install-shlibs-strip:: install-shlibs

```
uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)$ (BSDLIB_INSTALL_DIR)/$(BSD_LIB)
```

```
clean::
$(RM)
-rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(BSD_LIB)
```

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Index: tdbsa/tdb.c

=====

--- tdbsa.orig/tdb.c

+++ tdbsa/tdb.c

@@ -4,11 +4,11 @@ Rev: 23371

Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)

\*/

/\*

- Unix SMB/CIFS implementation.
- + trivial database library - standalone version

- trivial database library - private includes

-

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Gadi Oxman, August 1995

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Theodore Ts'o  
23-June-2007

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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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## 1.245 sudo 1.9.5p2

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Adler, Mark  
Allbery, Russ  
Anderson, Jamie  
Andrew, Nick  
Andric, Dimitry  
Barron, Danny  
Bates, Tom  
Behan, Zdenk  
Bellis, Ray  
Benali, Elias  
Beverly, Jamie  
Boardman, Spider  
Bos, Sander  
Bostley, P.J.  
Bowes, Keith  
Boyce, Keith Garry  
Brantley, Michael  
Braun, Rob  
Bezina, Pavel  
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Brown, Jerry  
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Bussjaeger, Andreas  
Calvin, Gary  
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Faigon, Ariel  
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Gelman, Stephen

Gerraty, Simon J.  
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Kopeek, Daniel  
Kranenburg, Paul  
Krause, David  
Lakin, Eric  
Larsen, Case  
Levin, Dmitry V.  
Libby, Kendall  
Lobbes, Phillip E.  
McIntyre, Jason  
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McLaughlin, Tom  
Makey, Jeff  
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Marchionna, Michael D.  
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Meskes, Michael  
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Moldung, Jan Thomas  
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Mueller, Andreas  
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Percival, Ted  
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Peron, Christian S.J.  
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Petten, Diego Elio  
Pickett, Joel  
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Wieringen, Marco van  
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The following people have worked to translate sudo into other languages as part of the Translation Project, see <https://translationproject.org> for more details.

Albuquerque, Pedro  
Blttermann, Mario  
Bogusz, Jakub  
Buo-ren, Lin  
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Chornoivan, Yuri  
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zip/usr/lib/python3.7/site-packages/pycparser/ply/lex.py
* /opt/ws_local/PERMITS_SQL/1089654772_1600397317.82/0/python3-pycparser-2-18-1-ph-
zip/usr/lib/python3.7/site-packages/pycparser/ply/yacc.py
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zip/usr/lib/python3.7/site-packages/pycparser/_build_tables.py
* /opt/ws_local/PERMITS_SQL/1089654772_1600397317.82/0/python3-pycparser-2-18-1-ph-
zip/usr/lib/python3.7/site-packages/pycparser/plyparser.py
* /opt/ws_local/PERMITS_SQL/1089654772_1600397317.82/0/python3-pycparser-2-18-1-ph-
zip/usr/lib/python3.7/site-packages/pycparser/_c_ast.cfg
* /opt/ws_local/PERMITS_SQL/1089654772_1600397317.82/0/python3-pycparser-2-18-1-ph-
zip/usr/lib/python3.7/site-packages/pycparser/ast_transforms.py
* /opt/ws_local/PERMITS_SQL/1089654772_1600397317.82/0/python3-pycparser-2-18-1-ph-
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zip/usr/lib/python3.7/site-packages/pycparser/c_generator.py
* /opt/ws_local/PERMITS_SQL/1089654772_1600397317.82/0/python3-pycparser-2-18-1-ph-
zip/usr/lib/python3.7/site-packages/pycparser/_ast_gen.py
*
/opt/ws_local/PERMITS_SQL/1089654772_1600397317.82/0/python3-pycparser-2-18-1-ph-
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## 1.247 kerberos 1.16.1

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```

```
/* tests/shlib/t_loader.c */
```

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/*
```

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cmd/krb5/ipropl/ipropl\_hdr.h  
cmd/krb5/kadmin/server/ipropl\_svc.c  
cmd/krb5/kproplog/kproplog.c  
cmd/krb5/slave/kpropd\_rpc.c  
lib/gss\_mechs/mech\_krb5/et/kdb5\_err.c  
lib/gss\_mechs/mech\_spnego/mech/gssapiP\_spnego.h  
lib/gss\_mechs/mech\_spnego/mech/spnego\_mech.c  
lib/krb5/kadm5/kadm\_host\_srv\_names.c  
lib/krb5/kdb/kdb\_convert.c  
lib/krb5/kdb/kdb\_hdr.h  
lib/krb5/kdb/kdb\_log.c  
lib/krb5/kdb/kdb\_log.h  
lib/libgss/g\_accept\_sec\_context.c  
lib/libgss/g\_acquire\_cred.c  
lib/libgss/g\_canon\_name.c  
lib/libgss/g\_compare\_name.c  
lib/libgss/g\_context\_time.c  
lib/libgss/g\_delete\_sec\_context.c  
lib/libgss/g\_dsp\_name.c  
lib/libgss/g\_dsp\_status.c  
lib/libgss/g\_dup\_name.c  
lib/libgss/g\_exp\_sec\_context.c  
lib/libgss/g\_export\_name.c  
lib/libgss/g\_glue.c  
lib/libgss/g\_imp\_name.c  
lib/libgss/g\_imp\_sec\_context.c  
lib/libgss/g\_init\_sec\_context.c



lib/libgss/g\_initialize.c  
lib/libgss/g\_inquire\_context.c  
lib/libgss/g\_inquire\_cred.c  
lib/libgss/g\_inquire\_names.c  
lib/libgss/g\_process\_context.c  
lib/libgss/g\_rel\_buffer.c  
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lib/libgss/g\_unseal.c  
lib/libgss/g\_userok.c  
lib/libgss/g\_utils.c  
lib/libgss/g\_verify.c  
lib/libgss/gssd\_pname\_to\_uid.c  
uts/common/gssapi/include/gssapi\_err\_generic.h  
uts/common/gssapi/include/mechglueP.h

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- lib/gssapi/generic/gssapi\_err\_generic.et
- lib/gssapi/mechglue/g\_accept\_sec\_context.c
- lib/gssapi/mechglue/g\_acquire\_cred.c
- lib/gssapi/mechglue/g\_canon\_name.c
- lib/gssapi/mechglue/g\_compare\_name.c
- lib/gssapi/mechglue/g\_context\_time.c
- lib/gssapi/mechglue/g\_delete\_sec\_context.c
- lib/gssapi/mechglue/g\_dsp\_name.c
- lib/gssapi/mechglue/g\_dsp\_status.c
- lib/gssapi/mechglue/g\_dup\_name.c
- lib/gssapi/mechglue/g\_exp\_sec\_context.c
- lib/gssapi/mechglue/g\_export\_name.c
- lib/gssapi/mechglue/g\_glue.c
- lib/gssapi/mechglue/g\_imp\_name.c
- lib/gssapi/mechglue/g\_imp\_sec\_context.c
- lib/gssapi/mechglue/g\_init\_sec\_context.c
- lib/gssapi/mechglue/g\_initialize.c
- lib/gssapi/mechglue/g\_inquire\_context.c
- lib/gssapi/mechglue/g\_inquire\_cred.c
- lib/gssapi/mechglue/g\_inquire\_names.c
- lib/gssapi/mechglue/g\_process\_context.c
- lib/gssapi/mechglue/g\_rel\_buffer.c
- lib/gssapi/mechglue/g\_rel\_cred.c
- lib/gssapi/mechglue/g\_rel\_name.c
- lib/gssapi/mechglue/g\_rel\_oid\_set.c
- lib/gssapi/mechglue/g\_seal.c
- lib/gssapi/mechglue/g\_sign.c
- lib/gssapi/mechglue/g\_store\_cred.c
- lib/gssapi/mechglue/g\_unseal.c
- lib/gssapi/mechglue/g\_userok.c
- lib/gssapi/mechglue/g\_utils.c
- lib/gssapi/mechglue/g\_verify.c
- lib/gssapi/mechglue/gssd\_pname\_to\_uid.c
- lib/gssapi/mechglue/mglueP.h
- lib/gssapi/mechglue/oid\_ops.c
- lib/gssapi/spnego/gssapiP\_spnego.h
- lib/gssapi/spnego/spnego\_mech.c

and the initial implementation of incremental propagation,  
including  
the following new or changed files:

- include/iprop\_hdr.h
- kadmin/server/ipropd\_svc.c



lib/kdb/iprof.x  
lib/kdb/kdb\_convert.c  
lib/kdb/kdb\_log.c  
lib/kdb/kdb\_log.h  
lib/krb5/error\_tables/kdb5\_err.et  
kprop/kpropd\_rpc.c  
kprop/kproplog.c

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## 1.252 mime-pull 1.8

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## 1.254 walinuxagent 2.2.46

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# 1.255 pyvmomi 5.5.0.2014.1.1

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pyVmomi  
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## 1.258 python3-asn1crypto 0.24.0-1.ph3

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the



software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3				
2.1.2	2002	PSF	yes	
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
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2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes

2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
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2.6.5	2.6.4	2010	PSF	yes
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#### A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting  
Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands  
as a successor of a language called ABC. Guido remains Python's  
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for  
National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>)  
in Reston, Virginia where he released several versions of the  
software.

In May 2000, Guido and the Python core development team moved to  
BeOpen.com to form the BeOpen PythonLabs team. In October of the same  
year, the PythonLabs team moved to Digital Creations (now Zope  
Corporation, see <http://www.zope.com>). In 2001, the Python Software  
Foundation (PSF, see <http://www.python.org/psf/>) was formed, a  
non-profit organization created specifically to own Python-related  
Intellectual Property. Zope Corporation is a sponsoring member  
of  
the PSF.

All Python releases are Open Source (see <http://www.opensource.org> for  
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releases have also been GPL-compatible; the table below summarizes  
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Release	Derived from	Year	Owner	GPL- compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no

1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3				
2.1.2	2002	PSF	yes	
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
2.4.4	2.4.3	2006	PSF	yes
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
3.1.1	3.1	2009	PSF	yes
3.1.2	3.1	2010	PSF	yes
3.2	3.1	2010	PSF	yes

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(CA). These were automatically extracted from Mozilla's root certificates  
file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore  
can be directly used with curl / libcurl / php\_curl, or with  
an Apache+mod\_ssl webserver for SSL client authentication.

Just configure this file as the SSLCACertificateFile.#

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# PLY (Python Lex-Yacc)                      Version 3.11

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Introduction

=====

PLY is a 100% Python implementation of the common parsing tools lex  
and yacc. Here are a few highlights:

- PLY is very closely modeled after traditional lex/yacc.  
If you know how to use these tools in C, you will find PLY to be similar.
- PLY provides *very* extensive error reporting and diagnostic information to assist in parser construction. The original implementation was developed for instructional purposes. As a result, the system tries to identify the most common types of errors made by novice users.
- PLY provides full support for empty productions, error recovery, precedence specifiers, and moderately ambiguous grammars.
- Parsing is based on LR-parsing which is fast, memory efficient, better suited to large grammars, and which has a number of nice properties when dealing with syntax errors and other parsing problems. Currently, PLY builds its parsing tables using the LALR(1) algorithm used in yacc.
- PLY uses Python introspection features to build lexers and parsers. This greatly simplifies the task of parser construction since it reduces the number of files and eliminates the need to run a separate lex/yacc tool before running your program.
- PLY can be used to build parsers for "real" programming languages. Although it is not ultra-fast due to its Python implementation, PLY can be used to parse grammars consisting of several hundred rules (as might be found for a language like C). The lexer and LR parser are also reasonably efficient when parsing typically sized programs. People have used PLY to build parsers for C, C++, ADA, and other real programming languages.

#### How to Use

=====

PLY consists of two files : lex.py and yacc.py. These are contained within the 'ply' directory which may also be used as a Python package. To use PLY, simply copy the 'ply' directory to your project and import lex and yacc from the associated 'ply' package. For example:

```
import ply.lex as lex
import ply.yacc as yacc
```

Alternatively, you can copy just the files lex.py and yacc.py individually and use them as modules. For example:

```
import lex
```

```
import yacc
```

The file setup.py can be used to install  
ply using distutils.

The file doc/ply.html contains complete documentation on how to use  
the system.

The example directory contains several different examples including a  
PLY specification for ANSI C as given in K&R 2nd Ed.

A simple example is found at the end of this document

#### Requirements

=====

PLY requires the use of Python 2.6 or greater. However, you should  
use the latest Python release if possible. It should work on just  
about any platform. PLY has been tested with both CPython and Jython.  
It also seems to work with IronPython.

#### Resources

=====

More information about PLY can be obtained on the PLY webpage at:

<http://www.dabeaz.com/ply>

For a detailed overview of parsing theory, consult the excellent  
book "Compilers : Principles, Techniques, and Tools" by Aho, Sethi, and  
Ullman. The topics found in "Lex & Yacc" by Levine, Mason, and Brown  
may also be useful.

The GitHub page for PLY can be found at:

<https://github.com/dabeaz/ply>

An old and  
relatively inactive discussion group for PLY is found at:

<http://groups.google.com/group/ply-hack>

#### Acknowledgments

=====

A special thanks is in order for all of the students in CS326 who  
suffered through about 25 different versions of these tools :-).

The CHANGES file acknowledges those who have contributed patches.

Elias Ioup did the first implementation of LALR(1) parsing in PLY-1.x.



Andrew Waters and Markus Schoepflin were instrumental in reporting bugs and testing a revised LALR(1) implementation for PLY-2.0.

#### Special Note for PLY-3.0

```
=====
```

PLY-3.0 the first PLY release to support Python 3. However, backwards compatibility with Python 2.6 is still preserved. PLY provides dual Python 2/3 compatibility by restricting its implementation to a common subset of basic language features. You should not convert PLY using 2to3--it is not necessary and may in fact break the implementation.

#### Example

```
=====
```

Here is a simple example showing a PLY implementation of a calculator with variables.

```

calc.py
#
A simple calculator with variables.

tokens = (
 'NAME','NUMBER',
 'PLUS','MINUS','TIMES','DIVIDE','EQUALS',
 'LPAREN','RPAREN',
)

Tokens

t_PLUS = r\+'
t_MINUS = r\-'
t_TIMES = r*'
t_DIVIDE = r\/'
t_EQUALS = r\='
t_LPAREN = r\('
t_RPAREN = r\) '
t_NAME = r'[a-zA-Z_][a-zA-Z0-9_]*'

def t_NUMBER(t):
 r\d+'
 t.value = int(t.value)
 return t

Ignored characters
t_ignore = " \t"
```

```

def t_newline(t):
 r'\n+'
 t.lexer.lineno += t.value.count("\n")

def t_error(t):
 print("Illegal character '%s'" % t.value[0])
 t.lexer.skip(1)

Build the lexer
import ply.lex as lex
lex.lex()

Precedence
rules for the arithmetic operators
precedence = (
 ('left','PLUS','MINUS'),
 ('left','TIMES','DIVIDE'),
 ('right','UMINUS'),
)

dictionary of names (for storing variables)
names = { }

def p_statement_assign(p):
 'statement : NAME EQUALS expression'
 names[p[1]] = p[3]

def p_statement_expr(p):
 'statement : expression'
 print(p[1])

def p_expression_binop(p):
 """expression : expression PLUS expression
 | expression MINUS expression
 | expression TIMES expression
 | expression DIVIDE expression"""
 if p[2] == '+': p[0] = p[1] + p[3]
 elif p[2] == '-': p[0] = p[1] - p[3]
 elif p[2] == '*': p[0] = p[1] * p[3]
 elif p[2] == '/': p[0] = p[1] / p[3]

def p_expression_uminus(p):
 'expression : MINUS expression %prec UMINUS'
 p[0] = -p[2]

def p_expression_group(p):
 'expression : LPAREN

```

```

expression RPAREN'
 p[0] = p[2]

def p_expression_number(p):
 'expression : NUMBER'
 p[0] = p[1]

def p_expression_name(p):
 'expression : NAME'
 try:
 p[0] = names[p[1]]
 except LookupError:
 print("Undefined name '%s'" % p[1])
 p[0] = 0

def p_error(p):
 print("Syntax error at '%s'" % p.value)

import ply.yacc as yacc
yacc.yacc()

while True:
 try:
 s = raw_input('calc > ') # use input() on Python 3
 except EOFError:
 break
 yacc.parse(s)

```

## Bug Reports and Patches

=====

My goal with PLY is to simply have a decent lex/yacc implementation for Python. As a general rule, I don't spend huge amounts of time working on it unless I receive very specific bug reports and/or patches to fix problems. I also try to incorporate submitted feature requests and enhancements into each new version. Please visit the PLY github page at <https://github.com/dabeaz/ply> to submit issues and pull requests. To contact me about bugs and/or new features, please send email to [dave@dabeaz.com](mailto:dave@dabeaz.com).

-- Dave

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## 1.262 urllib3 1.23

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# Contributions to the urllib3 project

## Creator & Maintainer

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## Contributors

In chronological order:

\* victor.vde <<http://code.google.com/u/victor.vde/>>

\* HTTPS patch (which inspired HTTPSConnectionPool)

- \* erikcederstrand <<http://code.google.com/u/erikcederstrand/>>
- \* NTLM-authenticated HTTPSConnectionPool
- \* Basic-authenticated HTTPSConnectionPool (merged into make\_headers)
  
- \* niphlod <niphlod@gmail.com>
- \* Client-verified SSL certificates for HTTPSConnectionPool
- \* Response gzip and deflate encoding support
- \* Better unicode support for filepost using StringIO buffers
  
- \* btoconnor <brian@btoconnor.net>
- \* Non-multipart encoding for POST requests
  
- \* p.dobrogost <<http://code.google.com/u/@WBRSRIBZDhBFXQB6/>>
- \* Code review, PEP8 compliance, benchmark fix
  
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- \* georgemarshall <<http://github.com/georgemarshall>>
- \* Bugfixes, Improvements and Test coverage
  
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- \* Python 3 support
  
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- \* IPv6 url support and test coverage
  
- \* Shivaram Lingamneni <slingamn@cs.stanford.edu>
- \* Support for explicitly closing pooled connections
  
- \* hartator <hartator@gmail.com>
- \* Corrected multipart behavior for params
  
- \* Thomas Weischuh <thomas@t-8ch.de>
- \* Support for TLS SNI
- \* API unification of ssl\_version/cert\_reqs
- \* SSL fingerprint and alternative hostname verification
- \* Bugfixes in testsuite
  
- \* Sune Kirkeby <mig@ibofobi.dk>
- \* Optional SNI-support for Python 2 via PyOpenSSL.
  
- \* Marc Schlaich <marc.schlaich@gmail.com>
- \* Various bugfixes and test improvements.

- \* Bryce Boe <bbzbryce@gmail.com>
  - \* Correct six.moves conflict
  - \* Fixed pickle support of some exceptions
  
- \* Boris Figovsky <boris.figovsky@ravellosystems.com>
  - \* Allowed to skip SSL hostname verification
  
- \* Cory Benfield  
<<http://lukasa.co.uk/about/>>
  - \* Stream method for Response objects.
  - \* Return native strings in header values.
  - \* Generate 'Host' header when using proxies.
  
- \* Jason Robinson <jaywink@basshero.org>
  - \* Add missing WrappedSocket.fileno method in PyOpenSSL
  
- \* Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
  - \* Fixed a race condition
  
- \* Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
  - \* Added HTTPS (CONNECT) proxy support
  
- \* Stephen Holsapple <sholsapp@gmail.com>
  - \* Added abstraction for granular control of request fields
  
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  - \* Support for non-ASCII header parameters
  
- \* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
  - \* Support for separate connect and request timeouts
  
- \* Peter Waller <p@pwaller.net>
  - \* HTTPResponse.tell() for determining amount received over the wire
  
- \* Nipunn Koorapati <nipunn1313@gmail.com>
  - \* Ignore default ports when comparing hosts for equality
  
- \* Danilo @dbrgn <<http://dbrgn.ch/>>
  - \* Disabled TLS compression by default on Python 3.2+
  - \* Disabled TLS compression in pyopenssl contrib module
  - \* Configurable cipher suites in pyopenssl contrib module
  
- \* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
  - \* Account retries on proxy errors
  
- \* Nicolas Delaby <nicolas.delaby@ezeep.com>
  - \* Use the platform-specific CA certificate locations

- \* Josh Schneier <<https://github.com/jschneier>>
  - \* HTTPHeaderDict and associated tests and docs
  - \* Bugfixes, docs, test coverage
  
- \* Tahia Khan <<http://tahia.tk/>>
  - \* Added Timeout examples in docs
  
- \* Arthur Grunseid <<http://grunseid.com>>
  - \* source\_address support and tests (with <https://github.com/bui>)
  
- \* Ian Cordasco <[graffatcolmingov@gmail.com](mailto:graffatcolmingov@gmail.com)>
  - \* PEP8 Compliance and Linting
  - \* Add ability to pass socket options to an HTTP Connection
  
- \* Erik Tollerud <[erik.tollerud@gmail.com](mailto:erik.tollerud@gmail.com)>
  - \* Support for standard library io module.
  
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  - \* Google App Engine documentation
  
- \* Aaron Meurer <[asmeurer@gmail.com](mailto:asmeurer@gmail.com)>
  - \* Added Url.url, which unparses a Url
  
- \* Evgeny Kapun <[abacabadabacaba@gmail.com](mailto:abacabadabacaba@gmail.com)>
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- \* Benjamin Meyer <[bm\\_witness@yahoo.com](mailto:bm_witness@yahoo.com)>
  - \* Security Warning Documentation update for proper capture
  
- \* Shivan Sornarajah <[github@sornars.com](mailto:github@sornars.com)>
  - \* Support for using ConnectionPool and PoolManager as context managers.
  
- \* Alex Gaynor <[alex.gaynor@gmail.com](mailto:alex.gaynor@gmail.com)>
  - \* Updates to the default SSL configuration
  
- \* Tomas Tomecek <[ttomecek@redhat.com](mailto:ttomecek@redhat.com)>
  - \* Implemented generator for getting chunks from chunked responses.
  
- \* tlynn <<https://github.com/tlynn>>
  - \* Respect the warning preferences at import.
  
- \* David D. Riddle <[ddriddle@illinois.edu](mailto:ddriddle@illinois.edu)>
  - \* IPv6 bugfixes in testsuite
  
- \* Jon Wayne Parrott <[jonwayne@google.com](mailto:jonwayne@google.com)>
  - \* App Engine environment tests.

- \* John Krauss <<https://github.com/talos>>
- \* Clues to debugging problems with `cryptography` dependency in docs
  
- \* Disassem <<https://github.com/Disassem>>
- \* Fix pool-default headers not applying for url-encoded requests like GET.
  
- \* James Atherfold <[jlatherfold@hotmail.com](mailto:jlatherfold@hotmail.com)>
- \* Bugfixes relating to cleanup of connections during errors.
  
- \* Christian Pedersen <<https://github.com/chripede>>
- \* IPv6 HTTPS proxy bugfix
  
- \* Jordan Moldow <<https://github.com/jmoldow>>
- \* Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
- \* Bugfix for ``ConnectionPool.urlopen(release\_conn=False)``.
- \* Creation of ``HTTPConnectionPool.ResponseCls``.
  
- \* Predrag Gruevski <<https://github.com/obi1kenobi>>
- \* Made cert digest comparison use a constant-time algorithm.
  
- \* Adam Talsma <<https://github.com/a-tal>>
- \* Bugfix to ca\_cert file paths.
  
- \* Evan Meagher <<https://evanmeagher.net>>
- \* Bugfix related to `memoryview` usage in PyOpenSSL adapter
  
- \* John Vandenberg <[jayvdb@gmail.com](mailto:jayvdb@gmail.com)>
- \* Python 2.6 fixes; pyflakes and pep8 compliance
  
- \* Andy Caldwell <[andy.m.caldwell@gmail.com](mailto:andy.m.caldwell@gmail.com)>
- \* Bugfix related to reusing connections in indeterminate states.
  
- \* Ville Skytt <[ville.skytta@iki.fi](mailto:ville.skytta@iki.fi)>
- \* Logging efficiency improvements, spelling fixes, Travis config.
  
- \* Shige Takeda <[smtakeda@gmail.com](mailto:smtakeda@gmail.com)>
- \* Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response
  
- \* Jesse Shapiro <[jesse@jesseshapiro.net](mailto:jesse@jesseshapiro.net)>
- \* Various character-encoding fixes/tweaks
- \* Disabling IPv6 DNS when IPv6 connections not supported
  
- \* David Foster <<http://dafoster.net/>>
- \* Ensure order of request and response headers are preserved.
  
- \* Jeremy Cline <[jeremy@jcline.org](mailto:jeremy@jcline.org)>



- \* Added connection pool keys by scheme
  
- \* Aviv Palivoda <palaviv@gmail.com>
  - \* History list to Retry object.
  - \* HTTPResponse contains the last Retry object.
  
- \* Nate Prewitt <nate.prewitt@gmail.com>
  - \* Ensure timeouts are not booleans and greater than zero.
  - \* Fixed infinite loop in ``stream`` when amt=None.
  - \* Added length\_remaining to determine remaining data to be read.
  - \* Added enforce\_content\_length to raise exception when incorrect content-length received.
  
- \* Seth Michael Larson <sethmichaellarson@protonmail.com>
  - \* Created selectors  
backport that supports PEP 475.
  
- \* Alexandre Dias <alex.dias@smarkets.com>
  - \* Don't retry on timeout if method not in whitelist
  
- \* Moinuddin Quadri <moin18@gmail.com>
  - \* Lazily load idna package
  
- \* Tom White <s6yg1ez3@mail2tor.com>
  - \* Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.
  
- \* Tim Burke <tim.burke@gmail.com>
  - \* Stop buffering entire deflate-encoded responses.
  
- \* Tuukka Mustonen <tuukka.mustonen@gmail.com>
  - \* Add counter for status\_forcelist retries.
  
- \* Erik Rose <erik@mozilla.com>
  - \* Bugfix to pyopenssl vendoring
  
- \* Wolfgang Richter <wolfgang.richter@gmail.com>
  - \* Bugfix related to loading full certificate chains with PyOpenSSL backend.
  
- \* Mike Miller <github@mikeage.net>
  - \* Logging improvements to include the HTTP(S) port when opening a new connection
  
- \* Ioannis Tziakos <mail@itziakos.gr>
  - \* Fix ``util.selectors.\_fileobj\_to\_fd`` to accept ``long``.
  - \* Update appveyor tox setup to use the 64bit python.
  
- \* Akamai (through Jesse Shapiro) <jshapiro@akamai.com>
  - \* Ongoing maintenance
  
- \* Dominique Leuenberger <dimstar@opensuse.org>

- \* Minor fixes in the test suite
  
  - \* Will Bond <will@wbond.net>
  - \* Add Python 2.6 support to ``contrib.securetransport``
  
  - \* Aleksei Alekseev <alekseev.yeskela@gmail.com>
  - \* using auth info for socks proxy
  
  - \* Chris Wilcox <git@crwilcox.com>
  - \* Improve contribution guide
  - \* Add ``HTTPResponse.geturl`` method to provide ``urllib2.urlopen().geturl()`` behavior
  
  - \* [Your name or handle] <[email or website]>
  - \* [Brief summary of your changes]
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## 1.263 python-requests 2.19.1

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## 1.265 xz 5.2.4

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```

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=====

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# 1.266 python-pip 18.0

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see <http://www.cwi.nl>) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see <http://www.zope.com>). In 2001, the Python Software Foundation (PSF, see <http://www.python.org/psf/>) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3				
2.1.2	2002	PSF	yes	
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
2.4.4	2.4.3	2006	PSF	yes
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes

2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
2.7	2.6	2010	PSF	yes

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# 1.268 pyopenssl 18.0.0

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## 1.269 pyasn1 0.4.4-1.ph3

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0.4.4/pyasn1/codec/ber/encoder.py
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0.4.4/pyasn1/codec/native/decoder.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/compat/test_octets.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/type/char.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/codec/ber/eoo.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/type/test_tag.py
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/opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/test_debug.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/codec/cer/encoder.py
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0.4.4/tests/codec/ber/test_encoder.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/compat/test_integer.py
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0.4.4/tests/codec/native/test_decoder.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/setup.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/compat/octets.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/codec/cer/decoder.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/compat/binary.py
*
/opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/type/__main__.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/type/univ.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/type/base.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/der/encoder.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/codec/cer/__main__.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/type/tagmap.py
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*
/opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/compat/test_binary.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/type/error.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/type/constraint.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/codec/__main__.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/compat/string.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/debug.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
0.4.4/pyasn1/codec/der/decoder.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/type/useful.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/type/test_char.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/pyasn1/type/tag.py
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* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/base.py
* /opt/cola/permits/1161616804_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/tests/type/test_univ.py
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ASN.1 library for Python

-----

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[![Python Versions](https://img.shields.io/pypi/pyversions/pyasn1.svg)](https://pypi.org/project/pyasn1/)  
[![Build status](https://travis-ci.org/etingof/pyasn1.svg?branch=master)](https://secure.travis-ci.org/etingof/pyasn1)  
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[![GitHub license](https://img.shields.io/badge/license-BSD-blue.svg)](https://raw.githubusercontent.com/etingof/pyasn1/master/LICENSE.txt)

This is a free and open source implementation of ASN.1 types and codecs as a Python package. It has been first written to support particular protocol (SNMP) but then generalized to be suitable for a wide range of protocols based on

[ASN.1 specification](https://www.itu.int/rec/dologin\_pub.asp?lang=e&id=T-REC-X.208-198811-W!!PDF-E&type=items).

Features

-----

- \* Generic implementation of ASN.1 types (X.208)
- \* Standards compliant BER/CER/DER codecs
- \* Dumps/loads ASN.1 structures from Python types
- \* 100% Python, works with Python 2.4 up to Python 3.7
- \* MT-safe
- \* Contributed ASN.1 compiler [Asn1ate](https://github.com/kimgr/asn1ate)

## Why using pyasn1

-----

ASN.1 solves the data serialisation problem. This solution was designed long ago by the wise Ancients. Back then, they did not have the luxury of wasting bits. That is why ASN.1 is designed to serialise data structures of unbounded complexity into something compact and efficient when it comes to processing the data.

That probably explains why many network protocols and file formats still rely on the 30+ years old technology. Including a number of high-profile Internet protocols and file formats.

Quite a number of books cover the topic of ASN.1.  
 [Communication between heterogeneous systems](http://www.oss.com/asn1/dubuisson.html)  
 by  
 Olivier Dubuisson is one of those high quality books freely available on the Internet.

The pyasn1 package is designed to help Python programmers tackling network protocols and file formats at the comfort of their Python prompt. The tool struggles to capture all aspects of a rather complicated ASN.1 system and to represent it on the Python terms.

## How to use pyasn1

-----

With pyasn1 you can build Python objects from ASN.1 data structures. For example, the following ASN.1 data structure:

```

```bash
Record ::= SEQUENCE {
  id    INTEGER,
  room  [0] INTEGER OPTIONAL,
  house [1] INTEGER DEFAULT 0
}
```
```

Could be expressed in pyasn1 like this:

```

```python
class Record(Sequence):
    componentType = NamedTypes(
        NamedType('id', Integer()),
        OptionalNamedType(
            'room', Integer().subtype(
                implicitTag=Tag(tagClassContext, tagFormatSimple, 0)
            )
        ),
        DefaultedNamedType(
            'house',
            Integer(0).subtype(
                implicitTag=Tag(tagClassContext, tagFormatSimple, 1)
            )
        )
    )
```

```

It is in the spirit of ASN.1 to take abstract data description and turn it into a programming language specific form. Once you have your ASN.1 data structure expressed in Python, you can use it along the lines of similar Python type (e.g. ASN.1 `SET` is similar to Python `dict`, `SET OF` to `list`):

```

```python
>>> record = Record()
>>> record['id'] = 123
>>> record['room'] = 321
>>> str(record)
Record:
id=123
room=321
>>>
```

```

Part of the power of ASN.1 comes from its serialisation features. You can serialise your data structure and send it over the network.

```

```python
>>> from pyasn1.codec.der.encoder import encode
>>> substrate = encode(record)
>>> hexdump(substrate)
00000: 30 07 02 01 7B 80 02 01 41
```

```

Conversely, you can turn serialised ASN.1 content, as received from network or read from a file, into a Python object which you can

introspect, modify,  
encode and send back.

```
```python
>>> from pyasn1.codec.der.decoder import decode
>>> received_record, rest_of_substrate = decode(substrate, asn1Spec=Record())
>>>
>>> for field in received_record:
>>>     print('{ } is {}'.format(field, received_record[field]))
id is 123
room is 321
house is 0
>>>
>>> record == received_record
True
>>> received_record.update(room=123)
>>> substrate = encode(received_record)
>>> hexdump(substrate)
00000: 30 06 02 01 7B 80 01 7B
```
```

The pyasn1 classes struggle to emulate their Python prototypes (e.g. int, list, dict etc.). But ASN.1 types exhibit more complicated behaviour.

To make life easier for a Pythonista, they can turn their pyasn1 classes into Python built-ins:

```
```python
>>> from pyasn1.codec.native.encoder import encode
>>> encode(record)
{'id': 123, 'room': 321, 'house': 0}
```
```

Or vice-versa -- you can initialize an ASN.1 structure from a tree of Python objects:

```
```python
>>> from pyasn1.codec.native.decoder import decode
>>> record = decode({'id':
123, 'room': 321, 'house': 0}, asn1Spec=Record())
>>> str(record)
Record:
id=123
room=321
>>>
```
```

With ASN.1 design, serialisation codecs are decoupled from data objects, so you could turn every single ASN.1 object into many different

serialised forms. As of this moment, pyasn1 supports BER, DER, CER and Python built-ins codecs. The extremely compact PER encoding is expected to be introduced in the upcoming pyasn1 release.

More information on pyasn1 APIs can be found in the [documentation](<http://snmplabs.com/pyasn1/>), compiled ASN.1 modules for different protocols and file formats could be found in the pyasn1-modules [repo](<https://github.com/etingof/pyasn1-modules>).

How to get pyasn1

-----

The pyasn1 package is distributed under terms and conditions of 2-clause BSD [license](<http://snmplabs.com/pyasn1/license.html>). Source code is freely available as a GitHub [repo](<https://github.com/etingof/pyasn1>).

You could `pip install pyasn1` or download it from [PyPI](<https://pypi.org/project/pyasn1>).

If

something does not work as expected, [open an issue](<https://github.com/etingof/pyasn1/issues>) at GitHub or post your question [on Stack Overflow](<https://stackoverflow.com/questions/ask>) or try browsing pyasn1 [mailing list archives](<https://sourceforge.net/p/pyasn1/mailman/pyasn1-users/>).

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Found in path(s):

\* /opt/cola/permits/1161616804\_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/README.md  
No license file was found, but licenses were detected in source scan.

ASN.1 library for Python

=====

```
.. toctree::
 :maxdepth: 1
```

Abstract Syntax Notation One (ASN.1 <[http://en.wikipedia.org/wiki/Abstract\\_Syntax\\_Notation\\_1x](http://en.wikipedia.org/wiki/Abstract_Syntax_Notation_1x)>) is a technology for exchanging structured data in a universally understood, hardware agnostic way. Many industrial, security and telephony applications heavily rely on ASN.1.

The `pyasn1` <<https://pypi.org/project/pyasn1/>> library implements ASN.1 support in pure-Python.

## What is ASN.1

-----

ASN.1 is a large, arguably over-engineered and extremely old data modelling and serialisation tool. It is probably among the first serialisation protocols in the history of computer science and technology.

ASN.1 started its life over 30 years ago as a serialisation mechanism for the first electronic mail (known as X.400). Later on it was split off the e-mail application and became a stand-alone tech still being actively supported by its designers and widely used in industry and technology.

Since then ASN.1 is sort of haunted by its relations with the OSI model -- the first, unsuccessful, version of the Internet. You can read many interesting discussions <<https://news.ycombinator.com/item?id=8871453>> on that topic.

In the following years, generations of software engineers tackled the serialisation problem many times. We can see that in Google's `ProtoBuffers` <<https://developers.google.com/protocol-buffers/>> or `FlatBuffers` <<https://google.github.io/flatbuffers/>>, for example. Interestingly, many new takes on binary protocol design do not depart far from ASN.1 from technical perspective. It's more of a matter of striking a balance between processing overhead, wire format overhead and human readability.

Looking at what ASN.1 has to offer, it has three loosely coupled parts:

- \* Data types: the standard introduces a collection of basic data types (integers, bits, strings, arrays and records) that can be used for describing arbitrarily complex, nested data structures.
- \* Serialisation protocols: the above data structures could be converted into a series of octets for storage or transmission over the wire as well as recovered back into their structured form. The system is fully agnostic to hardware architectures differences.
- \* Schema language: ASN.1 data structures could be described in terms of a schema language for ASN.1 compiler to turn it into platform-specific implementation.

## ASN.1 applications

-----

Being an old and generally successful standard, ASN.1 is widely adopted for many uses. To give you an example, these technologies use ASN.1 for their data exchange needs:



- \* Signaling standards for the public switched telephone network (SS7 family)
- \* Network management standards (SNMP, CMIP)
- \* Directory standards (X.500 family, LDAP)
- \* Public Key Infrastructure standards (X.509, etc.)
- \* PBX control (CSTA)
- \* IP-based Videoconferencing (H.323 family)
- \* Biometrics (BIP, CBEFF, ACBio)
- \* Intelligent transportation (SAE J2735)
- \* Cellular telephony (GSM, GPRS/EDGE, UMTS, LTE)

## ASN.1 gotchas

-----

Apparently, ASN.1 is hard to implement properly. Quality open-source ASN.1 tools are rare, but ad-hoc implementations are numerous. Judging from the `statistics <http://cve.mitre.org/cgi-bin/cvekey.cgi?keyword=ASN.1>` on discovered security vulnerabilities, many people have implemented ASN.1 parsers and oftentimes fell victim to its edge cases.

On the bright side, ASN.1 has been around for a long time, it is well understood and security reviewed.

## Documentation

-----

```
.. toctree::
 :maxdepth: 2

 /pyasn1/contents
```

## Use case

-----

```
.. toctree::
 :maxdepth: 2

 /example-use-case
```

## Download & Install

-----

```
.. toctree::
 :maxdepth: 2

 /download
```

## Changes

-----

All changes and release history is maintained in changelog. There you could also download the latest unreleased pyasn1 tarball containing the latest fixes and improvements.

..

toctree::

:maxdepth: 1

/changelog

License

-----

The PyASN1 software is distributed under 2-clause BSD License.

.. toctree::

:maxdepth: 2

/license

Getting help

-----

Please, file your `issues` <<https://github.com/etingof/pyasn1/issues>>` \_

and `PRs` <<https://github.com/etingof/pyasn1/pulls>>` \_ at GitHub.

Alternatively, you could ask for help at

`Stack Overflow` <<http://stackoverflow.com/questions/tagged/pyasn1>>` \_

or search

`pyasn1-users` <<https://lists.sourceforge.net/lists/listinfo/pyasn1-users>>` \_

mailing list archive.

Books on ASN.1

-----

The pyasn1 implementation is largely based on reading up the following awesome books:

\* `ASN.1 - Communication between heterogeneous systems` <<http://www.oss.com/asn1/dubuisson.html>>` \_ by Olivier Dubuisson

\* `ASN.1 Complete` <<http://www.oss.com/asn1/resources/books-whitepapers-pubs/larmouth-asn1-book.pdf>>` \_ by Prof John Larmouth

Here you can get the official standards which is hard to read:

\* `ITU standards`

<<http://www.itu.int/ITU-T/studygroups/com17/languages/X.680-X.693-0207w.zip>>`\_

On the other end of the readability spectrum, here is a quick and sweet write up:

\* `A Layman's Guide to a Subset of ASN.1, BER, and DER <[ftp://ftp.rsasecurity.com/pub/pkcs/ascii/layman.asc](http://ftp.rsasecurity.com/pub/pkcs/ascii/layman.asc)>`\_  
by Burton S. Kaliski

If you are working with ASN.1, we'd highly recommend reading a proper book on the subject.

Found in path(s):

\* /opt/cola/permits/1161616804\_1621638494.08/0/pyasn1-0-4-4-1-tar-gz/pyasn1-0.4.4/docs/source/contents.rst

## 1.270 python-certifi 2018.8.13

### 1.270.1 Available under license :

This package contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php\_curl, or with an Apache+mod\_ssl webserver for SSL client authentication.

Just configure this file as the SSLCACertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$

\$Revision: 1.80 \$ \$Date: 2011/11/03 15:11:58 \$

## 1.271 idna 2.7-1.ph3

### 1.271.1 Available under license :

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""""

A library to support the Internationalised Domain Names in Applications

(IDNA) protocol as specified in RFC 5890 et.al. This new methodology, known as IDNA 2008, can generate materially different results to the previous standard. The library can act as a drop-in replacement for the "encodings.idna" module.

```
"""
```

```
import io, sys
```

```
from setuptools import setup
```

```
def main():
```

```
 python_version = sys.version_info[:2]
```

```
 if python_version < (2,6):
```

```
 raise SystemExit("Sorry, Python 2.6 or newer required")
```

```
 package_data = { }
```

```
 exec(open('idna/package_data.py').read(), package_data)
```

```
 arguments = {
```

```
 'name': 'idna',
```

```
 'packages': ['idna'],
```

```
 'version': package_data['__version__'],
```

```
 'description': 'Internationalized Domain Names in Applications (IDNA)',
```

```
 'long_description': io.open("README.rst", encoding="UTF-8").read(),
```

```
 'author': 'Kim Davies',
```

```
 'author_email': 'kim@cynosure.com.au',
```

```
 'license': 'BSD-like',
```

```
 'url': 'https://github.com/kjd/idna',
```

```
 'classifiers': [
```

```
 'Development Status :: 5 - Production/Stable',
```

```
 'Intended Audience :: Developers',
```

```
 'Intended Audience :: System Administrators',
```

```
 'License :: OSI Approved :: BSD License',
```

```
 'Operating System :: OS Independent',
```

```
 'Programming Language :: Python',
```

```
 'Programming Language :: Python :: 2.6',
```

```
 'Programming Language :: Python :: 2.7',
```

```
 'Programming Language :: Python :: 3',
```

```
 'Programming Language :: Python :: 3.3',
```

```
 'Programming Language :: Python :: 3.4',
```

```
 'Programming Language :: Python :: 3.5',
```

```
 'Programming Language :: Python :: 3.6',
```

```
 'Topic :: Internet :: Name Service (DNS)',
```

```
 'Topic :: Software Development :: Libraries :: Python Modules',
```

```
 'Topic :: Utilities',
```

```
],
```

```
 'test_suite':
```

```
'tests',
}

setup(**arguments)

if __name__ == '__main__':
 main()
```

Found in path(s):

\* /opt/cola/permits/1161616786\_1621638310.0/0/idna-2-7-1-tar-gz/idna-2.7/setup.py

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# 1.272 python-requests 2.19.1-4.ph3

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Original Project:

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## 1.273 shared-mime-info 1.1

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```
<one line to give the library's name and a brief idea of what it does.>
Copyright (C) <year> <name of author>
```

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Also add information on how to contact you by electronic and paper mail.

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<signature of Ty Coon>, 1 April 1990  
Ty Coon, President of Vice

That's all there is to it!

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this

function must

be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the

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#### END OF TERMS AND CONDITIONS

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stand

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-- Wolfgang Denk

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>

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or your  
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library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990  
Ty Coon, President of Vice

That's all there is to it!

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Version 2, June 1991

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Foundation's software and to any other program whose authors commit to  
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the GNU Lesser General Public License instead.) You can apply it to  
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Our General Public Licenses are designed to make sure that you have the freedom to distribute copies of free software (and charge for this service if you wish), that you receive source code or can get it if you want it, that you can change the software or use pieces of it in new free programs; and that you know you can do these things.

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For example, if you distribute copies of such a program, whether gratis or for a fee, you must give the recipients all the rights that you have. You must make sure that they, too, receive or can get the source code. And you must show them these terms so they know their rights.

We protect your rights with two steps: (1) copyright the software, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the software.

Also, for each author's protection and ours, we want to make certain that everyone understands that there is no warranty for this free software. If the software is modified by someone else and passed on, we want its recipients to know that what they have is not the original, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that redistributors of a free program will individually obtain patent licenses, in effect making the program proprietary. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

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Thus, it is not the intent of this section to claim rights or contest  
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years, to give any third party, for a charge no more than your  
cost of physically performing source distribution, a complete  
machine-readable copy of the corresponding source code, to be  
distributed under the terms of Sections 1 and 2 above on a medium  
customarily used for software interchange; or,

c) Accompany it with the information you received as to the offer  
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allowed only for noncommercial distribution  
and only if you  
received the program in object code or executable form with such  
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The source code for a work means the preferred form of the work for  
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code means all the source code for all modules it contains, plus any  
associated interface definition files, plus the scripts used to  
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To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the program's name and a brief idea of what it does.>
Copyright (C) <year> <name of author>
```

```
This program is free software; you can redistribute it and/or modify
it under the terms of the GNU
```

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Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this  
when it starts in an interactive mode:

```
Gnomovision version 69, Copyright (C) year name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute
it
under certain conditions; type `show c' for details.
```

The hypothetical commands `show w' and `show c' should show the appropriate  
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be called something other than `show w' and `show c'; they could even be  
mouse-clicks or menu items--whatever suits your program.

You should also get your employer (if you work as a programmer) or your  
school, if any, to sign a "copyright disclaimer" for the program, if  
necessary. Here is a sample; alter the names:

```
Yoyodyne, Inc., hereby disclaims all copyright interest in the program
`Gnomovision' (which makes passes at compilers) written by James Hacker.
```

```
<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice
```

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Version 2.1, February 1999

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Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting

the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation

and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

- a) The modified work must itself be a software library.
- b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.
- c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.
- d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any

application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

### 3. You may opt to apply the terms of the ordinary GNU General Public

License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

### 4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding

machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse



engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

- a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)
- b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.
- c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
- d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
- e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is

normally

distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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heading of "derived work".

The header files "include/image.h" and "include/asm-\*/u-boot.h"  
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4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or

linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

- a) Accompany

the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

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However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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#### Appendix: How to Apply These Terms to Your New Libraries

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## 1.277 sysv-init 2.86-1

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The of the start-stop-daemon

- \* A rewrite
- of the original Debian's start-stop-daemon Perl script
- \* in C (faster - it is executed many times during system startup).
- \*
- \* Written by Marek Michalkiewicz <[marekm@i17linuxb.ists.pwr.wroc.pl](mailto:marekm@i17linuxb.ists.pwr.wroc.pl)>,
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## 1.278 hd-parm 9.56

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This program comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.
```

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<PFX>Free Software Foundation, either version 3 of the License, or
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## 1.282 chardet 3.0.4-2.ph4

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Ext headers	GL/gltext.h	Khronos
	GL/gltext.h	Khronos
	GL/wglext.h	Khronos
	KHR/khrplatform.h	Khronos

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include/GL/glxext.h
include/GL/wglxext.h :
```

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## jopt-simple v5.0.4

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</pre>

# 1.289 pango 1.42.4

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## 1.290 python-certifi 2018.08.13

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This package contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#

This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree:

<http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#>

It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php\_curl, or with an Apache+mod\_ssl webserver for SSL client authentication. Just configure this file as the SSLCACertificateFile.#

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@(#) \$RCSfile: certdata.txt,v \$

\$Revision: 1.80 \$ \$Date: 2011/11/03 15:11:58 \$

## 1.291 six 1.15.0-3.ph4

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# 1.292 idna 10.0.0

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# 1.301 libaio 0.3.111

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# 1.303 lvm2 2.02.180

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## 1.304 cavium-liquidio 5.1.0

### 1.304.1 Available under license :

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```
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```

```
OCTEON-SDK/bootloader/emmc-boot/tools/Makefile | 11 ++++++-----
OCTEON-SDK/host/coredump-util/Makefile | 9 ++++++---
OCTEON-SDK/host/debug-agent/Makefile | 9 ++++++---
OCTEON-SDK/host/remote-utils/Makefile | 6 +++++-
4 files changed, 23 insertions(+), 12 deletions(-)
```

```
diff --git a/OCTEON-SDK/bootloader/emmc-boot/tools/Makefile b/OCTEON-SDK/bootloader/emmc-
boot/tools/Makefile
index da570c4..0fcc1b7 100644
--- a/OCTEON-SDK/bootloader/emmc-boot/tools/Makefile
+++ b/OCTEON-SDK/bootloader/emmc-boot/tools/Makefile
```

```
@@ -2,20 +2,23 @@ CFLAGS = -g -O0 -W -Wall -Wno-unused-parameter -I$(OCTEON_ROOT)/executive
LDFLAGS = -O2 -lz
```

```
.PHONY: all
```

```
all: write_stage1 translate_env
- - sudo cp -pf $^ $(OCTEON_ROOT)/host/bin
+#CISCO_MODIFICATION - commented out
+# - sudo cp -pf $^ $(OCTEON_ROOT)/host/bin
```

```
write_stage1: write_stage1.o
$(CC) $(LDFLAGS) -o $@ $?
- - sudo chown -f root $@
- - sudo chmod -f +s,g+r,o+r $@
+#CISCO_MODIFICATION - commented out
+# - sudo chown -f root $@
+#CISCO_MODIFICATION - commented out
+# - sudo chmod -f +s,g+r,o+r $@
```

```
translate_env: translate_env.o
$(CC) $(LDFLAGS) -o $@ $?
```

```
%.o: %.c
$(CC) $(CFLAGS) -c $< -o $@
```

```
clean:
```

```
- rm -f write_stage1 *.o
\ No newline at end of file
+ rm -f write_stage1 *.o
diff --git a/OCTEON-SDK/host/coredump-util/Makefile b/OCTEON-SDK/host/coredump-util/Makefile
index ebe04f5..f386adc 100644
--- a/OCTEON-SDK/host/coredump-util/Makefile
+++ b/OCTEON-SDK/host/coredump-util/Makefile
@@ -31,14 +31,17 @@ STRIP=$(CROSS)strip
```

```
BINS=$(DIR)/oct-remote-coredump
```

```
all: $(BINS) $(LIBS)
- $(STRIP) $(BINS)
- - sudo chown -f root $(BINS)
- - sudo chmod -f +s,g+r,o+r $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chown -f root $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chmod -f +s,g+r,o+r $(BINS)
```

```
$(DIR)/oct-remote-coredump:
```

```
oct-remote-coredump.c oct-remote-coredump-elf.c $(LIBS)
$(CC) $(CFLAGS) -o $@ $^ $(LIBS) $(LIBS_FLAGS)
```

clean:

```
-- sudo rm -f $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo rm -f $(BINS)
- rm -f $(BINS)
diff --git a/OCTEON-SDK/host/debug-agent/Makefile b/OCTEON-SDK/host/debug-agent/Makefile
index 40726c5..63dad9a 100644
--- a/OCTEON-SDK/host/debug-agent/Makefile
+++ b/OCTEON-SDK/host/debug-agent/Makefile
@@ -28,14 +28,17 @@ STRIP=$(CROSS)strip
```

BINS=\$(DIR)/oct-debug-agent

```
all: $(BINS) $(LIBS)
- $(STRIP) $(BINS)
-- sudo chown -f root $(BINS)
-- sudo chmod -f +s,g+r,o+r $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chown -f root $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chmod -f +s,g+r,o+r $(BINS)
```

```
$(DIR)/oct-debug-agent: agent.c proxy.c remote.c $(LIBS)
$(CC) $(CFLAGS) -o $@ $^ $(LIBS) $(LIBS_FLAGS)
```

clean:

```
-- sudo rm -f $(DIR)/oct-debug-agent
+#CISCO_MODIFICATION - commented out
+# - sudo rm -f $(DIR)/oct-debug-agent
- rm -f $(DIR)/oct-debug-agent
diff
--git a/OCTEON-SDK/host/remote-utils/Makefile b/OCTEON-SDK/host/remote-utils/Makefile
index 70a73e0..4935119 100644
--- a/OCTEON-SDK/host/remote-utils/Makefile
+++ b/OCTEON-SDK/host/remote-utils/Makefile
@@ -56,13 +56,15 @@ BINS = \
$(DIR)/oct-remote-mmc
```

.PHONY: all

```
all: $(BINS)
- $(STRIP) $(BINS)
-- sudo chown -f root $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chown -f root $(BINS)
ifndef OCTEON_LIQUIDIO
```



```
- - sudo chmod -f +s,g+r,o+r $(BINS)
+#CISCO_MODIFICATION - commented out
+# - sudo chmod -f +s,g+r,o+r $(BINS)
endif
```

```
CVMX_COMMON_FILES = $(OCTEON_ROOT)/executive/cvmx-clock.c \
$(OCTEON_ROOT)/executive/cvmx-l2c.c \
$(OCTEON_ROOT)/executive/cvmx-twsi.c \
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/0001-Removed-unnecessary-use-of-sudo-from-makefiles.patch
```

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```
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```

```
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```

```
diff -urN OCTEON-SDK-old/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c OCTEON-SDK/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c
```

```
--- OCTEON-SDK-old/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c 2016-04-11 17:26:49.408984670 -0700
```

```
+++ OCTEON-SDK/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c 2016-04-11 17:25:32.188857052 -0700
```

```
@@ -153,6 +153,7 @@
```

```
#include "configs/octeon_sff7800_shared.h"
```

```
#include "configs/octeon_niagara83x_shared.h"
```

```
#include "configs/octeon_rainier_shared.h"
```

```
+#include "configs/octeon_kilburn_shared.h"
```

```
#ifdef __U_BOOT__
```

```
include <asm/arch/octeon_eeprom_types.h>
```

```
#else
```

```
@@ -11790,6 +11791,11 @@
```

```
 { OCTEON_CLARK_SPD_VALUES };
```

```
#endif
```

```
+#if !defined(CVMX_BUILD_FOR_UBOOT) || defined(CONFIG_OCTEON_KILBURN)
```

```
+static uint8_t __attribute__((unused)) octeon_kilburn_cfg0_spd_values[256] =
```

```
+ {
```

```
 OCTEON_KILBURN_CFG0_SPD_VALUES };
```

```
+#endif
```

```
+
```

```
#if !defined(CVMX_BUILD_FOR_UBOOT) || \
```

```
 defined(CONFIG_OCTEON_NAS7000_REF_EMMC_STAGE2) || \
```

```
 (defined(CONFIG_OCTEON_NAS7000_REF) && defined(unix))
```

```
@@ -12215,6 +12221,18 @@
```

```
}
```

```
},
```

```
endif
```

```

+# if !defined(CVMX_BUILD_FOR_UBOOT) || \
+ defined(CONFIG_OCTEON_KILBURN)
+ {
+ .board_type = CVMX_BOARD_TYPE_CUST_KILBURN,
+ .eeprom_addr = OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR,
+ .chip_ddr_config = {
+ {
+ .ddr_config = {
OCTEON_KILBURN_DDR_CONFIGURATION(OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION)
+ }
+ }
+ },
+# endif
if defined(CONFIG_OCTEON_RAINIER) || !defined(CVMX_BUILD_FOR_UBOOT)
+ {
+ .board_type = CVMX_BOARD_TYPE_RAINIER,
diff -urN OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/config.mk OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/config.mk
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/config.mk 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/config.mk 2016-04-11
17:24:08.439857563 -0700
@@ -0,0 +1,35 @@
+#
+# (C) Copyright 2003
+# Wolfgang Denk, DENX Software Engineering, wd@denx.de.
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+# Cavium Inc., Inc. support@cavium.com
+#
+# See file CREDITS for list of people who contributed to this
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+# along with this program; if not, write to the Free Software
+# Foundation, Inc., 59 Temple Place, Suite 330, Boston,
+# MA 02111-1307
+ USA
+ }

```

```

+
+#
+# Oocteon CN63XX
+#
+
+CONFIG_SYS_TEXT_BASE = 0xC0000000
+PLATFORM_CPPFLAGS += -mtune=octeon2 -finline -ffunction-sections \
+ -fdata-sections -free-pre -fno-asynchronous-unwind-tables \
+ -DCVMX_ENABLE_PARAMETER_CHECKING=0 \
+ -DCVMX_ENABLE_DEBUG_PRINTS=1
+#PLATFORM_CPPFLAGS += -DCVMX_ENABLE_CSR_ADDRESS_CHECKING=1
diff -urN OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c OCTEON-
SDK/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c 1969-12-31 16:00:00.000000000 -
0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c 2016-04-11 17:24:08.439857563 -
0700
@@ -0,0 +1,576 @@
+/*
+ * (C) Copyright 2015 Cavium Inc. <support@cavium.com>
+ *
+ * SPDX-License-Identifier: GPL-2.0+
+ */
+
+#include <common.h>
+#include <asm/mipsregs.h>
+#include <asm/arch/octeon_boot.h>
+#include <asm/arch/cvmx-sim-magic.h>
+#include <asm/arch/cvmx-qlm.h>
+#include <asm/arch/octeon_fdt.h>
+#include
<fdt_support.h>
+#include <asm/arch/octeon_board_common.h>
+#include <asm/arch/lib_octeon.h>
+#include <asm/arch/lib_octeon_shared.h>
+#include <asm/arch/octeon_qlm.h>
+#include <asm/arch/cvmx-helper-util.h>
+#include <asm/arch/cvmx-helper-cfg.h>
+#include <asm/arch/cvmx-smi-defs.h>
+#include <asm/gpio.h>
+
+#include <command.h>
+#include <spi_flash.h>
+#include <spi.h>
+#define MAX_MIX_ENV_VARS 4
+
+extern void print_isp_volt(const char **labels, uint16_t isp_dev_addr, uint8_t adc_chan);
+

```

```

+extern int read_ispPAC_mvolt(uint16_t isp_dev_addr, uint8_t adc_chan);
+
+extern int read_ispPAC_mvolt_avg(int loops, uint16_t isp_dev_addr, uint8_t adc_chan);
+
+DECLARE_GLOBAL_DATA_PTR;
+
+/*
+ * parse_env_var: Parse the environment variable ("bgx_for_mix%d") to
+ * extract the lmac it is set to.
+ *
+ * index: Index of environment variable to parse.
+ * environment variable.
+ * env_bgx: Updated with the bgx of the lmac in the environment
+ * variable.
+ * env_lmac: Updated
+ with the index of lmac in the environment
+ * variable.
+ *
+ * returns: Zero on success, error otherwise.
+ */
+static int parse_env_var(int index, int *env_bgx, int *env_lmac)
+{
+ char env_var[20];
+ ulong xipd_port;
+
+ sprintf(env_var, "bgx_for_mix%d", index);
+ if ((xipd_port = getenv_ulong(env_var, 0, 0xffff)) != 0xffff) {
+ int xiface;
+ struct cvmx_xiface xi;
+ struct cvmx_xport xp;
+
+ /*
+ * The environemt variable is set to the xipd port. Convert the
+ * xipd port to numa node, bgx, and lmac.
+ */
+ xiface = cvmx_helper_get_interface_num(xipd_port);
+ xi = cvmx_helper_xiface_to_node_interface(xiface);
+ xp = cvmx_helper_ipd_port_to_xport(xipd_port);
+ *env_bgx = xi.interface;
+ *env_lmac = cvmx_helper_get_interface_index_num(xp.port);
+ return 0;
+ }
+
+ return -1;
+ }
+
+/*
+ * get_lmac_fdt_node: Search the device tree for the node corresponding to

```

```

+ * a given bgx lmac.
+ *
+ * fdt: Pointer to flat device tree
+ * search_node: Numa node of
the lmac to search for.
+ * search_bgx: Bgx of the lmac to search for.
+ * search_lmac: Lmac index to search for.
+ * compat: Compatible string to search for.
+
+ * returns: The device tree node of the lmac if found,
+ * or -1 otherwise.
+ */
+static int get_lmac_fdt_node(const void *fdt, int search_node, int search_bgx,
+ int search_lmac, const char *compat)
+{
+ int node;
+ const fdt32_t *reg;
+ u64 addr;
+ int fdt_node = -1;
+ int fdt_bgx = -1;
+ int fdt_lmac = -1;
+ int len;
+ int parent;
+
+ /* Iterate through all bgx ports */
+ node = -1;
+ while ((node = fdt_node_offset_by_compatible((void *)fdt, node,
+ compat)) >= 0) {
+ /* Get the node and bgx from the physical address */
+ if (((parent = fdt_parent_offset(fdt, node)) < 0) ||
+ !(reg = fdt_getprop(fdt, parent, "reg", &len)))
+ continue;
+
+ addr = fdt_translate_address((void *)fdt, parent, reg);
+ fdt_node = (addr >> 36) & 0x7;
+ fdt_bgx = (addr >> 24) & 0xf;
+
+ /* Get the lmac
index from the reg property */
+ if ((reg = fdt_getprop(fdt, node, "reg", &len)))
+ fdt_lmac = *reg;
+
+ /* Check for a match */
+ if (search_node == fdt_node && search_bgx == fdt_bgx &&
+ search_lmac == fdt_lmac)
+ return node;
+ }
+
+

```

```

+ return -1;
+ }
+
+ /*
+ * get_mix_fdt_node: Search the device tree for the node corresponding to
+ * a given mix.
+ *
+ * fdt: Pointer to flat device tree
+ * search_node: Mix numa node to search for.
+ * search_index: Mix index to search for.
+ *
+ * returns: The device tree node of the lmac if found,
+ * or -1 otherwise.
+ */
+static int get_mix_fdt_node(const void *fdt, int search_node, int search_index)
+{
+ int node;
+
+ /* Iterate through all the mix fdt nodes */
+ node = -1;
+ while ((node = fdt_node_offset_by_compatible((void *)fdt, node,
+ "cavium,octeon-7890-mix")) >= 0) {
+ int parent;
+ int len;
+ const char *name;
+ int mix_numa_node;
+ const fdt32_t *reg;
+ int mix_index = -1;
+ u64 addr;
+
+ /* Get the
+ numa node of the mix from the parent node name */
+ if (((parent = fdt_parent_offset(fdt, node)) < 0) ||
+ ((name = fdt_get_name(fdt, parent, &len)) == NULL) ||
+ ((name = strchr(name, '@')) == NULL))
+ continue;
+
+ name++;
+ mix_numa_node = simple_strtol(name, NULL, 0) ? 1 : 0;
+
+ /* Get the mix index from the reg property */
+ if ((reg = fdt_getprop(fdt, node, "reg", &len))) {
+ addr = fdt_translate_address((void *)fdt, parent, reg);
+ mix_index = (addr >> 11) & 1;
+ }
+
+ /* Check for a match */
+ if (mix_numa_node == search_node && mix_index == search_index)

```

```

+ return node;
+ }
+
+ return -1;
+}
+
+/*
+ * fdt_fix_mix: Fix the mix nodes in the device tree. Only the mix nodes
+ * configured by the user will be preserved. All other mix
+ * nodes will be trimmed.
+ *
+ * fdt: Pointer to flat device tree
+ *
+ * returns: Zero on success, error otherwise.
+ */
+static int fdt_fix_mix(const void *fdt)
+{
+ int node;
+ int next_node;
+ int len;
+ int i;
+
+ /*
+ Parse all the mix port environment variables */
+ for (i = 0; i < MAX_MIX_ENV_VARS; i++) {
+ int env_node = 0;
+ int env_bgx = -1;
+ int env_lmac = -1;
+ int lmac_fdt_node = -1;
+ int mix_fdt_node = -1;
+ int lmac_phandle;
+ char *compat;
+
+ /* Get the lmac for this environment variable */
+ if (parse_env_var(i, &env_bgx, &env_lmac))
+ continue;
+
+ /* Get the fdt node for this lmac and add a phandle to it */
+ compat = "cavium,octeon-7890-bgx-port";
+ if ((lmac_fdt_node = get_lmac_fdt_node(fdt, env_node, env_bgx,
+ env_lmac, compat)) < 0) {
+ /* Must check for the xcv compatible string too */
+ compat = "cavium,octeon-7360-xcv";
+ if ((lmac_fdt_node = get_lmac_fdt_node(fdt, env_node,
+ env_bgx,
+ env_lmac,
+ compat)) < 0) {
+ printf("WARNING: Failed to get lmac fdt node "

```

```

+ "for %d%d%d\n", env_node, env_bgx,
+ env_lmac);
+ continue;
+ }
+ }
+
+ lmac_phandle = fdt_alloc_phandle((void
+ *)fdt);
+ fdt_set_phandle((void *)fdt, lmac_fdt_node, lmac_phandle);
+
+ /* Get the fdt mix node corresponding to this lmac */
+ if ((mix_fdt_node = get_mix_fdt_node(fdt, env_node,
+ env_lmac)) < 0)
+ continue;
+
+ /* Point the mix to the lmac */
+ fdt_getprop(fdt, mix_fdt_node, "cavium,mac-handle", &len);
+ fdt_setprop_inplace((void *)fdt, mix_fdt_node,
+ "cavium,mac-handle", &lmac_phandle, len);
+ }
+
+ /* Trim unused mix'es from the device tree */
+ for (node = fdt_next_node(fdt, -1, NULL); node >= 0;
+ node = next_node) {
+ const char *compat;
+ const fdt32_t *reg;
+
+ next_node = fdt_next_node(fdt, node, NULL);
+
+ if ((compat = fdt_getprop(fdt, node, "compatible", &len))) {
+ if (strcmp(compat, "cavium,octeon-7890-mix"))
+ continue;
+
+ if ((reg = fdt_getprop(fdt, node,
+ "cavium,mac-handle", &len))) {
+ if (*reg == 0xffff)
+ fdt_nop_node((void *)fdt, node);
+ }
+ }
+ }
+
+ return 0;
+ }
+
+ int no_phy[8] = {0, 0,
+ 0, 0, 0, 0, 0, 0};
+
+ static void kill_fdt_phy(void *fdt, int offset, void *arg)

```



```

+{
+ int len, phy_offset;
+ const fdt32_t *php;
+ uint32_t phandle;
+
+ php = fdt_getprop(fdt, offset, "phy-handle", &len);
+ if (php && len == sizeof(*php)) {
+ phandle = fdt32_to_cpu(*php);
+ fdt_nop_property(fdt, offset, "phy-handle");
+ phy_offset = fdt_node_offset_by_phandle(fdt, phandle);
+ if (phy_offset > 0)
+ fdt_nop_node(fdt, phy_offset);
+ }
+}
+
+void __fixup_xcv(void)
+{
+ unsigned long bgx = getenv_ulong("bgx_for_rgmii", 10, (unsigned long)-1);
+ char fdt_key[16];
+ int i;
+
+ debug("%s: BGX %d\n", __func__, (int)bgx);
+
+ for (i = 0; i < 3; i++) {
+ snprintf(fdt_key, sizeof(fdt_key),
+ bgx == i ? "%d,xcv" : "%d,not-xcv", i);
+ debug("%s: trimming bgx %lu with key %s\n",
+ __func__, bgx, fdt_key);
+
+ octeon_fdt_patch_rename((void *)gd->fdt_blob, fdt_key,
+ "cavium,xcv-trim", true, NULL, NULL);
+ }
+}
+
+/* QLM0 - QLM6 */
+void __fixup_fdt(void)
+{
+ int qlm;
+ int speed
+ = 0;
+
+ for (qlm = 0; qlm < 7; qlm++) {
+ enum cvmx_qlm_mode mode;
+ char fdt_key[16];
+ const char *type_str = "none";
+ mode = cvmx_qlm_get_mode(qlm);
+ switch (mode) {
+ case CVMX_QLM_MODE_SGMII:

```

```

+ case CVMX_QLM_MODE_RGMII_SGMII:
+ case CVMX_QLM_MODE_RGMII_SGMII_1X1:
+ type_str = "sgmii";
+ break;
+ case CVMX_QLM_MODE_XAUI:
+ case CVMX_QLM_MODE_RGMII_XAUI:
+ speed = (cvmx_qlm_get_gbaud_mhz(qlm) * 8 / 10) * 4;
+ if (speed == 10000)
+ type_str = "xaui";
+ else
+ type_str = "dxaui";
+ break;
+ case CVMX_QLM_MODE_RXAUI:
+ case CVMX_QLM_MODE_RGMII_RXAUI:
+ type_str = "rxaui";
+ break;
+ case CVMX_QLM_MODE_XLAUI:
+ case CVMX_QLM_MODE_RGMII_XLAUI:
+ type_str = "xlaui";
+ break;
+ case CVMX_QLM_MODE_XFI:
+ case CVMX_QLM_MODE_RGMII_XFI:
+ case CVMX_QLM_MODE_RGMII_XFI_1X1:
+ type_str = "xfi";
+ break;
+ case CVMX_QLM_MODE_10G_KR:
+ case CVMX_QLM_MODE_RGMII_10G_KR:
+ type_str = "10G_KR";
+ break;
+ case CVMX_QLM_MODE_40G_KR4:
+ case CVMX_QLM_MODE_RGMII_40G_KR4:
+ type_str
= "40G_KR4";
+ break;
+ case CVMX_QLM_MODE_SATA_2X1:
+ type_str = "sata";
+ break;
+ case CVMX_QLM_MODE_SGMII_2X1:
+ case CVMX_QLM_MODE_XFI_1X2:
+ case CVMX_QLM_MODE_10G_KR_1X2:
+ case CVMX_QLM_MODE_RXAUI_1X2:
+ case CVMX_QLM_MODE_MIXED: // special for DLM5 & DLM6
+ {
+ cvmx_bgxx_cmr_config_t cmr_config;
+ cvmx_bgxx_spux_br_pmd_control_t pmd_control;
+ int mux = cvmx_qlm_mux_interface(2);
+ if (mux == 2) { // only dlm6
+ cmr_config.u64 = cvmx_read_csr(

```

```

+ CVMX_BGXX_CM_RX_CONFIG(2, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(2, 2));
+ } else {
+ if (qlm == 5) {
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CM_RX_CONFIG(0, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(0, 2));
+ } else {
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CM_RX_CONFIG(2, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(2,
+ 2));
+ }
+ }
+ switch (cmr_config.s.lmac_type) {
+ case 0:
+ type_str = "sgmii";
+ break;
+ case 1:
+ type_str = "xaui";
+ break;
+ case 2:
+ type_str = "rxaui";
+ break;
+ case 3:
+ if (pmd_control.s.train_en)
+ type_str = "10G_KR";
+ else
+ type_str = "xfi";
+ break;
+ case 4:
+ if (pmd_control.s.train_en)
+ type_str = "40G_KR4";
+ else
+ type_str = "xlaui";
+ break;
+ default:
+ type_str = "none";
+ break;
+ }
+ break;
+ }
+ default:
+ type_str = "none";
+ break;

```

```

+ }
+ sprintf(fdt_key, "%d,%s", qlm, type_str);
+ debug("Patching qlm %d for %s for mode %d%s\n",
+ qlm, fdt_key, mode, no_phy[qlm] ? ", removing PHY" : "");
+ octeon_fdt_patch_rename((void *)gd->fdt_blob, fdt_key, NULL,
+ true, no_phy[qlm] ? kill_fdt_phy : NULL, NULL);
+ }
+}
+
+int board_fixup_fdt(void)
+{
+ __fixup_fdt();
+ __fixup_xcv();
+
+ /* Fix the mix ports */
+ fdt_fix_mix(gd->fdt_blob);
+
+ return 0;
+}
+
+/*
+ Raise an integer to a power */
+static uint64_t ipow(uint64_t base, uint64_t exp)
+{
+ uint64_t result = 1;
+ while (exp) {
+ if (exp & 1)
+ result *= base;
+ exp >>= 1;
+ base *= base;
+ }
+ return result;
+}
+
+/*
+ * This function now supports only single (node0) configuration.
+ * Some of the called funcs (read_ispPAC_mvols_avg(), octeon_led_str_write())
+ * are hard-coded for node0. Also we set gd->arch.mcu_rev_maj, ...
+ * It can be extended to support multinode if needed/important
+ */
+static int checkboardinfo(void)
+{
+ int core_mVolts, dram_mVolts0, dram_mVolts1;
+ char mcu_ip_msg[64] = { 0 };
+ char tmp[10];
+ int characters, idx = 0, value = 0;
+
+ debug("In %s\n", __func__);

```

```

+ if (octeon_show_info()) {
+
+ int mcu_rev_maj = 0;
+ int mcu_rev_min = 0;
+
+ if (twsii_mcu_read(0x00) == 0xa5
+ && twsii_mcu_read(0x01) == 0x5a) {
+ gd->arch.mcu_rev_maj = mcu_rev_maj = twsii_mcu_read(2);
+ gd->arch.mcu_rev_min = mcu_rev_min = twsii_mcu_read(3);
+ }
+ else {
+ return -1; /* Abort if we can't access the MCU */
+ }
+
+ core_mVolts = read_ispPAC_mvols_avg(10, BOARD_ISP_TWSI_ADDR, 8);
+ dram_mVolts0 = read_ispPAC_mvols_avg(10, BOARD_ISP_TWSI_ADDR, 7); /* DDR0 */
+ dram_mVolts1 = read_ispPAC_mvols_avg(10, BOARD_ISP_TWSI_ADDR, 6); /* DDR1 */
+
+ if (twsii_mcu_read(0x14) == 1)
+ sprintf(mcu_ip_msg, "MCU IPaddr: %d.%d.%d.%d, ",
+ twsii_mcu_read(0x10),
+ twsii_mcu_read(0x11),
+ twsii_mcu_read(0x12),
+ twsii_mcu_read(0x13));
+ printf("MCU rev: %d.%02d, %sCPU voltage: %d.%03d DDR{0,1} voltages: %d.%03d,%d.%03d\n",
+ gd->arch.mcu_rev_maj, gd->arch.mcu_rev_min, mcu_ip_msg,
+ core_mVolts / 1000, core_mVolts % 1000,
+ dram_mVolts0 / 1000, dram_mVolts0 % 1000,
+ dram_mVolts1 / 1000, dram_mVolts1 % 1000);
+
+ #define LED_CHARACTERS 8
+ value = core_mVolts;
+ idx = sprintf(tmp, "%lu ", gd->cpu_clk/(1000*1000));
+ characters = LED_CHARACTERS - idx;
+
+ if
+ (value / 1000) {
+ idx += sprintf(tmp + idx, "%d", value / 1000);
+ characters = LED_CHARACTERS - idx;
+ }
+
+ characters -= 1; /* Account for decimal point */
+
+ value %= 1000;
+ value = DIV_ROUND_UP(value, ipow(10, max(3 - characters, 0)));
+
+ idx += sprintf(tmp + idx, ".%0*d", min(3, characters), value);
+
+

```

```

+ /* Display CPU speed and voltage on display */
+ octeon_led_str_write(tmp);
+ } else {
+ octeon_led_str_write("Boot ");
+ }
+
+ return 0;
+}
+
+/**
+ * Here is the description of the parameters that are passed to QLM configuration
+ * param0 : The QLM to configure
+ * param1 : Speed to configure the QLM at
+ * param2 : Mode the QLM to configure
+ * param3 : 1 = RC, 0 = EP
+ * param4 : 0 = GEN1, 1 = GEN2, 2 = GEN3
+ * param5 : ref clock select, 0 = 100Mhz, 1 = 125MHz, 2 = 156MHz
+ * param6 : ref clock input to use:
+ * 0 - external reference (QLMx_REF_CLK)
+ * 1 = common clock 0 (QLMC_REF_CLK0)
+ * 2 = common_clock 1 (QLMC_REF_CLK1)
+ */
+void board_configure_qlms(void)
+{
+ int
+ qlm;
+ char env_var[16];
+ int node = 0;
+ int pcie_gen;
+ const char *mode_str;
+ int rc;
+ uint8_t qlm45_gpio;
+ bool qlm4_present, qlm5_present;
+
+ octeon_init_qlm(0);
+
+ mdelay(100);
+
+ cvmx_coremask_for_each_node(node, gd->arch.node_mask) {
+ debug("Configuring node %d, QLM 0 and 1 \n", node);
+ snprintf(env_var, sizeof(env_var), "pcie0:%d_gen", node);
+ pcie_gen = getenv_ulong(env_var, 10, 3) - 1;
+ octeon_configure_qlm(0, 8000, CVMX_QLM_MODE_PCIE_1X8, 0, 2, 0, 1); /* PCIEG3X8 */
+ octeon_configure_qlm(1, 8000, CVMX_QLM_MODE_PCIE_1X8, 0, 2, 0, 1); /* PCIEG3X8 */
+
+ debug("Configuring node %d, QLMs 2 for XLAUI\n", node);
+ octeon_configure_qlm(2, 103125, CVMX_QLM_MODE_XLAUI, 0, 0, 2, 2); /* 40G_KR4 */

```

```

+ octeon_qlm_tune_v3(0, 2, 103125, 0xd, 0xd0);
+
+/* For other skews of kilburn, uncomment them */
+
+ printf("Configuring node %d, QLMs 3 for XAUI\n", node);
+ octeon_configure_qlm(3,
+ 103125, CVMX_QLM_MODE_XFI, 0, 0, 2, 2); /* 10G_KR */
+ octeon_qlm_tune_v3(0, 3, 103125, 0xd, 0xd0);
+
+ }
+}
+
+int checkboard(void)
+{
+ checkboardinfo();
+
+ return 0;
+}
+
+int early_board_init(void)
+{
+ octeon_board_get_clock_info(KILBURN_DEF_DRAM_FREQ);
+ octeon_board_get_descriptor(CVMX_BOARD_TYPE_CUST_KILBURN, 1, 0);
+
+ /* Even though the CPU ref freq is stored in the clock descriptor, we
+ * don't read it here. The MCU reads it and configures the clock, and
+ * we read how the clock is actually configured.
+ * The bootloader does not need to read the clock descriptor tuple for
+ * normal operation on rev 2 and later boards.
+ */
+ octeon_board_get_mac_addr();
+
+ return 0;
+}
diff -urN OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn.dts OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/kilburn.dts
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn.dts 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/kilburn.dts 2016-04-11
17:24:08.439857563 -0700
@@ -0,0 +1,181 @@
+/dts-v1/;
+/*
+ * Cavium Inc. Kilburn 76XX
+ */
+ / {
+ model = "cavium,kilburn";
+ compatible = "cavium,kilburn";
+ #address-cells = <2>;

```

```

+ #size-cells = <2>;
+
+ soc@0 {
+ interrupt-parent = <&ciu3>;
+ compatible = "simple-bus";
+ #address-cells = <2>;
+ #size-cells = <2>;
+ ranges; /* Direct mapping */
+ cavium,node-trim = "0,soc";
+
+ ciu3: interrupt-controller@101000000000 {
+ compatible = "cavium,octeon-7890-ciu3";
+ interrupt-controller;
+ /* Interrupts are specified by two parts:
+ * 1) Source number (20 significant bits)
+ * 2) Trigger type: (4 == level, 1 == edge)
+ */
+ #address-cells = <0>;
+ #interrupt-cells = <2>;
+ reg = <0x10100 0x00000000 0x0 0xb0000000>;
+ };
+
+ bootbus: bootbus@118000000000 {
+ compatible = "cavium,octeon-3860-bootbus";
+ reg = <0x11800 0x00000000 0x0 0x200>;
+ /* The chip select number and offset */
+ #address-cells = <2>;
+ /* The size of the
+ chip select region */
+ #size-cells = <1>;
+ ranges = <0 0 0 0x1ec00000 0x1400000>,
+ <1 0 0x10000 0x10000000 0>,
+ <2 0 0x10000 0x20000000 0>,
+ <3 0 0x10000 0x30000000 0>,
+ <4 0 0x10000 0x40000000 0>,
+ <5 0 0x10000 0x50000000 0>,
+ <6 0 0x10000 0x60000000 0>,
+ <7 0 0x10000 0x90000000 0>;
+
+ cavium,cs-config@0 {
+ compatible = "cavium,octeon-3860-bootbus-config";
+ cavium,cs-index = <0>;
+ cavium,t-adr = <10>;
+ cavium,t-ce = <100>;
+ cavium,t-oe = <30>;
+ cavium,t-we = <25>;
+ cavium,t-rd-hld = <0>;
+ cavium,t-wr-hld = <20>;

```



```

+ cavium,t-pause = <10>;
+ cavium,t-wait = <0>;
+ cavium,t-page = <20>;
+ cavium,t-rd-dly = <0>;
+
+ cavium,page-mode = <1>;
+ cavium,pages = <8>;
+ cavium,bus-width = <16>;
+ cavium,ale-mode = <1>;
+ };
+
+ flash0: nor@0,0 {
+ compatible = "cfi-flash";
+ reg = <0 0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <1>;
+
+ partition@0
+ {
+ label = "bootloader";
+ reg = <0 0x340000>;
+ read-only;
+ };
+ partition@300000 {
+ label = "storage";
+ reg = <0x340000 0xcbe000>;
+ };
+ partition@fe0000 {
+ label = "environment";
+ reg = <0xfe0000 0x20000>;
+ read-only;
+ };
+ };
+ };
+
+ serial@1180000000800 {
+ compatible = "cavium,octeon-3860-uart","ns16550";
+ reg = <0x11800 0x00000800 0x0 0x400>;
+ clock-frequency = <0>;
+ current-speed = <115200>;
+ reg-shift = <3>;
+ interrupts = <0x08000 4>;
+ };
+
+ serial@1180000000c00 {
+ compatible = "cavium,octeon-3860-uart","ns16550";
+ reg = <0x11800 0x00000c00 0x0 0x400>;
+ clock-frequency = <0>;

```

```

+ current-speed = <115200>;
+ reg-shift = <3>;
+ interrupts = <0x08040 4>;
+ };
+
+ gpio0: gpio-controller@1070000000800 {
+ #gpio-cells = <2>;
+ compatible = "cavium,octeon-7890-gpio";
+ reg = <0x10700 0x00000800 0x0 0x100>;
+ gpio-controller;
+ /* Interrupts are specified by two
parts:
+ * 1) GPIO pin number (0..15)
+ * 2) Triggering (1 - edge rising
+ * 2 - edge falling
+ * 4 - level active high
+ * 8 - level active low)
+ */
+ interrupt-controller;
+ #interrupt-cells = <2>;
+ /* The GPIO pins connect to 16 consecutive CUI bits */
+ interrupts = <0x03000 4>, <0x03001 4>, <0x03002 4>, <0x03003 4>,
+ <0x03004 4>, <0x03005 4>, <0x03006 4>, <0x03007 4>,
+ <0x03008 4>, <0x03009 4>, <0x0300a 4>, <0x0300b 4>,
+ <0x0300c 4>, <0x0300d 4>, <0x0300e 4>, <0x0300f 4>;
+ };
+
+
+
+ /* BGX definitions here must match kilburn_board.c settings. */
+ /* BGX 0*/
+ ethernet-mac-nexus@11800e0000000 {
+ compatible = "cavium,octeon-7890-bgx";
+ reg = <0x11800 0xe0000000 0x0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <0>;
+
+ /* SerDes 0, may differ from PCS Lane/LMAC */
+ ethernet-mac@0 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <0>;
+ local-mac-address = [00 00 00 00 00 00
];
+ cavium,qlm-trim = "2,xlaui";
+ };
+ };
+
+
+ /* BGX 1*/

```

```

+ ethernet-mac-nexus@11800e1000000 {
+ compatible = "cavium,octeon-7890-bgx";
+ reg = <0x11800 0xe1000000 0x0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <0>;
+ ethernet-mac@0 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <0>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@1 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <1>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@2 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <2>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@3 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <3>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+
+ };
+ };
+ };
diff -urN OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/Makefile
OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/Makefile
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/Makefile 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/Makefile 2016-04-11 17:24:08.439857563 -0700
@@ -0,0 +1,68 @@
+#
+# (C) Copyright 2003
+# Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+#
+# See file CREDITS for list of people who contributed to this
+# project.
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```

```

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of the GNU General Public License
+# along with this program; if not, write to the Free Software
+# Foundation, Inc., 59 Temple Place, Suite 330, Boston,
+# MA 02111-1307 USA
+#
+#
+include $(TOPDIR)/config.mk
+
+CFLAGS += -I$(TOPDIR)/arch/mips/include/asm/arch-octeon
+
+LIB = $(obj)lib$(BOARD).o
+
+COBJS = $(BOARD)_board.o
+SOBJS =
+COBJS-y =
+#OBS = $(BOARD).o flash.o
+
+SRCS := $(SOBJS:.o=.S) $(COBJS:.o=.c) $(COBJS-y:.o=.c)
+OBS := $(addprefix $(obj),$(COBJS))
+SOBJS := $(addprefix $(obj),$(SOBJS))
+
+all: $(obj).depend $(LIB)
+
+DTB = $(BOARD).dtb.o
+DTBOBJS := $(addprefix $(obj),$(DTB))
+
+$(BOARD).dtb.o: $(BOARD).dtb.S
+
+$(BOARD).dtb.S: $(BOARD).dtb
+ echo '.section .rodata, "a"' > $@
+ echo '.balign 8' >> $@
+ echo '.global __dtb_begin' >> $@
+ echo '__dtb_begin:' >> $@
+ echo '.incbin "$<"' >> $@
+ echo '__dtb_end:' >> $@
+ echo '.global __dtb_end' >> $@
+ echo '.balign 8' >> $@
+
+$(BOARD).dtb: $(BOARD).dts
+ $(TOPDIR)/tools/dtc -O dtb -p 1024 -o $@ $<

```

```

+
+$(LIB): $(obj).depend
$(OBJS) $(SOBJS) $(DTBOBJS)
+ $(call cmd_link_o_target, $(OBJS) $(SOBJS) $(DTBOBJS))
+
+#####
+include $(SRCTREE)/rules.mk
+
+
+sinclude $(obj).depend
+
+clean:
+ -rm -f $(BOARD).dtb $(BOARD).dtb.S
diff -urN OCTEON-SDK-old/bootloader/u-boot/boards.cfg OCTEON-SDK/bootloader/u-boot/boards.cfg
--- OCTEON-SDK-old/bootloader/u-boot/boards.cfg 2016-04-11 17:45:55.361670048 -0700
+++ OCTEON-SDK/bootloader/u-boot/boards.cfg 2016-04-11 17:23:03.316970283 -0700
@@ -112,3 +112,4 @@
Target ARCH CPU Board name Vendor SoC Options
#####
#####
octeon_rainier mips octeon rainier octeon -
octeon_rainier:OCTEON_RAINIER,OCTEON_PCI_HOST=1
+octeon_kilburn mips octeon kilburn octeon -
octeon_kilburn:OCTEON_KILBURN,OCTEON_PCI_HOST=0
diff
-urN OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn.h OCTEON-SDK/bootloader/u-
boot/include/configs/octeon_kilburn.h
--- OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn.h 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn.h 2016-04-11 17:24:54.087755446 -0700
@@ -0,0 +1,279 @@
+/*
+ * (C) Copyright 2003
+ * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+ *
+ * Copyright (C) 2015 Cavium, Inc. <support@cavium.com>
+ *
+ * SPDX-License-Identifier: GPL-2.0+
+ */
+
+
+/* This file contains the configuration parameters for
+ * Oocteon EBB7304 board.
+ */
+
+
+#ifndef __CONFIG_H__
+#define __CONFIG_H__
+
+
+/**
+ * Define CONFIG_OCTEON_PCI_HOST = 1 to map the pci devices on the

```

```

+ * bus. Define CONFIG_OCTEON_PCI_HOST = 0 for target mode when the
+ * host system performs the pci bus mapping instead. Note that pci
+ * commands are enabled to allow access to configuration
space for
+ * both modes.
+ */
+#ifndef CONFIG_OCTEON_PCI_HOST
+# define CONFIG_OCTEON_PCI_HOST 1
+#endif
+
+/** Enable USB support on OCTEON III */
+#define CONFIG_OCTEON_USB_OCTEON3
+
+#define CONFIG_OCTEON_BIG_STACK_SIZE 0x4000 /** 16K stack */
+
+#include "octeon_common.h"
+
+/* CONFIG_OCTEON_KILBURN set by Makefile in include/config.h */
+
+/** Default DDR clock if tuple doesn't exist in EEPROM */
+#define KILBURN_DEF_DRAM_FREQ 1050
+
+// #define CONFIG_OCTEON_ENABLE_LED_DISPLAY /** 8-char LED support */
+
+#define CONFIG_LBA48 /** 48-bit mode */
+#define CONFIG_SYS_64BIT_LBA /** 64-bit LBA support */
+#define CONFIG_SYS_ATA_BASE_ADDR 0 /** Make compile happy */
+/* Base address of Common memory for Compact flash */
+
+/* eMMC support */
+#define CONFIG_OCTEON_MMC /** Enable MMC support */
+#define CONFIG_MMC_MBLOCK /** Multi-block support */
+#define CONFIG_CMD_MMC /** Enable mmc command */
+#define CONFIG_SYS_MMC_SET_DEV /** Enable multiple MMC devices */
+#define
CONFIG_MMC /** Enable MMC support */
+#define CONFIG_OCTEON_MIN_BUS_SPEED_HZ 100000 /** Minimum MMC bus speed */
+
+/** Maximum number of MMC devices needed for API */
+#define CONFIG_SYS_MMC_MAX_DEVICE 2
+
+#if 0
+/** Enable support for native U-Boot application API */
+#define CONFIG_API
+#endif
+
+
+/* TWSI address for the ispPAC-POWR1220AT8 Voltage Controller chip */

```

```

+#define BOARD_ISP_TWSI_ADDR 0x08
+#define CONFIG_ISPPAC_POWER
+
+/** Address of MCU on TWSI bus */
+#define BOARD_MCU_TWSI_ADDR 0x60
+
+/** Address of board EEPROM on TWSI bus */
+#define CONFIG_SYS_I2C_EEPROM_ADDR OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR
+/** Default EEPROM address */
+#define CONFIG_SYS_DEF_EEPROM_ADDR CONFIG_SYS_I2C_EEPROM_ADDR
+/* These speed up writes to the serial EEPROM by enabling page writes.
+ * Please see the datasheet for details.
+ */
+#define CONFIG_SYS_EEPROM_PAGE_WRITE_BITS 5 /* 32 bytes */
+#define CONFIG_SYS_EEPROM_PAGE_WRITE_DELAY_MS 5
+
+/* Set bootdelay to 0 for immediate
boot */
+#define CONFIG_BOOTDELAY 0 /** autoboot after X seconds */
+
+#undef CONFIG_BOOTARGS
+
+/** Enable octbootbus command */
+#define CONFIG_CMD_OCTEON_BOOTBUS
+
+/**
+ * The 'mtdids' environment variable is used in conjunction with the 'mtdparts'
+ * variable to define the MTD partitions for u-boot.
+ */
+#define MTDPARTS_DEFAULT \
+ "mtdparts=octeon_nor0:3m(bootloader)ro," \
+ "2m(kernel)," \
+ "3064k(cramfs)," \
+ "8k(environment)ro;" \
+ "octeon_nand0:6m(bootloader)ro," \
+ "2m(environment)," \
+ "8m(kernel)," \
+ "512m(rootfs)\0"
+
+#define MTDIDS_DEFAULT "nor0=octeon_nor0,nand0=octeon_nand0\0"
+
+/**
+ * End of the NAND boot partition which is searched for a bootloader. Must be
+ * on an erase block boundary. It should be large enough to be able to
+ handle
+ * at least one bad block.
+ */
+#define CONFIG_OCTEON_NAND_BOOT_END 0x600000

```

```

+
+
+/* Define this to enable built-in octeon ethernet support */
+#define CONFIG_OCTEON_SGMII_ENET /** SGMII ethernet support */
+#define CONFIG_OCTEON_XAUI_ENET /** XAUI ethernet support */
+
+/* Enable Octeon built-in networking if RGMII support is enabled */
+#if defined(CONFIG_OCTEON_RGMII_ENET) || defined(CONFIG_OCTEON_SGMII_ENET) || \
+ defined(CONFIG_OCTEON_XAUI_ENET) || defined(CONFIG_OCTEON_QSGMII_ENET)
+# define CONFIG_OCTEON_INTERNAL_ENET
+#endif
+#define CONFIG_OCTEON_MGMT_ENET
+
+/* PCI console is supported since oct-remote-boot is supported.
+ * Enable MUX and oct-remote-bootcmd support as well
+ */
+#define CONFIG_SYS_PCI_CONSOLE
+#define CONFIG_CONSOLE_MUX
+#define CONFIG_OCTEON_BOOTCMD
+
+#include "octeon_cmd_conf.h"
+
+#ifdef CONFIG_CMD_NET
+/**
+ * Define available PHYs
+ */
+# define CONFIG_PHY_GIGE /** Gigabit Ethernet PHY support */
+# define CONFIG_PHY_MARVELL /**
+ Marvell PHY support */
+# define CONFIG_PHY_VITESSE /** Vitesse PHY support */
+# define CONFIG_PHY_BROADCOM /** Broadcom PHY support */
+# define CONFIG_PHY_CORTINA /** Cortina PHY support */
+# include "octeon_network.h"
+#endif
+
+
+
+#define CONFIG_CMD_OCTEON_TLVEEPROM /** "tlv_eeeprom" command */
+/* "qlm" command */
+#define CONFIG_CMD_QLM /** QLM command */
+
+/* Enable MTD partitioning support */
+#define CONFIG_CMD_MTDPARTS
+
+/* Enable JFFS2 support */
+#define CONFIG_CMD_JFFS2
+
+/* Enable "date" command to set and get date from RTC */

```



```

+#define CONFIG_CMD_DATE
+#define CONFIG_CMD_FLASH /** flinfo, erase, protect */
+#define CONFIG_CMD_EXT2 /** EXT2/3 filesystem support */
+#define CONFIG_CMD_EXT4 /** EXT4 filesystem support */
+#define CONFIG_CMD_FAT /** FAT support */
+#define CONFIG_FAT_WRITE /** FAT write support */
+
+/* SPI NOR flash support */
+#define CONFIG_SF_DEFAULT_BUS 0 /** SPI flash default bus */
+#define CONFIG_SF_DEFAULT_CS 0 /** SPI flash default
chip select */
+#define CONFIG_SF_DEFAULT_SPEED 50000000 /** Default speed in MHz */
+
+#define CONFIG_OCTEON_SPI /** Enable OCTEON SPI driver */
+#define CONFIG_SPI_FLASH /** Enable SPI flash driver */
+#define CONFIG_SPI_FLASH_STMICRO /** Enable ST Micro SPI flash */
+#define CONFIG_CMD_SPI /** Enable SPI command */
+#define CONFIG_CMD_SF /** Enable SPI flash command */
+
+#define CONFIG_OCTEON_SPI_NO_FAILSAFE /** SPI failsafe not yet supported */
+/**
+ * Start of where the SPI bootloader images are placed. Must be on an erase
+ * block boundary.
+ */
+#define CONFIG_OCTEON_SPI_BOOT_START 0x10000
+
+/**
+ * End of the SPI boot partition which is searched for a bootloader. Must be
+ * on an erase block boundary.
+ */
+#define CONFIG_OCTEON_SPI_BOOT_END 0x400000
+
+/** Maximum number of NAND devices */
+#define CONFIG_SYS_MAX_NAND_DEVICE 1
+#define CONFIG_SYS_NAND_FORCE_WIDTH 8 /** Force to 8-bit width */
+/** Not used for Octeon, but must be set */
+#define
CONFIG_SYS_NAND_BASE 0
+/** Maximum number of NAND devices */
+#define CONFIG_SYS_NAND_MAX_CHIPS CONFIG_SYS_MAX_NAND_DEVICE
+
+/** Enable multi-bit ECC support */
+#define CONFIG_OCTEON_HW_BCH
+#include "config_octeon_nand.h"
+
+
+/**
+ * Miscellaneous configurable options

```

```

+ */
+/** Environment variables that will be set by default */
+#define CONFIG_EXTRA_ENV_SETTINGS \
+ "burn_app=erase $(flash_unused_addr) +$(filesize);cp.b $(fileaddr) $(flash_unused_addr) $(filesize)\0" \
+ "bf=bootoct $(flash_unused_addr) forceboot numcores=$(numcores)\0" \
+ "linux_mmc=fatload mmc 1 $(loadaddr) vmlinux.64;bootoctlinux $(loadaddr)\0" \
+ "autoload=n\0" \
+ "ls=fatls mmc 0\0" \
+ ""
+
+/*-----
+ * FLASH and environment organization
+ */
+#define CONFIG_SYS_FLASH_SIZE (16*1024*1024) /** Flash size (bytes) */
+#define CONFIG_SYS_MAX_FLASH_BANKS 1 /** max
number of memory banks */
+#define CONFIG_SYS_MAX_FLASH_SECT (256) /** max number of sectors on one chip */
+
+/* Width of CFI bus to start scanning */
+#define CONFIG_SYS_FLASH_CFI_WIDTH FLASH_CFI_16BIT
+/* Enable extra elements in CFI driver for storing flash geometry */
+#define CONFIG_SYS_FLASH_CFI 1
+/* Enable CFI flash driver */
+#define CONFIG_FLASH_CFI_DRIVER 1
+#define CONFIG_SYS_FLASH_USE_BUFFER_WRITE
+
+/* We're not RAM_RESIDENT so CONFIG_ENV_IS_IN_FLASH will be set. */
+#if CONFIG_RAM_RESIDENT
+# define CONFIG_ENV_IS_NOWHERE 1
+#else
+# define CONFIG_ENV_IS_IN_FLASH 1
+#endif
+
+#define CONFIG_LIBATA /** Needed for SATA */
+#define CONFIG_CMD_SATA /** Enable the sata command */
+#define CONFIG_SYS_SATA_MAX_DEVICE 2 /** Support up to 2 devices */
+#define CONFIG_SATA_AHCI /** Enable AHCI driver */
+#define CONFIG_SATA_AHCI_PLAT /** Enable AHCI platform support */
+#define CONFIG_SATA_AHCI_OCTEON /** Enable Octeon AHCI driver */
+#define CONFIG_AHCI_SETFEATURES_XFER /**
Enable setfeature xfer feature */
+
+/* Address and size of Primary Environment Sector */
+#define CONFIG_ENV_SIZE (128*1024) /** Size of environment */
+#define CONFIG_ENV_ADDR (CONFIG_SYS_FLASH_BASE + CONFIG_SYS_FLASH_SIZE -
CONFIG_ENV_SIZE)
+
+/*-----

```

```

+ * Cache Configuration (not used)
+ */
+#define CONFIG_SYS_DCACHE_SIZE (32 * 1024) /** L1 Data cache size */
+#define CONFIG_SYS_ICACHE_SIZE (78 * 1024) /** L1 icache size */
+
+#if CONFIG_OCTEON_PCI_HOST
+/** Right now only the Intel E1000 driver has been ported */
+# define CONFIG_E1000
+#endif
+
+/** The CN73XX has two TWSI buses and the EBB7304 board uses both of them */
+#define CONFIG_SYS_MAX_I2C_BUS 1
+
+#define CONFIG_RTC_DS1337 /** RTC clock chip */
+#define CONFIG_SYS_I2C_RTC_ADDR 0x68 /** RTC I2C address */
+#define CONFIG_SYS_RTC_BUS_NUM 0 /** RTC I2C bus */
+
+/** Enable watchdog support */
+#define CONFIG_HW_WATCHDOG
+
+/** Configure QLM support
+ */
+#define CONFIG_OCTEON_QLM
+
+/* Include shared board configuration, consisting mainly of DRAM details. */
+#include "octeon_kilburn_shared.h"
+
+/** Disable DDR2 support */
+#define CONFIG_OCTEON_DISABLE_DDR2
+
+#endif /* __CONFIG_H__ */
diff -urN OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn_shared.h OCTEON-
SDK/bootloader/u-boot/include/configs/octeon_kilburn_shared.h
--- OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn_shared.h 1969-12-31 16:00:00.000000000
-0800
+++ OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn_shared.h 2016-04-11 17:25:01.645670598 -
0700
@@ -0,0 +1,598 @@
+#ifndef __OCTEON_KILBURN_SHARED_H__
+#define __OCTEON_KILBURN_SHARED_H__
+
+
+
+/**
+ * Define this to use SPD data defined in header files instead of
+ * using TWSI to access the SPDs in the DIMMs. This is a workaround
+ * for boards that lack working TWSI interfaces.
+ */
+
+

```

```

+/* 1-DIMM, 4-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC0(spd_pointer) \
+ {{0x0,
+ 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC2(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC3(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+
+/* 2-DIMM, 4-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC2(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC3(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0,
+ 0x0}, {spd_pointer, NULL}}
+
+/* 1-DIMM, 2-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC2(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC3(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+
+/* 2-DIMM, 2-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}

```

```
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}\
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC2(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}\
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC3(spd_pointer) \
\
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}\
+\
+#define MT18ASFIG72AZ_2G1AYESZG_VALUES \
+0x23,0x06,0x0c,0x02,0x84,0x19,0x00,0x00,0x00,0x00,0x00,0x03,0x09,0x0b,0x80,0x00,\
+0x00,0x00,0x08,0x0c,0xf4,0x1b,0x00,0x00,0x6c,0x6c,0x6c,0x11,0x08,0x74,0x20,0x08,\
+0x00,0x05,0x70,0x03,0x00,0xa8,0x1e,0x2b,0x2b,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0xec,0xb5,0xce,0x00,0x00,0x00,0x00,0xc2,0x98,0xf4,\
+0x11,0x11,0x04,0x01,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x0c,0x2e\
+\
+#define
MT18ASFIG72AZ_2G1AXESZG_VALUES \
+0x23,0x10,0x0c,0x02,0x84,0x19,0x00,0x00,0x00,0x00,0x00,0x03,0x09,0x0b,0x80,0x00,\
+0x00,0x00,0x08,0x0c,0xf4,0x1b,0x00,0x00,0x6c,0x6c,0x6c,0x11,0x08,0x74,0x20,0x08,\
+0x00,0x05,0x70,0x03,0x00,0xa8,0x1e,0x2b,0x2b,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x0c,0x2c,0x15,0x35,\
+0x15,0x35,0x0b,0x2c,0x15,0x35,0x0b,0x2c,0x0b,0x2c,0x0b,0x35,0x15,0x36,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x0c,0x2e\
+
```

```

+/*
MT18ASF1G72AZ_2G1AYESZG_VALUES ID: 101 DDR4 */
+/* MT18ASF1G72AZ_2G1AXESZG_VALUES ID: 102 DDR4 */
+
+/*-----*/
+/*
Configuration 0: gpio 1 = OFF */
+/*-----*/
+/* Select SPD from the list. */
+#define OCTEON_KILBURN_CFG0_SPD_VALUES MT18ASF1G72AZ_2G1AXESZG_VALUES
+
+/* Select socket configuration.
+ * _1DIMM_4LMC _2DIMM_4LMC
+ * _1DIMM_2LMC _2DIMM_2LMC
+ */
+#define OCTEON_KILBURN_CFG0_DIMM_LMC_CONFIGURATION _1DIMM_2LMC
+/*-----*/
+
+/*-----*/
+/* Configuration 1: gpio 1 = ON */
+/*-----*/
+/* Select SPD from the list. */
+#define OCTEON_KILBURN_CFG1_SPD_VALUES MT18ASF1G72AZ_2G1AYESZG_VALUES
+
+/* Select socket configuration.
+ * _1DIMM_4LMC _2DIMM_4LMC
+ * _1DIMM_2LMC _2DIMM_2LMC
+ */
+#define OCTEON_KILBURN_CFG1_DIMM_LMC_CONFIGURATION _1DIMM_2LMC
+/*-----*/
+
+
+#define
MERGE_AGAIN(X, Y) X ## Y
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION0 {{0x50, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION1 {{0x54, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION2 {{0x0, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION3 {{0x0, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+
+#define OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR (0x50)
+
+
+
+/*

```

```

+ * Local copy of these parameters to allow for customization for this
+ * board design. The generic version resides in lib_octeon_shared.h.
+ */
+
+/* LMC0_MODEREG_PARAMS1 */
+#define OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = 0, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = 0, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = 0, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0, \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_2SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_HiZ, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = 0, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \

```

```

+ .pasr_10 = 0,
+ \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = ddr4_rttwr_HiZ, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0 \
+ }
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_240ohm,
+ \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = ddr4_rttwr_240ohm, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 =
+ ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0, \
+ }
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_2SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \

```



```

+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_240ohm, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = ddr4_rttnom_120ohm, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = ddr4_rttwr_240ohm, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = ddr4_rttnom_120ohm, \
+ .pasr_10 = 0, \
+ .asr_10 = 0,
+
+ \
+ .srt_10 = 0, \
+ .rtt_wr_10 = ddr4_rttwr_240ohm, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = ddr4_rttnom_120ohm, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = ddr4_rttwr_240ohm, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = ddr4_rttnom_120ohm, \
+ }
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS1_4RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = rttwr_60ohm, \
+ .dic_00 = dic_34ohm,
+
+ \
+ .rtt_nom_00 = rttnom_20ohm, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = rttwr_60ohm, \
+ .dic_01 = dic_34ohm, \
+ .rtt_nom_01 = rttnom_none, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = rttwr_60ohm, \
+ .dic_10 = dic_34ohm, \
+ .rtt_nom_10 = rttnom_20ohm, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \

```

```

+ .srt_11 = 0, \
+ .rtt_wr_11 = rttwr_60ohm,
 \
+ .dic_11 = dic_34ohm, \
+ .rtt_nom_11 = rttnom_none, \
+ } \
+ }
+
+
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_1SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_60ohm, \
+ .vref_value_00 = 0x22, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = 0, \
+ .vref_value_01 = 0, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+
+/* FIX */
+#define OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_2SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_34ohm, \
+ .vref_value_00 = 0x1f, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = 0, \
+ .vref_value_01 = 0, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = ddr4_rttpark_34ohm, \
+ .vref_value_10 = 0x1f, \
+ .vref_range_10
= 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_1SLOT \

```

```

+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_120ohm, \
+ .vref_value_00 = 0x19, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_120ohm, \
+ .vref_value_01 = 0x19, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_2SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_60ohm, \
+ .vref_value_00 = 0x19, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_60ohm, \
+ .vref_value_01 = 0x19, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = ddr4_rttpark_60ohm, \
+ .vref_value_10 = 0x19, \
+ .vref_range_10 = 0, \
+ .rtt_park_11
+ = ddr4_rttpark_60ohm, \
+ .vref_value_11 = 0x19, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_4RANK_1SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_80ohm, \
+ .vref_value_00 = 0x1f, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_80ohm, \
+ .vref_value_01 = 0x1f, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \

```

```

+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm,
0x00000000ULL, OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_1SLOT, ddr4_rodt_ctl_48_ohm, 0x00000000ULL,
0 }, \
+ /* 2 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_2SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_2SLOT, ddr4_rodt_ctl_48_ohm, 0x00000000ULL,
0 }
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_1SLOT, ddr4_rodt_ctl_80_ohm, 0x00000000ULL,
0 }, \
+ /* 2 */ { ddr4_dqx_driver_34_ohm,
0x0c0c0303ULL, OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_2SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_2SLOT, ddr4_rodt_ctl_48_ohm, 0x04080102ULL,
0 }
+
+#define OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x01030203ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_4RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_4RANK_1SLOT, ddr4_rodt_ctl_48_ohm, 0x01010202ULL,
0 }
+
+/* Construct a static initializer for the ddr_configuration_t variable that holds
+** (almost) all of the information required for DDR initialization.
+*/
+

```

```

+/*
+ The parameters below make up the custom_lmc_config data structure.
+ This structure
+ is used to customize the way that the LMC DRAM
+ Controller is configured for a particular board design.
+
+ Refer to the file lib_octeon_board_table_entry.h for a description
+ of the custom board settings. It is usually kept in the following
+ location... arch/mips/include/asm/arch-octeon/
+
+*/
+
+#define OCTEON_KILBURN_DDR_CONFIGURATION(SPD_CONFIGURATION) \
+ /* Interface 0 */ \
+ { \
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdimm = 2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdimm = 0, \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdimm = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0}, \
+ .dimmm_config_table = { \
+ SPD_CONFIGURATION##0, \
+ DIMM_CONFIG_TERMINATOR}, \
+ .unbuffered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \

```

```

+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk
= 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION}, \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ }, \
+ /* Interface 1 */ \
+ { \
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdimm = 2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdimm = 0, \
+
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdimm = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0}, \
+ .dimm_config_table = { \
+ SPD_CONFIGURATION##1, \
+ DIMM_CONFIG_TERMINATOR}, \
+ .unbuffered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0,

```

```

\
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION}, \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ },
\
+ /* Interface 2 */
+ {
+ .custom_lmc_config = {
+ .min_rtt_nom_idx = 1,
+ .max_rtt_nom_idx = 7,
+ .min_rodt_ctl = 1,
+ .max_rodt_ctl = 7,
+ .ck_ctl = ddr4_driver_34_ohm,
+ .cmd_ctl = ddr4_driver_34_ohm,
+ .ctl_ctl = ddr4_driver_34_ohm,
+ .min_cas_latency = 0,
+ .offset_en = 1,
+ .offset_udimm = 2,
+ .offset_rdimm = 2,
+
+ .ddr_rtt_nom_auto = 0,
+ .ddr_rodt_ctl_auto = 0,
+ .rlevel_comp_offset_udimm = 0,
+ .rlevel_comp_offset_rdimm = 0,
+ .rlevel_compute = 0,
+ .ddr2t_udimm = 1,
+ .ddr2t_rdimm = 1,
+ .maximum_adjacent_rlevel_delay_increment = 2,
+ .fprch2 = 2,
+ .dll_write_offset = NULL,
+ .dll_read_offset = NULL,
+ .parity = 0},
+ .dimm_config_table = {
+ SPD_CONFIGURATION##2,
+
+ DIMM_CONFIG_TERMINATOR},
+ .unbuffered = {

```

```

+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION},
+
+ \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ },
+ /* Interface 3 */
+ {
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm,
+ \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdim = 2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdim = 0, \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdim = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2,
+ \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0},
+ .dimm_config_table = { \
+ SPD_CONFIGURATION##3, \
+ DIMM_CONFIG_TERMINATOR},

```



```
+ .unbuffered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk
= 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION}, \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ }
+
+##endif /* __OCTEON_KILBURN_SHARED_H__ */
diff -urN OCTEON-SDK-old/executive/cvmx-app-init.h OCTEON-SDK/executive/cvmx-app-init.h
--- OCTEON-SDK-old/executive/cvmx-app-init.h 2016-04-11 17:31:52.343671090 -0700
+++ OCTEON-SDK/executive/cvmx-app-init.h 2016-04-11 17:32:06.134670808 -0700
@@ -362,6 +362,7 @@
** use any numbers in this
range. */
CVMX_BOARD_TYPE_CUST_PRIVATE_MIN = 20001,
CVMX_BOARD_TYPE_UBNT_E100 = 20002,
+ CVMX_BOARD_TYPE_CUST_KILBURN = 20003,
CVMX_BOARD_TYPE_CUST_PRIVATE_MAX = 30000,

/* Range for IO modules */
@@ -503,6 +504,7 @@
ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_PRIVATE_MIN)
ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_UBNT_E100)
ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_CLARK)
+ ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_KILBURN)
ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_PRIVATE_MAX)
/* Module range */
ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_MODULE_MIN)
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-
sdk/patches/kilburn_sdk_p4.patch
```

No license file was found, but licenses were detected in source scan.

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```
diff -aur a/OCTEON-SDK/bootloader/u-boot/tools/setlocalversion b/OCTEON-SDK/bootloader/u-
boot/tools/setlocalversion
--- a/OCTEON-SDK/bootloader/u-boot/tools/setlocalversion 2018-03-23 14:00:20.547230326 -0400
+++ b/OCTEON-SDK/bootloader/u-boot/tools/setlocalversion 2018-04-04 08:20:27.261648898 -0400
@@ -14,6 +14,7 @@
```

```
scm_only=false
srctree=.
+sdk="3.1.2-p12"
if test "$1" = "--save-scmversion"; then
 scm_only=true
 shift
@@ -154,7 +155,7 @@
if rev=`svnversion 2>/dev/null` ; then
 rev=`echo "${rev}" | awk '{print $NF}'`
 sdk_rev=`svnversion $OCTEON_ROOT/executive | awk '{print $NF}'`
- printf -- '(Development build, svnversion: u-boot:%s, exec:%s)' $rev $sdk
+ printf -- '(Development build, exec:%s)' $sdk
fi
```

```
localversion* files in the build and source directory
```

```
Found in path(s):
```

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/u-boot-SDK-version-
banner.patch
```

```
No license file was found, but licenses were detected in source scan.
```

```
Copyright (c) 2016 by Cisco Systems, Inc.
```

```
All rights reserved.
```

```
OCTEON-SDK/host/remote-utils/Makefile | 3 +++
```

```
1 files changed, 3 insertions(+), 0 deletions(-)
```

```
diff --git a/OCTEON-SDK/host/remote-utils/Makefile b/OCTEON-SDK/host/remote-utils/Makefile
index eb47e32..08ebb01 100644
```

```
--- a/OCTEON-SDK/host/remote-utils/Makefile
+++ b/OCTEON-SDK/host/remote-utils/Makefile
@@ -78,10 +78,13 @@ CVMX_FILES = $(OCTEON_ROOT)/executive/cvmx-csr-db.c \
 $(OCTEON_ROOT)/executive/cvmx-qlm.c \
 $(OCTEON_ROOT)/executive/cvmx-qlm-tables.c \
 $(OCTEON_ROOT)/executive/cvmx-srio.c \
 $(CVMX_COMMON_FILES)
```

```
+$$(LIBS):
```

```
+ $(MAKE) -C ../remote-lib all
```

```
+
```

```
$(DIR)/oct-remote-boot: $(LIBS) oct-remote-boot.c crc32.c $(BOOTLOADER_FILES)/lib_octeon_shared.c
```

```
$(CVMX_COMMON_FILES) oct-remote-common.c
```

```
$(CC) $(CFLAGS) -o $@ $^ $(LIBS) $(LIBS_FLAGS)
$(DIR)/oct-remote-bootcmd: $(LIBS) oct-remote-bootcmd.c oct-remote-common.c
$(CC)
$(CFLAGS) -o $@ $^ $(LIBS) $(LIBS_FLAGS)
$(DIR)/oct-remote-console: $(LIBS) oct-remote-console.c
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-
OCTEON-SDK-host-remote-utils-Makefile-remote-lib-target.patch
No license file was found, but licenses were detected in source scan.
```

```
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```

```
CNNIC-SDK-SRC/modules/pci-tap/Makefile | 4 ++--
```

```
1 file changed, 2 insertions(+), 2 deletions(-)
```

```
diff --git a/CNNIC-SDK-SRC/modules/pci-tap/Makefile b/CNNIC-SDK-SRC/modules/pci-tap/Makefile
index f92ff25..ef34d48 100755
--- a/CNNIC-SDK-SRC/modules/pci-tap/Makefile
+++ b/CNNIC-SDK-SRC/modules/pci-tap/Makefile
@@ -39,12 +39,12 @@ EXTRA_CFLAGS = $(CFLAGS_LOCAL) -DGPIO_CIU3_BASE_IRQ=1
export NUMBER_OF_BAR_DESC=1
OCT_PCI_TAP=pci-tap
HOST_TAP_BIN=host-pci-tap
```

```
all: hostapp octapp
```

```
-hostapp: CC=gcc
-hostapp: AR=ar
+hostapp: CC=$(TARGET_PREFIX)gcc
+hostapp: AR?=$(TARGET_PREFIX)ar
hostapp: EXTRA_CFLAGS += -Wno-error=declaration-after-statement -
DN_DESC=$(NUMBER_OF_BAR_DESC)
hostapp: EXTRA_LDFLAGS = -L $(CNNIC_ROOT)/modules/pci-tap/ -lpthread -lcutils
hostapp: $(OCT_PCI_TAP)
```

```
cnutils-link:
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-
CNNIC-SDK-SRC-modules-pci-tap-Makefile.patch
No license file was found, but licenses were detected in source scan.
```

```
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```

```
OCTEON-SDK/executive/cvmx-app-init-linux.c | 4 +++-
```

1 file changed, 3 insertions(+), 1 deletion(-)

diff --git a/OCTEON-SDK/executive/cvmx-app-init-linux.c b/OCTEON-SDK/executive/cvmx-app-init-linux.c  
index 30da977..dbbfed1 100644

--- a/OCTEON-SDK/executive/cvmx-app-init-linux.c

+++ b/OCTEON-SDK/executive/cvmx-app-init-linux.c

@@ -54,11 +54,11 @@

```
* protections are circumvented. If you do something bad, Linux won't
* save you.
* # Most hardware can only be initialized once. Unless you're very careful,
* this also means you Linux application can only run once.
*
```

- \* <hr>\$Revision: 107404 \$<hr>

+ \* <hr>\$Revision: #1 \$<hr>

\*

\*/

```
#define _GNU_SOURCE
```

```
#include <stdint.h>
```

```
#include <stdio.h>
```

@@ -158,10 +158,11 @@ void simprintf(const char \*format, ...)

```
vprintf(format, ap);
```

```
va_end(ap);
```

```
cvmx_spinlock_unlock(&simprintf_lock);
```

```
}
```

```
+#if 0
```

```
/**
```

```
* Setup the CVMX_SHARED data section to be shared across
* all processors running this application. A memory mapped
* region is allocated using shm_open and mmap. The current
* contents of the CVMX_SHARED section are copied into the
```

@@ -408,5 +409,6 @@ int main(int argc, const char \*argv[])

```
}
```

```
}
```

```
return result;
```

```
}
```

```
+#endif
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-OCTEON-SDK-executive-cvmx-app-init-linux.c.patch
```

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```
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```

```
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```

CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile | 4 +---

1 file changed, 2 insertions(+), 2 deletions(-)

```
diff --git a/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile b/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile
```

```
index 09be185..d220800 100644
```

```
--- a/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile
+++ b/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/cnutils/Makefile
@@ -24,4 +24,4 @@
```

```
-CC = gcc -include /usr/include/errno.h
-AR = ar
+CC ?= gcc -include /usr/include/errno.h
+AR ?= ar
NAME = cnutils
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-CNNIC-SDK-SRC-modules-driver-src-host-linux-user-drv-cnutils-Makefile.patch
```

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```
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```

```
OCTEON-SDK/bootloader/u-boot/Makefile | 1 -
```

1 files changed, 0 insertions(+), 1 deletions(-)

```
diff --git a/OCTEON-SDK/bootloader/u-boot/Makefile b/OCTEON-SDK/bootloader/u-boot/Makefile
```

```
index e2d16b5..d8cce23 100644
```

```
--- a/OCTEON-SDK/bootloader/u-boot/Makefile
+++ b/OCTEON-SDK/bootloader/u-boot/Makefile
@@ -907,11 +907,10 @@ clobber: tidy
 @rm -f $(obj)MLO MLO.byteswap
 @rm -f $(obj)SPL
 @rm -f $(obj)tools/xway-swap-bytes
 @rm -f $(obj)arch/powerpc/cpu/mpc824x/bedbug_603e.c
 @rm -f $(obj)arch/powerpc/cpu/mpc83xx/ddr-gen?.c
- @rm -fr $(obj)include/asm/proc $(obj)include/asm/arch $(obj)include/asm
 @rm -fr $(obj)include/asm/proc
ifneq ($(CPU),octeon)
 @rm -fr $(obj)include/asm/arch $(obj)include/asm
endif
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-OCTEON-SDK-bootloader-u-boot-Makefile.patch
```

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```
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```

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CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c | 4 +++-  
1 file changed, 3 insertions(+), 1 deletion(-)

diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c

index 7fb8234..7233926 100644

```
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
@@ -486,11 +486,13 @@ cvmcs_ioq_send_to_pko(cvmx_wqe_t *wqe, int dir, int port, int queue,
 * success, pko will free up the buffer.
 */
```

```
int
cvmcs_ioq_forward_packet_to_host(cvmx_wqe_t *wqe, int rxq)
```

```
{
+#if 0 //for LINA: as WQE and data share the same buffer.
```

```
 cvmx_buf_ptr_t *lptr;
+#endif
 cvmx_resp_hdr_t *resp_ptr;
 cvmx_buf_ptr_pki_t *pki_lptr;
 int port, queue;
```

```
#if 0 //for LINA: as WQE and data share the same buffer.
```

```
@@ -653,11 +655,11 @@ cavium_parse_cvs_string(const char *cvs_name, char *ver_string, int len)
 */
```

```
void cvmcs_ioq_config_interrupts()
```

```
{
 if (OCTEON_IS_MODEL(OCTEON_CN78XX)) {
 /* Workaround to disable CVMX_DPI_INT_REG[21] */
- cvmx_error_intsn_disable_v3(0xdf015);
+ cvmx_error_intsn_disable_v3(0, 0xdf015);
 }
 return;
}
```

```
/*
```

Found in path(s):

\* /opt/cola/permits/1305654281\_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-CNNIC-SDK-SRC-modules-driver-src-firmware-apps-ioq-cvmcs-ioq-main.c.patch

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diff -up octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h.orig octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h

```

--- octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h.orig 2016-08-10 21:58:14.872582801 -0400
+++ octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h 2016-08-10 21:59:52.281988152 -0400
@@ -72,15 +72,26 @@ uint64_t user_drv_pci_get_scratchreg(voi
* The function waits indefinitely for OCTEON to respond otherwise.
*/

-static void host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
+static int host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
 uint64_t scratch_reg_addr, scratch_val = 0;
+ time_t t0;
+
 scratch_reg_addr = user_drv_pci_get_scratchreg(user_dev);

 /* Wait for OCTEON to to send indication
of handshake start */
+ t0 = time(NULL);
 do
 {
 sleep(1);
+ if (time(NULL) > t0 + HS_TIMEOUT)
+ {
+ syslog(LOG_INFO, "Timed out waiting for handshake to start.");
+ return -1;
+ }
+ else if (killed)
+ return -1;
+
 scratch_val = user_drv_pci_read_csr(scratch_reg_addr, user_dev);
 } while(scratch_val != HOSTFW_HS_IND);

@@ -91,8 +102,20 @@ static void host_oct_handshake(void *us
 user_drv_pci_write_csr(scratch_reg_addr, scratch_val, user_dev);

 /* Loop until OCTEON writes the address */
+ t0 = time(NULL);
 while(scratch_val == HOSTFW_HS_ACK)
+ {
+ usleep(1000);
+ if (time(NULL) > t0 + HS_TIMEOUT)
+ {
+ syslog(LOG_INFO, "Timed out waiting for handshake to complete.\n");
+ return -1;
+ }
+ else if (killed)
+ return -1;
+
 scratch_val = user_drv_pci_read_csr(scratch_reg_addr, user_dev);

```

```

+ }

/*Host read follows host write*/
write_buf->base = scratch_val;
@@ -103,7 +126,7 @@ static void host_oct_handshake(void *us
user_drv_pci_write_csr(scratch_reg_addr, scratch_val, user_dev);

cavium_print_msg("Host-Octeon Handshake completed\n");
- return;
+ return 0;
}

static int host_wait_on_fds()
@@ -123,8 +146,8 @@ static int host_wait_on_fds()
cavium_print_msg("Select faied with error %d\n", errno);
return 0;
}
- return 1;

+ return ret;
}

/*
@@ -187,26 +210,38 @@ static inline int host_oct_init()
*/
static void host_oct_data_loop(struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
- while (1)
+ int ret = 0, ret2 = 0;
+ int idle_count = 0;
+
+ while (!killed)
{
-
- if(host_wait_on_fds())
+ if((ret = host_wait_on_fds()))
{
- if (FD_ISSET(tun_fd, &readfds))
- {
- if(host_oct_network_to_pci(write_buf, tun_fd)){
- /* Inform Octeon
*/
- host_interrupt_octeon(oct_pci_dev);
+ if (FD_ISSET(tun_fd, &readfds))
+ {
+ if((ret = host_oct_network_to_pci(write_buf, tun_fd)) > 0){
+ #ifdef USE_GPIO

```



```

+ /* Inform Octeon */
+ host_interrupt_octeon(oct_pci_dev);
+ #endif
+ }
+ else if (ret < 0)
+ break;
+ }
- }
 }

 /* Rx: Read data from OCTEON memory and write to tap */
- host_oct_pci_to_network(read_buf);
+ ret2 = host_oct_pci_to_network(read_buf);

+ if (ret == 0 && ret2 == 0)
+ {
+ idle_count ++;
+ if (idle_count ++ > 1000)
+ usleep(IDLE_SLEEP);
+ }
+ else
+ idle_count = 0;
 }
-
- user_drv_pci_close(oct_pci_dev);
}

#endif /* _HOST_TAP_DEFS_H */
diff -up octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/Makefile.orig octeon-sdk/CNNIC-SDK-SRC/modules/pci-
tap/Makefile

octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/Makefile.orig 2016-08-10 21:58:14.873582795 -0400
+++ octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/Makefile 2016-08-10 21:59:55.418969002 -0400
@@ -35,7 +35,7 @@ CFLAGS_LOCAL = -O2 -W -Wall -Werror -Wno

EXTRA_CFLAGS = $(CFLAGS_LOCAL) -DGPIO_CIU3_BASE_IRQ=1

-export NUMBER_OF_BAR_DESC=1
+export NUMBER_OF_BAR_DESC=16
OCT_PCI_TAP=pci-tap
HOST_TAP_BIN=host-pci-tap

diff -up octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h.orig octeon-sdk/CNNIC-SDK-
SRC/modules/pci-tap/oct-tap-defs.h
--- octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h.orig 2016-08-10 21:58:14.874582789 -0400
+++ octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h 2016-08-10 21:59:52.320987914 -0400
@@ -79,11 +79,13 @@ void *oct_pci_dev = NULL;
* return Setup Read and write addresses for Host and return.

```

```

* The function waits indefinitely for HOST to respond otherwise.
*/
-static void
-host_oct_handshake(void *user_dev,struct nw_buffer
*read_buf, struct nw_buffer *write_buf)
+static int host_oct_handshake(void *user_dev,struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
+ time_t t0;
 uint64_t read_idx_addr, scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1, scratch_val;
 const cvmx_bootmem_named_block_desc_t *desc;
+
+ifdef USE_GPIO
 scratch_val = cvmx_read_csr_node(cvmx_get_node_num(),
CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
 scratch_val |= 0x1; /* Enable Output bit */

@@ -96,30 +98,60 @@ host_oct_handshake(void *user_dev,struct
 scratch_val |= 0x1;
 cvmx_write_csr_node(cvmx_get_node_num(), CVMX_GPIO_TX_SET, scratch_val);
}
+endif

/* Handshake */
scratch_val = HOSTFW_HS_IND;
cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);

/* Wait for acknowledgement from host */
+ t0 = time(NULL);
 while(scratch_val != HOSTFW_HS_ACK)
+ {
+ usleep(10000);
+ if (time(NULL) > t0 + HS_TIMEOUT)
+
+ {
+ syslog(LOG_INFO, "Timed out waiting for handshake ack.");
+ return -1;
+ }
+ else if (killed)
+ return -1;
+
 scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr);
+ }

/* Fetch the named block allocated for pci-tap buffers*/
desc = cvmx_bootmem_find_named_block(BLOCK_NAME);

- if(!desc)
+ if(!desc || desc->size != N_DESC*BLOCK_SIZE)

```

```

{
+ if (desc && desc->size != N_DESC*BLOCK_SIZE)
+ {
+ syslog(LOG_INFO, "Memory block size mismatch: have %lu, need %d.",
+ desc->size, N_DESC*BLOCK_SIZE);
+
+ cvmx_bootmem_free_named(BLOCK_NAME);
+ }
+
+ /* Allocate named block for write index */
+ read_buf->base = cvmx_bootmem_alloc_named_range(N_DESC*BLOCK_SIZE,
+ NB_MIN_ADDR, NB_MAX_ADDR, NB_4KB_ALIGNED, BLOCK_NAME);
+
+ syslog(LOG_INFO, "Allocated memory block at %p of size %d.",
+ read_buf->base,
+ N_DESC*BLOCK_SIZE);
+ }
+ else
+ {
+ syslog(LOG_INFO, "Found memory block at 0x%lx of size %lu.",
+ desc->base_addr, desc->size);
+
+ read_buf->base = cvmx_phys_to_ptr(desc->base_addr);
+ }
+
+ if(read_buf->base == (void *)-1)
+ {
+ cavium_print_msg("Failed to allocate named block\n");
+ exit(1);
+ return -1;
+ }

+ cavium_print_dbg("read_idx_ptr: %p\n",read_buf->base);
+ @@ -141,18 +173,31 @@ host_oct_handshake(void *user_dev,struct
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr);
+ cavium_print_dbg("scratch_val : %lx\n",scratch_val);

+ t0 = time(NULL);
+ while(scratch_val != HOSTFW_HS_NB_DONE)
+ {
+ usleep(1000);
+ if (time(NULL) > t0 + HS_TIMEOUT)
+ {
+ syslog(LOG_INFO, "Timed out waiting for handshake to complete.");
+ return -1;
+ }
+ else if (killed)
+ return -1;

```

```

+
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(),
+ scratch_reg_addr);
+ }
+ cavium_print_msg("Host-Octeon Handshake Completed\n");
+ cavium_print_msg("OCT-PCI-TAP running as daemon\n");
+
+
+ return 0;
+ }

/*
 * Wait on GPIO and TAP Fds. A set gpio fd indicates the availability
 * of data on BAR1 memory.
 */
#ifdef USE_GPIO
static int oct_wait_on_fds()
{
 int nfds = 0;
@@ -175,6 +220,7 @@ static int oct_wait_on_fds()
 return 0;

}
#endif

/*
 * Perform device and network initialization.
@@ -184,18 +230,31 @@ static int oct_wait_on_fds()
 */
static inline int host_oct_init()
{
+ const cvmx_bootmem_named_block_desc_t *desc;
+
+ /* Run the application only on the first core */
+ if(cvmx_get_core_num() != 0)
+ exit (0);

+ /* Fetch the named block allocated for reserving boot memory */
+ desc = cvmx_bootmem_find_named_block("miscbuf");
+ if(desc)
+ {
+ syslog(LOG_INFO, "Found reserved miscbuf memory block in %luMiB bytes. Freeing it ...",
+
+ desc->size / 1024 / 1024);
+ cvmx_bootmem_free_named("miscbuf");
+ }

+ /* Connect to the device */
+ if ((tun_fd = host_oct_tun_alloc(TAP_IF_NAME, IFF_TAP)) < 0) /* tun interface */

```

```

 return tun_fd;

#ifdef USE_GPIO
 if((gpio_fd = open(GPIO_DEV_FILE, O_RDWR)) < 0) {
 cavium_print_msg("Error opening %s\n",GPIO_DEV_FILE);
 return gpio_fd;
 }
#endif

 return 0;
}
@@ -209,7 +268,8 @@ static inline int host_oct_init()
*/
static void host_oct_data_loop(nw_buf_t *read_buf, nw_buf_t *write_buf)
{
- while (1)
#ifdef USE_GPIO
+ while (!killed)
 {
 oct_wait_on_fds();

@@ -221,9 +281,29 @@ static void host_oct_data_loop(nw_buf_t

 if (FD_ISSET(tun_fd, &readfds))
 {
- host_oct_network_to_pci(write_buf, tun_fd);
+ if (host_oct_network_to_pci(write_buf, tun_fd) < 0)
+ break;
+ }
+ }
+ #else
+ int ret, ret2;
+ int idle_count
= 0;
+
+ while (!killed)
+ {
+ ret = host_oct_pci_to_network(read_buf);
+ ret2 = host_oct_network_to_pci(write_buf, tun_fd);
+ if (ret2 < 0)
+ break;
+ if (ret == 0 && ret2 == 0)
+ {
+ if (++ idle_count > 1000)
+ usleep(IDLE_SLEEP);
+ }
+ else
+ idle_count = 0;

```

```

 }
+##endif
}

#endif /* _OCT_TAP_DEFS_H */
diff -up octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c.orig octeon-sdk/CNNIC-SDK-SRC/modules/pci-
tap/pci-tap.c
--- octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c.orig 2016-08-10 21:58:14.874582789 -0400
+++ octeon-sdk/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c 2016-08-10 22:08:33.831402860 -0400
@@ -33,8 +33,22 @@
#include <stdint.h>
#include <time.h>
#include <unistd.h>
+#include <signal.h>
+
+#define USE_GPIO
+#define HS_TIMEOUT 15
+#define TX_TIMEOUT 5
+#define IDLE_SLEEP 200
+
+static volatile sig_atomic_t killed = 0;
+
#include "pci-tap.h"

+void exit_handler(int
sig)
+{
+ killed = 1;
+}
+
+/*
+ * Bind to the TAP adapter and make all reads non blocking.
+ *
@@ -68,7 +82,7 @@ int host_oct_tun_alloc(char *dev, int fl
return err;
}

-#ifndef HOST_TAP
+#if defined(HOST_TAP) || !defined(USE_GPIO)
/* Make all reads non-blocking */
if(fcntl(fd,F_SETFL,O_NONBLOCK) < 0) {
cavium_print_msg("Failed to make non-blocking fd.\n");
@@ -148,7 +162,7 @@ int host_oct_pci_to_network(void *pbuf)
BUF_INC_IDX(preadbuf, ridx);
cavium_write_mem(preadbuf->base + OFFSETOF(nw_buf_t, ridx), &(preadbuf->ridx), sizeof(uint64_t),
oct_pci_dev);
}
-#ifndef OCT_TAP

```

```

+ #if defined(OCT_TAP) && defined(USE_GPIO)
+ else {
+ cavium_print_msg("Invalid interrupt from host with Packet len: 0\n");
+ }
@@ -167,12 +181,19 @@ int host_oct_network_to_pci(void * buf,
{
+ int nread = 0, skipidx;
+ nw_buf_t *pwbuff = (nw_buf_t *)buf;
+ static time_t t0 = 0;

+ if(!buf_isfull(pwbuff,
+ oct_pci_dev)){

+ t0 = time(NULL);
+
+ /*Calculate of amount of descriptor index to be skipped if ndescs > 1*/
+ skipidx = (pwbuff->widx)?(pwbuff->widx -1):0;
+ if((nread = read(tun_fd, pwbuff->desc[pwbuff->widx].data, MAX_ETH_FRAM_SZ))<0){
+ #ifndef USE_GPIO
+ if (errno == EAGAIN)
+ return 0;
+ #endif
+ cavium_print_msg("TUNFD read error [%d, %s]\n", errno, strerror(errno));
+ return -1;
+ }
@@ -189,6 +210,11 @@ int host_oct_network_to_pci(void * buf,
+ &(pwbuff->widx), sizeof(uint64_t), oct_pci_dev);
+ }
+ }
+ else if (t0 && time(NULL) > t0 + TX_TIMEOUT)
+ {
+ syslog(LOG_ERR, "Time out waiting for write buffer.");
+ return -1;
+ }
+ return nread;
+ }

@@ -220,6 +246,8 @@ int main(int argc, char *argv[])
+ nw_buf_t *read_buf = NULL;
+ nw_buf_t *write_buf = NULL;

+ openlog("pci-tap", LOG_PID | LOG_PERROR, LOG_USER);
+
+ if (host_oct_init())
+ {
+ cavium_print_msg("Init Failed\n");
@@ -229,25 +257,43 @@ int main(int argc, char *argv[])
+ read_buf = (nw_buf_t *) malloc(1*NW_BUF_SIZE);

```

```

write_buf = (nw_buf_t *) malloc(1*NW_BUF_SIZE);

- memset(read_buf, 0x0, NW_BUF_SIZE);
- memset(write_buf, 0x0, NW_BUF_SIZE);
+ if (!read_buf || !write_buf)
+ {
+ cavium_print_msg("Failed to allocate r/w buffers.\n");
+ return 1;
+ }

- host_oct_handshake(oct_pci_dev,read_buf,write_buf);
+ daemon(0,1);

- /*Double check: Reset indexes after handshake*/
- read_buf->ridx = 0;
- read_buf->widx = 0;
- write_buf->ridx = 0;
- write_buf->widx = 0;
-
- /*Write widx/ridx values to nw_buf_t on PCI BAR*/
- cavium_write_mem(write_buf->base, &(write_buf->widx),
- 2*sizeof(uint64_t), oct_pci_dev);
- cavium_write_mem(read_buf->base, &(read_buf->widx),
- 2*sizeof(uint64_t), oct_pci_dev);
- daemon(0,0);
+ //install signal
handler
+ signal(SIGINT, exit_handler);
+ signal(SIGTERM, exit_handler);
+ signal(SIGHUP, SIG_IGN);
+ signal(SIGPIPE, SIG_IGN);
+
+ while (!killed)
+ {
+ memset(read_buf, 0x0, NW_BUF_SIZE);
+ memset(write_buf, 0x0, NW_BUF_SIZE);
+
+ if (host_oct_handshake(oct_pci_dev,read_buf,write_buf))
+ continue;

- host_oct_data_loop(read_buf, write_buf);
+ /*Double check: Reset indexes after handshake*/
+ read_buf->ridx = 0;
+ read_buf->widx = 0;
+ write_buf->ridx = 0;
+ write_buf->widx = 0;

+ host_oct_data_loop(read_buf, write_buf);

```



```

+ }
+
+ #ifdef HOST_TAP
+ user_drv_pci_close(oct_pci_dev);
+ #endif
+
+ free(read_buf);
+ free(write_buf);
+
+ return 0;
}

```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/pci-tap-timeout-signal-handshake.patch
```

No license file was found, but licenses were detected in source scan.

```
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```

```
OCTEON-SDK/executive/cvmx.mk | 6 ++++--
1 file changed, 4 insertions(+), 2 deletions(-)
```

```
diff --git a/OCTEON-SDK/executive/cvmx.mk b/OCTEON-SDK/executive/cvmx.mk
```

```
index 035b2b5..aae36bb 100644
```

```
--- a/OCTEON-SDK/executive/cvmx.mk
```

```
+++ b/OCTEON-SDK/executive/cvmx.mk
```

```
@@ -56,17 +56,18 @@ MADE_WITH_OCTEON_MODEL = "runtime"-
```

```
$(CVMX_USE_1_TO_1_TLB_MAPPINGS)
```

```
else
```

```
MADE_WITH_OCTEON_MODEL = $(OCTEON_MODEL)-$(CVMX_USE_1_TO_1_TLB_MAPPINGS)
```

```
endif
```

```
set special filename for target and change any spaces in it to commas
```

```
-LIB_MADE_WITH = $(shell echo "$(EXEC_OBJ_DIR)/lib-made_with-
```

```
OCTEON_MODEL=$(MADE_WITH_OCTEON_MODEL)-ISA=${OCTEON_ISA}=-
```

```
OCTEON_CPPFLAGS_GLOBAL_ADD=$(OCTEON_CPPFLAGS_GLOBAL_ADD)=." | sed 's\/,\/g')
```

```
+MADE_WITH_CPPFLAGS_GLOBAL_ADD = $(shell echo "$(OCTEON_CPPFLAGS_GLOBAL_ADD)" | sed 's/-I.*\(\. *\)\//-I...1/g')
```

```
+LIB_MADE_WITH = $(shell echo "$(EXEC_OBJ_DIR)/lib-made_with-
```

```
OCTEON_MODEL=$(MADE_WITH_OCTEON_MODEL)-ISA=${OCTEON_ISA}=-
```

```
OCTEON_CPPFLAGS_GLOBAL_ADD=$(MADE_WITH_CPPFLAGS_GLOBAL_ADD)=." | sed 's\/,\/g')
```

```
LIB_MADE_WITH_ALL = $(EXEC_OBJ_DIR)/lib-made_with-*
```

```
$(LIB_MADE_WITH):
```

```
mkdir -p $(EXEC_OBJ_DIR)
```

```

- rm -f $(LIB_MADE_WITH_ALL)
+ -rm -f $(LIB_MADE_WITH_ALL)
touch "$(LIB_MADE_WITH)"

component specification
LIBRARY_CVMX := $(EXEC_OBJ_DIR)/libcvmx.a

@@ -193,10 +194,11 @@ EXEC_OBJS_$(d) := \
$(EXEC_OBJ_DIR)/cvmx-zip.o \
$(EXEC_OBJ_DIR)/cvmx-zone.o \
$(EXEC_OBJ_DIR)/cvmx-mbox.o \
$(EXEC_OBJ_DIR)/octeon-feature.o \
$(EXEC_OBJ_DIR)/octeon-model.o \
+ $(EXEC_OBJ_DIR)/cvmx-config-init.o \
$(EXEC_OBJ_DIR)/octeon-pci-console.o

ifeq (linux,$(findstring linux,$(OCTEON_TARGET)))
EXEC_OBJS_$(d) += \
$(EXEC_OBJ_DIR)/cvmx-app-init-linux.o

Found in path(s):
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-OCTEON-SDK-executive-cvmx-mk.patch
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OCTEON-SDK/executive/cvmx-platform.h | 2 +-
1 file changed, 1 insertion(+), 1 deletion(-)

diff --git a/OCTEON-SDK/executive/cvmx-platform.h b/OCTEON-SDK/executive/cvmx-platform.h
index 173f400..10f76bc 100644
--- a/OCTEON-SDK/executive/cvmx-platform.h
+++ b/OCTEON-SDK/executive/cvmx-platform.h
@@ -111,11 +111,11 @@
* section that the loader will map as shared for all cores
* This is for data structures use by software ONLY,
* as it is not 1-1 VA-PA mapped.
*/
#if !defined(CVMX_BUILD_FOR_LINUX_HOST) && !defined(CVMX_BUILD_FOR_UBOOT)
-#define CVMX_SHARED __attribute__((cvmx_shared))
+#define CVMX_SHARED
#else
#define CVMX_SHARED
#endif

/* Thread local storage , to have a global variable be per core

```

Found in path(s):

\* /opt/cola/permits/1305654281\_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-OCTEON-SDK-executive-cvmx-platform.h.patch

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CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c | 3 +++

1 file changed, 3 insertions(+)

diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c

index 2878be4..9fcd51 100644

--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c

+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c

@@ -143,10 +143,13 @@ int cvmcs\_pki\_reconfig() {

printf("Entered pki reconfig\n");

/\*1. Always use cluster group-0.\*/

cluster\_group =0;

+ /\* FIXME - hack to get rid of a 'set but not used' error \*/

+ cluster\_mask = cluster\_group;

+

/\* 2. Assign the clusters to the cluster group -0 \*/

if(OCTEON\_IS\_MODEL(OCTEON\_CN78XX))

cluster\_mask = 0xf;

if(OCTEON\_IS\_MODEL(OCTEON\_CN73XX))

Found in path(s):

\* /opt/cola/permits/1305654281\_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-fixup-CNNIC-SDK-SRC-modules-driver-src-firmware-core-cvm-drv.c.patch

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OCTEON-SDK/env-setup | 4 ++--

1 file changed, 2 insertions(+), 2 deletions(-)

diff --git a/OCTEON-SDK/env-setup b/OCTEON-SDK/env-setup

index 9c3582b..61ab8f0 100644

--- a/OCTEON-SDK/env-setup

+++ b/OCTEON-SDK/env-setup

@@ -4,16 +4,16 @@

```

constraints on what can be done directly.

Check to see if the shell we are running uses 'setenv' or 'export' to
set variables. If the shell uses setenv, then the variable is set with
the value 2, otherwise stderr is redirected to /dev/null
-setenv __OCTEON_SHELL_TEST__ 2>/dev/null
+setenv __OCTEON_SHELL_TEST__ 2>/dev/null || true

Invoke the env-setup.pl script, which processes the arguments and constructs the correct
environment changes for the shell that it was called from.
eval `./env-setup.pl "$1" "$2" "$3" "$4" "$5" "$6" "$7"`

Clean up test variable so that it is not left around to
confuse subshells
-unsetenv __OCTEON_SHELL_TEST__ 2> /dev/null
+unsetenv __OCTEON_SHELL_TEST__ 2> /dev/null || true

Return success (otherwise we return an error in bash)
echo -n ""

Found in path(s):
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-OCTEON-SDK-env-setup.patch
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diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c 2019-03-18
17:56:09.872534277 -0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c 2019-03-19
13:45:15.897107069 -0400
@@ -516,11 +516,10 @@
 if(OCTEON_IS_MODEL(OCTEON_CN56XX))
 scratch_reg_addr = CVMX_PEXP_NPEI_SCRATCH_1;

 CFG_GET_APP_MODE(&oct_cfg) = app_mode;
/* 1: Send App Mode to Host and wait for acknowledgement. */
- printf("Send App Mode to Host\n");
 scratch_val = app_mode << 16 | HOSTFW_HS_APP_INDICATION;
 scratch_val = scratch_val | ((uint64_t)(hs_support ? HOSTFW_HS_SUPPORT_INDICATION : 0) << 48);
 cvmx_write_csr_node(node, scratch_reg_addr, scratch_val);

 while((scratch_val & 0xffff) != HOSTFW_HS_ACK)
@@ -533,11 +532,10 @@
while(counter--)

```

```

{
 block_ptr = cvmx_bootmem_alloc_named(BLOCK_SIZE, NB_4MB_ALLIGNED, BLOCK_NAME);
 if (block_ptr && !(CVM_DRV_GET_PHYS(block_ptr) & (NB_4MB_ALLIGNED - 1)))
 {
- printf(" NB Allocated with Physical Address : %016llx\n", CVM_DRV_GET_PHYS(block_ptr));
 break;
 }

 if (block_ptr)
 cvmx_bootmem_free_named(BLOCK_NAME);
@@ -554,18 +552,12 @@
/* 3: write the physical address of buffer to scratch register.*/
num_if = cvmx_helper_get_number_of_interfaces();
phys_addr = CVM_DRV_GET_PHYS(block_ptr);
scratch_val = (phys_addr << 16) | HOSTFW_HS_NB_BLOCK_INDICATION;

- printf(" num_cores: %u, num intf: %lu, NB Buff Phys Addr: %016lx \n",
- num_cores, num_if, phys_addr);
- printf(" Sending NB Phys Addr to host:%016lx \n", scratch_val);
-
cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);

- printf(" Waiting for cfg details
from the core.\n");
-
/* 4: Wait for host to send cfg details. */
while(cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr) != HOSTFW_HS_ACK);

/* 5: Rad the cfg details */
@@ -587,22 +579,16 @@

support_extended_stats = CFG_GET_EXTENDED_STATS(&oct_cfg);
fw_dmac_filter = CFG_GET_FW_DMACHFLT_STATUS(&oct_cfg);
enable_mtu_vlan = CFG_GET_ENABLE_MTU_VLAN(&oct_cfg);

- printf("Extended stats support %s.\n", support_extended_stats?"Enabled":"Disabled");
- printf("Firmware DMAC filter %s.\n", fw_dmac_filter?"Enabled":"Disabled");
- printf("Allocate Buffers for Control Requests: %s\n", setup_ctl_bufs?"Enabled":"Disabled");
- printf("VLAN MTU:%d\n", enable_mtu_vlan);
-
/* 6: Send HS completion indication to host */
scratch_val = (num_if & 0xff) << 24 | (num_cores & 0xff) << 16 | HOSTFW_HS_READ_DONE_INDICATION
;
cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);

/* wait for ACK from HOST */
while(cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr) !=

```

```

HOSTFW_HS_ACK);
- printf(" Host Firmware Handshake Completed : \n");

/* Free the Allocated NB */
cvmx_bootmem_free_named(BLOCK_NAME);

/** Allocate the memory for required host pci function info. */
@@ -661,17 +647,15 @@
 pkind = cvmx_helper_get_pknd(interface, index);

 CFG_GET_DPI_PKIND(&oct_cfg) = pkind;
 /*Higher 32 bits contains pkind value and lower 32 bits contains Indication*/
 scratch_val = (pkind << 16) | HOSTFW_HS_PKIND_INDICATION;
- printf("DBG: Pkind for dpi port is: 0x%016lx\n", pkind);

 cvmx_write_csr_node(node, scratch_reg_addr, scratch_val);

 /* wait for ACK from Host */
 while(cvmx_read_csr_node(node, scratch_reg_addr) != HOSTFW_HS_ACK);
- printf("Received ACK from Host for Pkind value.\n");
 }

 /* Send core clk, co-proc clk and Active indication to Host */
 if(OCTEON_IS_MODEL(OCTEON_CN78XX) || OCTEON_IS_MODEL(OCTEON_CN73XX) ||
 OCTEON_IS_MODEL(OCTEON_CN70XX)
 || OCTEON_IS_MODEL(OCTEON_CN71XX))
diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c 2019-03-18 17:56:09.872534277 -
0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c 2019-03-18 18:49:58.437223960
-0400
@@ -742,11 +742,10 @@
 /* wait for ACK from Host */
 while(scratch_val != HOSTFW_HS_ACK)
 {
 scratch_val = cvmx_read_csr_node(node, scratch_reg_addr);
 }
- cvmcs_print("# cvmcs-ioq: Received ACK from Host for Pkind value.\n");

 return;
}

@@ -865,11 +864,10 @@
 while(scratch_val != HOSTFW_HS_PKO_READY_ACK)
 {
 cvmx_wait(500);

```

```

 scratch_val = cvmx_read_csr_node(node, scratch_reg_addr);
 }
- cvmcs_print("# cvmcs-ioq: PKO ready.\n");

 return;
}

/*
diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c 2019-03-18 17:56:09.867534215 -0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c 2019-03-19 11:32:01.175748226 -0400
@@ -139,11 +139,10 @@
 cvmx_pki_clx_pkindx_cfg_t pkind_cfg_reg;
 cvmx_pki_stylex_buf_t style_buf_reg;

 node = cvmx_get_node_num();

- printf("Entered pki reconfig\n");
 /*1. Always use cluster group-0. */
 cluster_group =0;

 /* FIXME - hack to get rid of a 'set but not used' error */
 cluster_mask = cluster_group;
@@ -200,33 +199,28 @@
 pkind_cfg_style.u64 = cvmx_read_csr_node(node, CVMX_PKI_CLX_PKINDX_STYLE(pkind, cluster_idx));
 style = pkind_cfg_style.s.style;

 /* Set SW bit in the CVMX_PKI_STYLE[style]_WQ2 Register */
 word2 = cvmx_read_csr_node(node, CVMX_PKI_STYLEX_WQ2(style));
- printf("PKI_STYLE[%d]_WQ2[sw] - Before : 0x%016lx\n", style, word2);
-#if 0
 0
 word2 = word2 | 0x8000000000000000; /* set the SW bit for the packets coming from host */
+#if 0
 /* write Word-2 of WQE to the STYLEn register with s/w bit set to 1 */
 cvmx_write_csr_node(node, CVMX_PKI_STYLEX_WQ2(style), word2);
#endif
 word2 = cvmx_read_csr_node(node, CVMX_PKI_STYLEX_WQ2(style));
- printf("PKI_STYLE[%d]_WQ2[sw] - After : 0x%016lx \n\n", style, word2);

 /* Enable this if sharing the first buffer for WQE and Packet data */
 /* Configure the first skip */
 style_buf_reg.u64 = cvmx_read_csr_node(node, CVMX_PKI_STYLEX_BUF(style));
- printf("PKI_STYLE[%d]_BUF[first_skip] - Before : 0x%016lx\n", style, style_buf_reg.u64);
-#if 0
 /* Set the FIRST_SKIP = 0x5 in CVMX_PKI_STYLE[style]_BUF - The space for WQE first 5 words */
 /* Space of Five 8B words for WQE words */

```

```

style_buf_reg.s.first_skip = 0x5;
/* Use separate buffers for WQE and Packet Data */
style_buf_reg.s.dis_wq_dat = 0x1;
#endif
cvmx_write_csr_node(node,
CVMX_PKI_STYLEX_BUF(style), style_buf_reg.u64);
- printf("PKI_STYLE[%d]_BUF[first_skip] - After : 0x%016lx \n\n", style,
- cvmx_read_csr_node(node, CVMX_PKI_STYLEX_BUF(style)));

/* For 73XX DPI Has 2 Pkinds */
if(OCTEON_IS_MODEL(OCTEON_CN73XX) && index == 0) {
 index = 1;
 goto CFG_73XX;
@@ -243,17 +237,15 @@

int
cvm_drv_init()
{

- printf("[DRV] Driver Initialization\n");
if(OCTEON_IS_MODEL(OCTEON_CN56XX_PASS1)) {
 printf("[DRV] CN56XX Pass1 is not supported\n");
 return -1;
}

- printf("[DRV] Allocating memory for octeon device\n");
oct = cvmx_drv_fpa_alloc_sync(CVMX_FPA_PACKET_POOL);
if(oct == NULL) {
 printf("[DRV] octeon device alloc failed\n");
 return -1;
}
@@ -298,11 +290,10 @@
__cn68xx_qlm_gen2_fix();

if(setup_pci_pko_ports())
 return -1;
- printf("[DRV] PKO Done\n");

/* To indicate a RAW mode packet in WQE word2 */
if (OCTEON_IS_MODEL(OCTEON_CN78XX) || OCTEON_IS_MODEL(OCTEON_CN73XX))
{
 cvmcs_pki_reconfig();

@@ -399,17 +390,15 @@
}

```



```

#endif
#endif
- printf("----- PKI Registers Configuration : End -----\n\n");

}
else
 cvmx_write_csr(CVMX_PIP_RAW_WORD, cvmx_read_csr(CVMX_PIP_RAW_WORD)|0x8000);

- printf("[DRV] Setting up DMA Engines\n");
if(cvm_dma_queue_init(oct)) {
 printf("[DRV] DMA Queue initialization failed\n");
 return -1;
}

diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-dma.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-pci-dma.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-dma.c 2019-03-18 17:56:09.867534215 -0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-dma.c 2019-03-18 18:36:00.876939905 -0400
@@ -541,17 +541,10 @@
 for(i = 0; i < 6; i++) {
 cvmx_write_csr_node(node, CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);
 }

 cvmx_write_csr_node(node, CVMX_DPI_CTL, 1);
-
- printf("DPI_DMA_CONTROL:
0x%016lx\n", cvmx_read_csr_node(node, CVMX_DPI_DMA_CONTROL));
-
- for(i = 0; i < 6; i++) {
- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr_node(node,
CVMX_DPI_ENGX_BUF(i)));
- }
-
}

/** OCTEON III models DMA Engines set up.
@@ -616,17 +609,10 @@
for(i = 0; i < 6; i++) {
 cvmx_write_csr_node(node, CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);
}

cvmx_write_csr_node(node, CVMX_DPI_CTL, 1);
-
- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr_node(node, CVMX_DPI_DMA_CONTROL));
-
- for(i = 0; i < 6; i++) {

```

```

- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr_node(node, CVMX_DPI_ENGX_BUF(i)));
- }
-
}

```

static void

cvm\_enable\_cn70xx\_dma(cvm\_oct\_dev\_t \*oct)

@@ -670,18 +656,10 @@

```

cvmx_write_csr(CVMX_DPI_DMA_CONTROL, dma_ctl.u64);

```

```

for(i = 0; i < 6; i++) {

```

```

 cvmx_write_csr(CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);

```

```

}

```

-

-

```

- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr(CVMX_DPI_DMA_CONTROL));

```

-

```

- for(i

```

```

= 0; i < 6; i++) {

```

```

- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr(CVMX_DPI_ENGX_BUF(i)));

```

```

- }

```

-

```

}

```

/\* ENable this flag if only the DMA engines in CN63XX are used and the

DPI packet Input/Output will not be used.

@@ -728,17 +706,10 @@

```

for(i = 0; i < 6; i++) {

```

```

 cvmx_write_csr(CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);

```

```

}

```

```

cvmx_write_csr(CVMX_DPI_CTL, 1);

```

-

```

- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr(CVMX_DPI_DMA_CONTROL));

```

-

```

- for(i = 0; i < 6; i++) {

```

```

- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr(CVMX_DPI_ENGX_BUF(i)));

```

```

- }

```

-

```

}

```

static void

cvm\_enable\_cn66xx\_dma(cvm\_oct\_dev\_t \*oct)

@@ -775,18 +746,10 @@

```

cvmx_write_csr(CVMX_DPI_DMA_CONTROL, dma_ctl.u64);

```

```

for(i = 0; i < 6; i++) {
 cvmx_write_csr(CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);
}
-
-
- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr(CVMX_DPI_DMA_CONTROL));
-
- for(i = 0; i < 6; i++) {
- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr(CVMX_DPI_ENGX_BUF(i)));
- }
-
}

```

@@ -832,17 +795,10 @@

```

for(i = 0; i < 6; i++) {
 cvmx_write_csr(CVMX_DPI_DMA_ENGX_EN(i), engine_qmap[i]);
}

```

```

cvmx_write_csr(CVMX_DPI_CTL, 1);

```

```

-
- printf("DPI_DMA_CONTROL: 0x%016lx\n", cvmx_read_csr(CVMX_DPI_DMA_CONTROL));
-
- for(i = 0; i < 6; i++) {
- printf("DPI_ENGX_BUF(%d): 0x%016lx\n", i, cvmx_read_csr(CVMX_DPI_ENGX_BUF(i)));
- }
-
}

```

@@ -1069,12 +1025,10 @@

```

if(oct->chunk[i] == NULL) {
 printf("[DRV] Chunk alloc failed for DMA Queue[%d]\n", i);
 cvm_free_dma_queue(oct);
 return -ENOMEM;
}
- printf("[DRV] DMA Q[%d]: Chunk @ %p size: %d max_words: %d\n", i,
- oct->chunk[i], oct->chunk[i]->chunk_size, oct->chunk[i]->max_words);

if(cvm_dma_chip_specific_setup(oct, i))
 return -EINVAL;
}

```

```
diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-loadstore.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-pci-loadstore.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-loadstore.c 2019-03-18
17:56:09.868534227 -0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-loadstore.c 2019-03-18 18:29:05.857842039 -
0400
@@ -239,12 +239,10 @@
```

```
void
cvm_setup_pci_load_store()
{
- printf("%s: Setting up registers for PCI load/store ops\n", __FUNCTION__);
-
if(OCTEON_IS_MODEL(OCTEON_CN73XX))
 cn73xx_setup_pci_load_store();

if(OCTEON_IS_MODEL(OCTEON_CN78XX))
 cn78xx_setup_pci_load_store();
@@ -261,12 +259,10 @@
if(OCTEON_IS_MODEL(OCTEON_CN56XX))
 cn56xx_setup_pci_load_store();

if(OCTEON_IS_MODEL(OCTEON_CN58XX) || OCTEON_IS_MODEL(OCTEON_CN38XX))
 cn3xxx_setup_pci_load_store();
-
- printf("[DRV] subdid base is 0x%lx\n", pci_mem_subdid_base);
}
```

```
diff -ru5 a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-pci-pko.c
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c 2019-03-18 17:56:09.868534227 -0400
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c 2019-03-19
13:42:32.055133736 -0400
@@ -77,11 +77,10 @@
if(OCTEON_IS_MODEL(OCTEON_CN73XX))
{
 if(max_droq){
 activeqcnt = max_droq;
 size = sizeof(struct __cvm_pci_pko_qmap) * max_droq;
- printf("[DRV] Active CN73xx PCI Queues: %d \n", activeqcnt);
 }
 else {
 printf("[DRV] Active CN73xx PCI Queue Count is Zero\n");
 return -1;
 }
}
```

```

}
@@ -93,12 +92,10 @@
if(max_droq)
 activeqcnt = max_droq;
else
 activeqcnt = __get_active_pci_oq_count();

- printf("[DRV] Active CN78xx PCI Queues: %d (derived from checking queue registers)\n", activeqcnt);
-
}
else if(OCTEON_IS_MODEL(OCTEON_CN68XX))
{
 cvmx_sli_tx_pipe_t slitxpipe;
 slitxpipe.u64 = cvmx_read_csr(CVMX_PEXP_SLI_TX_PIPE);
@@ -108,11 +105,10 @@
 else
 activeqcnt = __get_active_pci_oq_count();

 slitxpipe.s.nump = activeqcnt;
 cvmx_write_csr(CVMX_PEXP_SLI_TX_PIPE, slitxpipe.u64);
- printf("[DRV] Active CN68xx PCI Queues: %d (derived
from checking queue registers)\n", activeqcnt);
}
else
{

if(max_droq)

```

Found in path(s):

\* /opt/cola/permits/1305654281\_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/remove-excessive-boottime-console-output.patch

No license file was found, but licenses were detected in source scan.

```

diff -Nru OCTEON-SDK-old/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c OCTEON-
SDK/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c
--- OCTEON-SDK-old/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c 2016-08-22
11:01:01.569856904 -0700
+++ OCTEON-SDK/bootloader/u-boot/arch/mips/cpu/octeon/lib_octeon_shared.c 2016-08-22 10:52:55.786734360 -
0700
@@ -155,6 +155,7 @@
#include "configs/octeon_sff7800_shared.h"
#include "configs/octeon_niagara83x_shared.h"
#include "configs/octeon_rainier_shared.h"
+#include "configs/octeon_kilburn_shared.h"
#ifdef __U_BOOT__
include <asm/arch/octeon_eeprom_types.h>
#else
@@ -11738,6 +11739,11 @@

```

```

 { OCTEON_CLARK_SPD_VALUES };
#endif

+#if !defined(CVMX_BUILD_FOR_UBOOT) || defined(CONFIG_OCTEON_KILBURN)
+static uint8_t __attribute__((unused)) octeon_kilburn_cfg0_spd_values[256] =
+ { OCTEON_KILBURN_CFG0_SPD_VALUES };
+#endif
+
+#if !defined(CVMX_BUILD_FOR_UBOOT)
+ \|
+ defined(CONFIG_OCTEON_NAS7000_REF_EMMC_STAGE2) \| \|
+ (defined(CONFIG_OCTEON_NAS7000_REF) && defined(unix))
+@@ -12163,6 +12169,18 @@
+ }
+ },
#endif
+# if !defined(CVMX_BUILD_FOR_UBOOT) \| \|
+ defined(CONFIG_OCTEON_KILBURN)
+ {
+ .board_type = CVMX_BOARD_TYPE_CUST_KILBURN,
+ .eeprom_addr = OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR,
+ .chip_ddr_config = {
+ {
+ .ddr_config = {
OCTEON_KILBURN_DDR_CONFIGURATION(OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION)
+ }
+ }
+ }
+ },
#endif
if defined(CONFIG_OCTEON_RAINIER) || !defined(CVMX_BUILD_FOR_UBOOT)
{
.board_type = CVMX_BOARD_TYPE_RAINIER,
diff -Nru OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/config.mk OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/config.mk
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/config.mk 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/config.mk 2016-08-22 11:09:07.448857046 -0700
@@ -0,0 +1,35
@@
+#
+# (C) Copyright 2003
+# Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+# (C) Copyright 2011
+# Cavium Inc., Inc. support@cavium.com
+#
+# See file CREDITS for list of people who contributed to this
+# project.
+#

```

```

+# This program is free software; you can redistribute it and/or
+# modify it under the terms of the GNU General Public License as
+# published by the Free Software Foundation; either version 2 of
+# the License, or (at your option) any later version.
+#
+# This program is distributed in the hope that it will be useful,
+# but WITHOUT ANY WARRANTY; without even the implied warranty of
+# MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
+# GNU General Public License for more details.
+#
+# You should have received a copy of the GNU General Public License
+# along with this program; if not, write to the Free Software
+# Foundation, Inc., 59 Temple Place, Suite 330, Boston,
+# MA 02111-1307 USA
+#
+
+#
+# Oceon CN63XX
+#
+
+CONFIG_SYS_TEXT_BASE = 0xC0000000
+PLATFORM_CPPFLAGS
+= -mtune=octeon2 -finline -ffunction-sections \
+ -fdata-sections -ffree-pre -fno-asynchronous-unwind-tables \
+ -DCVMX_ENABLE_PARAMETER_CHECKING=0 \
+ -DCVMX_ENABLE_DEBUG_PRINTS=1
+#PLATFORM_CPPFLAGS += -DCVMX_ENABLE_CSR_ADDRESS_CHECKING=1
diff -Nru OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c OCTEON-
SDK/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c 1969-12-31 16:00:00.000000000 -
0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/kilburn_board.c 2016-08-22 11:08:39.233919733 -
0700
@@ -0,0 +1,562 @@
+/*
+ * (C) Copyright 2015 Cavium Inc. <support@cavium.com>
+ *
+ * SPDX-License-Identifier: GPL-2.0+
+ */
+
+#include <common.h>
+#include <asm/mipsregs.h>
+#include <asm/arch/octeon_boot.h>
+#include <asm/arch/cvmx-sim-magic.h>
+#include <asm/arch/cvmx-qlm.h>
+#include <asm/arch/octeon_fdt.h>
+#include <fdt_support.h>
+#include <asm/arch/octeon_board_common.h>

```

```

+#include
<asm/arch/lib_octeon.h>
+#include <asm/arch/lib_octeon_shared.h>
+#include <asm/arch/octeon_qlm.h>
+#include <asm/arch/cvmx-helper-util.h>
+#include <asm/arch/cvmx-helper-cfg.h>
+#include <asm/arch/cvmx-smi-defs.h>
+#include <asm/gpio.h>
+
+#define MAX_MIX_ENV_VARS 4
+
+extern void print_isp_volt(const char **labels, uint16_t isp_dev_addr, uint8_t adc_chan);
+
+extern int read_ispPAC_mvols(uint16_t isp_dev_addr, uint8_t adc_chan);
+
+extern int read_ispPAC_mvols_avg(int loops, uint16_t isp_dev_addr, uint8_t adc_chan);
+
+DECLARE_GLOBAL_DATA_PTR;
+
+/*
+ * parse_env_var: Parse the environment variable ("bgx_for_mix%d") to
+ * extract the lmac it is set to.
+ *
+ * index: Index of environment variable to parse.
+ * environment variable.
+ * env_bgx: Updated with the bgx of the lmac in the environment
+ * variable.
+ * env_lmac: Updated with the index of lmac in the environment
+ * variable.
+ *
+ * returns: Zero on success, error otherwise.
+ */
+static
+int parse_env_var(int index, int *env_bgx, int *env_lmac)
+{
+ char env_var[20];
+ ulong xipd_port;
+
+ sprintf(env_var, "bgx_for_mix%d", index);
+ if ((xipd_port = getenv_ulong(env_var, 0, 0xffff)) != 0xffff) {
+ int xiface;
+ struct cvmx_xiface xi;
+ struct cvmx_xport xp;
+
+ /*
+ * The environemt variable is set to the xipd port. Convert the
+ * xipd port to numa node, bgx, and lmac.
+ */

```



```

+ xiface = cvmx_helper_get_interface_num(xipd_port);
+ xi = cvmx_helper_xiface_to_node_interface(xiface);
+ xp = cvmx_helper_ipd_port_to_xport(xipd_port);
+ *env_bgx = xi.interface;
+ *env_lmac = cvmx_helper_get_interface_index_num(xp.port);
+ return 0;
+ }
+
+ return -1;
+ }
+
+ /*
+ * get_lmac_fdt_node: Search the device tree for the node corresponding to
+ * a given bgx lmac.
+ *
+ * fdt: Pointer to flat device tree
+ * search_node: Numa node of the lmac to search for.
+ * search_bgx: Bgx of the lmac to search for.
+ * search_lmac: Lmac index to search for.
+ * compat: Compatible
+ string to search for.
+
+ * returns: The device tree node of the lmac if found,
+ * or -1 otherwise.
+ */
+static int get_lmac_fdt_node(const void *fdt, int search_node, int search_bgx,
+ int search_lmac, const char *compat)
+{
+ int node;
+ const fdt32_t *reg;
+ u64 addr;
+ int fdt_node = -1;
+ int fdt_bgx = -1;
+ int fdt_lmac = -1;
+ int len;
+ int parent;
+
+ /* Iterate through all bgx ports */
+ node = -1;
+ while ((node = fdt_node_offset_by_compatible((void *)fdt, node,
+ compat)) >= 0) {
+ /* Get the node and bgx from the physical address */
+ if (((parent = fdt_parent_offset(fdt, node)) < 0) ||
+ !(reg = fdt_getprop(fdt, parent, "reg", &len)))
+ continue;
+
+ addr = fdt_translate_address((void *)fdt, parent, reg);
+ fdt_node = (addr >> 36) & 0x7;

```

```

+ fdt_bgx = (addr >> 24) & 0xf;
+
+ /* Get the lmac index from the reg property */
+ if ((reg = fdt_getprop(fdt, node, "reg", &len)))
+ fdt_lmac = *reg;
+
+ /* Check for a
+ match */
+ if (search_node == fdt_node && search_bgx == fdt_bgx &&
+ search_lmac == fdt_lmac)
+ return node;
+ }
+
+ return -1;
+}
+
+/*
+ * get_mix_fdt_node: Search the device tree for the node corresponding to
+ * a given mix.
+ *
+ * fdt: Pointer to flat device tree
+ * search_node: Mix numa node to search for.
+ * search_index: Mix index to search for.
+ *
+ * returns: The device tree node of the lmac if found,
+ * or -1 otherwise.
+ */
+static int get_mix_fdt_node(const void *fdt, int search_node, int search_index)
+{
+ int node;
+
+ /* Iterate through all the mix fdt nodes */
+ node = -1;
+ while ((node = fdt_node_offset_by_compatible((void *)fdt, node,
+ "cavium,octeon-7890-mix")) >= 0) {
+ int parent;
+ int len;
+ const char *name;
+ int mix_numa_node;
+ const fdt32_t *reg;
+ int mix_index = -1;
+ u64 addr;
+
+ /* Get the numa node of the mix from the parent node name */
+ if (((parent = fdt_parent_offset(fdt, node)) < 0) ||
+ ((name =
+ fdt_get_name(fdt, parent, &len)) == NULL) ||
+ ((name = strchr(name, '@')) == NULL))

```

```

+ continue;
+
+ name++;
+ mix_numa_node = simple_strtol(name, NULL, 0) ? 1 : 0;
+
+ /* Get the mix index from the reg property */
+ if ((reg = fdt_getprop(fdt, node, "reg", &len)) {
+ addr = fdt_translate_address((void *)fdt, parent, reg);
+ mix_index = (addr >> 11) & 1;
+ }
+
+ /* Check for a match */
+ if (mix_numa_node == search_node && mix_index == search_index)
+ return node;
+ }
+
+ return -1;
+}
+
+/*
+ * fdt_fix_mix: Fix the mix nodes in the device tree. Only the mix nodes
+ * configured by the user will be preserved. All other mix
+ * nodes will be trimmed.
+ *
+ * fdt: Pointer to flat device tree
+ *
+ * returns: Zero on success, error otherwise.
+ */
+static int fdt_fix_mix(const void *fdt)
+{
+ int node;
+ int next_node;
+ int len;
+ int i;
+
+ /* Parse all the mix port environment variables */
+ for (i = 0; i < MAX_MIX_ENV_VARS; i++) {
+ int env_node = 0;
+ int env_bgx
+ = -1;
+ int env_lmac = -1;
+ int lmac_fdt_node = -1;
+ int mix_fdt_node = -1;
+ int lmac_phandle;
+ char *compat;
+
+ /* Get the lmac for this environment variable */
+ if (parse_env_var(i, &env_bgx, &env_lmac))

```

```

+ continue;
+
+ /* Get the fdt node for this lmac and add a phandle to it */
+ compat = "cavium,octeon-7890-bgx-port";
+ if ((lmac_fdt_node = get_lmac_fdt_node(fdt, env_node, env_bgx,
+ env_lmac, compat)) < 0) {
+ /* Must check for the xcv compatible string too */
+ compat = "cavium,octeon-7360-xcv";
+ if ((lmac_fdt_node = get_lmac_fdt_node(fdt, env_node,
+ env_bgx,
+ env_lmac,
+ compat)) < 0) {
+ printf("WARNING: Failed to get lmac fdt node "
+ "for %d%d%d\n", env_node, env_bgx,
+ env_lmac);
+ continue;
+ }
+ }
+
+ lmac_phandle = fdt_alloc_phandle((void *)fdt);
+ fdt_set_phandle((void *)fdt, lmac_fdt_node, lmac_phandle);
+
+ /* Get the fdt mix node corresponding to this
+ lmac */
+ if ((mix_fdt_node = get_mix_fdt_node(fdt, env_node,
+ env_lmac)) < 0)
+ continue;
+
+ /* Point the mix to the lmac */
+ fdt_getprop(fdt, mix_fdt_node, "cavium,mac-handle", &len);
+ fdt_setprop_inplace((void *)fdt, mix_fdt_node,
+ "cavium,mac-handle", &lmac_phandle, len);
+ }
+
+ /* Trim unused mix'es from the device tree */
+ for (node = fdt_next_node(fdt, -1, NULL); node >= 0;
+ node = next_node) {
+ const char *compat;
+ const fdt32_t *reg;
+
+ next_node = fdt_next_node(fdt, node, NULL);
+
+ if ((compat = fdt_getprop(fdt, node, "compatible", &len)) {
+ if (strcmp(compat, "cavium,octeon-7890-mix"))
+ continue;
+
+ if ((reg = fdt_getprop(fdt, node,
+ "cavium,mac-handle", &len)) {

```

```

+ if (*reg == 0xffff)
+ fdt_nop_node((void *)fdt, node);
+ }
+ }
+ }
+
+ return 0;
+}
+
+int no_phy[8] = {0, 0, 0, 0, 0, 0, 0, 0};
+
+static void kill_fdt_phy(void *fdt, int offset, void *arg)
+{
+ int len, phy_offset;
+ const fdt32_t
+ *php;
+ uint32_t phandle;
+
+ php = fdt_getprop(fdt, offset, "phy-handle", &len);
+ if (php && len == sizeof(*php)) {
+ phandle = fdt32_to_cpu(*php);
+ fdt_nop_property(fdt, offset, "phy-handle");
+ phy_offset = fdt_node_offset_by_phandle(fdt, phandle);
+ if (phy_offset > 0)
+ fdt_nop_node(fdt, phy_offset);
+ }
+}
+
+void __fixup_xcv(void)
+{
+ unsigned long bgx = getenv_ulong("bgx_for_rgmii", 10, (unsigned long)-1);
+ char fdt_key[16];
+ int i;
+
+ debug("%s: BGX %d\n", __func__, (int)bgx);
+
+ for (i = 0; i < 3; i++) {
+ snprintf(fdt_key, sizeof(fdt_key),
+ bgx == i ? "%d,xcv" : "%d,not-xcv", i);
+ debug("%s: trimming bgx %lu with key %s\n",
+ __func__, bgx, fdt_key);
+
+ octeon_fdt_patch_rename((void *)gd->fdt_blob, fdt_key,
+ "cavium,xcv-trim", true, NULL, NULL);
+ }
+}
+
+/* QLM0 - QLM6 */

```

```

+void __fixup_fdt(void)
+{
+ int qlm;
+ int speed = 0;
+
+ for (qlm = 0; qlm < 7; qlm++) {
+ enum cvmx_qlm_mode mode;
+ char fdt_key[16];
+ const char *type_str = "none";
+ mode
+ = cvmx_qlm_get_mode(qlm);
+ switch (mode) {
+ case CVMX_QLM_MODE_SGMII:
+ case CVMX_QLM_MODE_RGMII_SGMII:
+ case CVMX_QLM_MODE_RGMII_SGMII_1X1:
+ type_str = "sgmii";
+ break;
+ case CVMX_QLM_MODE_XAUI:
+ case CVMX_QLM_MODE_RGMII_XAUI:
+ speed = (cvmx_qlm_get_gbaud_mhz(qlm) * 8 / 10) * 4;
+ if (speed == 10000)
+ type_str = "xaui";
+ else
+ type_str = "dxaui";
+ break;
+ case CVMX_QLM_MODE_RXAUI:
+ case CVMX_QLM_MODE_RGMII_RXAUI:
+ type_str = "rxaui";
+ break;
+ case CVMX_QLM_MODE_XLAUI:
+ case CVMX_QLM_MODE_RGMII_XLAUI:
+ type_str = "xlaui";
+ break;
+ case CVMX_QLM_MODE_XFI:
+ case CVMX_QLM_MODE_RGMII_XFI:
+ case CVMX_QLM_MODE_RGMII_XFI_1X1:
+ type_str = "xfi";
+ break;
+ case CVMX_QLM_MODE_10G_KR:
+ case CVMX_QLM_MODE_RGMII_10G_KR:
+ type_str = "10G_KR";
+ break;
+ case CVMX_QLM_MODE_40G_KR4:
+ case CVMX_QLM_MODE_RGMII_40G_KR4:
+ type_str = "40G_KR4";
+ break;
+ case CVMX_QLM_MODE_SATA_2X1:
+ type_str = "sata";

```

```

+ break;
+ case
CVMX_QLM_MODE_SGMII_2X1:
+ case CVMX_QLM_MODE_XFI_1X2:
+ case CVMX_QLM_MODE_10G_KR_1X2:
+ case CVMX_QLM_MODE_RXAUI_1X2:
+ case CVMX_QLM_MODE_MIXED: // special for DLM5 & DLM6
+ {
+ cvmx_bgxx_cmr_config_t cmr_config;
+ cvmx_bgxx_spux_br_pmd_control_t pmd_control;
+ int mux = cvmx_qlm_mux_interface(2);
+ if (mux == 2) { // only dlm6
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CMRX_CONFIG(2, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(2, 2));
+ } else {
+ if (qlm == 5) {
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CMRX_CONFIG(0, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(0, 2));
+ } else {
+ cmr_config.u64 = cvmx_read_csr(
+ CVMX_BGXX_CMRX_CONFIG(2, 2));
+ pmd_control.u64 = cvmx_read_csr(
+ CVMX_BGXX_SPUX_BR_PMD_CONTROL(2, 2));
+ }
+ }
+ switch (cmr_config.s.lmac_type) {
+ case 0:
+ type_str = "sgmii";
+ break;
+ case 1:
+ type_str
= "xaui";
+ break;
+ case 2:
+ type_str = "rxaui";
+ break;
+ case 3:
+ if (pmd_control.s.train_en)
+ type_str = "10G_KR";
+ else
+ type_str = "xfi";
+ break;
+ case 4:
+ if (pmd_control.s.train_en)

```

```

+ type_str = "40G_KR4";
+ else
+ type_str = "xlaii";
+ break;
+ default:
+ type_str = "none";
+ break;
+ }
+ break;
+ }
+ default:
+ type_str = "none";
+ break;
+ }
+ sprintf(fdt_key, "%d,%s", qlm, type_str);
+ debug("Patching qlm %d for %s for mode %d%s\n",
+ qlm, fdt_key, mode, no_phy[qlm] ? ", removing PHY" : "");
+ octeon_fdt_patch_rename((void *)gd->fdt_blob, fdt_key, NULL,
+ true, no_phy[qlm] ? kill_fdt_phy : NULL, NULL);
+ }
+}
+
+int board_fixup_fdt(void)
+{
+ __fixup_fdt();
+ __fixup_xcv();
+
+ /* Fix the mix ports */
+ fdt_fix_mix(gd->fdt_blob);
+
+ return 0;
+}
+
+/* Raise an integer to a power */
+static uint64_t ipow(uint64_t base, uint64_t exp)
+{
+ uint64_t result = 1;
+ while (exp)
+ {
+ if (exp & 1)
+ result *= base;
+ exp >>= 1;
+ base *= base;
+ }
+ return result;
+}
+
+/*

```



```

+ * This function now supports only single (node0) configuration.
+ * Some of the called funcs (read_ispPAC_mvolt_avg(), octeon_led_str_write())
+ * are hard-coded for node0. Also we set gd->arch.mcu_rev_maj, ...
+ * It can be extended to support multinode if needed/important
+ */
+static int checkboardinfo(void)
+{
+ int core_mVolts, dram_mVolts0, dram_mVolts1;
+ char mcu_ip_msg[64] = { 0 };
+ char tmp[10];
+ int characters, idx = 0, value = 0;
+
+ debug("In %s\n", __func__);
+ if (octeon_show_info()) {
+
+ int mcu_rev_maj = 0;
+ int mcu_rev_min = 0;
+
+ if (twsii_mcu_read(0x00) == 0xa5
+ && twsii_mcu_read(0x01) == 0x5a) {
+ gd->arch.mcu_rev_maj = mcu_rev_maj = twsii_mcu_read(2);
+ gd->arch.mcu_rev_min = mcu_rev_min = twsii_mcu_read(3);
+ } else {
+ return -1; /* Abort if we can't access the MCU */
+ }
+
+ core_mVolts = read_ispPAC_mvolt_avg(10,
+ BOARD_ISP_TWSI_ADDR, 8);
+ dram_mVolts0 = read_ispPAC_mvolt_avg(10, BOARD_ISP_TWSI_ADDR, 7); /* DDR0 */
+ dram_mVolts1 = read_ispPAC_mvolt_avg(10, BOARD_ISP_TWSI_ADDR, 6); /* DDR1 */
+
+ if (twsii_mcu_read(0x14) == 1)
+ sprintf(mcu_ip_msg, "MCU IPaddr: %d.%d.%d.%d, ",
+ twsii_mcu_read(0x10),
+ twsii_mcu_read(0x11),
+ twsii_mcu_read(0x12),
+ twsii_mcu_read(0x13));
+ printf("MCU rev: %d.%02d, %sCPU voltage: %d.%03d DDR{0,1} voltages: %d.%03d,%d.%03d\n",
+ gd->arch.mcu_rev_maj, gd->arch.mcu_rev_min, mcu_ip_msg,
+ core_mVolts / 1000, core_mVolts % 1000,
+ dram_mVolts0 / 1000, dram_mVolts0 % 1000,
+ dram_mVolts1 / 1000, dram_mVolts1 % 1000);
+
+ #define LED_CHARACTERS 8
+ value = core_mVolts;
+ idx = sprintf(tmp, "%lu ", gd->cpu_clk/(1000*1000));
+ characters = LED_CHARACTERS - idx;
+
+

```

```

+ if (value / 1000) {
+ idx += sprintf(tmp + idx, "%d", value / 1000);
+ characters = LED_CHARACTERS - idx;
+ }
+
+ characters
- = 1; /* Account for decimal point */
+
+ value %= 1000;
+ value = DIV_ROUND_UP(value, ipow(10, max(3 - characters, 0)));
+
+ idx += sprintf(tmp + idx, "%0*d", min(3, characters), value);
+
+ /* Display CPU speed and voltage on display */
+ octeon_led_str_write(tmp);
+ } else {
+ octeon_led_str_write("Boot ");
+ }
+
+ return 0;
+}
+
+/**
+ * Here is the description of the parameters that are passed to QLM configuration
+ * param0 : The QLM to configure
+ * param1 : Speed to configure the QLM at
+ * param2 : Mode the QLM to configure
+ * param3 : 1 = RC, 0 = EP
+ * param4 : 0 = GEN1, 1 = GEN2, 2 = GEN3
+ * param5 : ref clock select, 0 = 100Mhz, 1 = 125MHz, 2 = 156MHz
+ * param6 : ref clock input to use:
+ * 0 - external reference (QLMx_REF_CLK)
+ * 1 = common clock 0 (QLMC_REF_CLK0)
+ * 2 = common_clock 1 (QLMC_REF_CLK1)
+ */
+void board_configure_qlms(void)
+{
+ int node=0;
+
+ +
+ octeon_init_qlm(0);
+ mdelay(100);
+
+ +
+ cvmx_coremask_for_each_node(node, gd->arch.node_mask)
+ {
+ + debug("Configuring node %d, QLM 0 and 1 \n", node);
+ + octeon_configure_qlm(0, 8000, CVMX_QLM_MODE_PCIE_1X8, 0, 2, 0, 1); /* PCIEG3X8 */
+ + octeon_configure_qlm(1, 8000, CVMX_QLM_MODE_PCIE_1X8, 0, 2, 0, 1); /* PCIEG3X8 */
+ +

```

```

+
+ debug("Configuring node %d, QLMs 2 for XLAUI\n", node);
+ octeon_configure_qlm(2, 103125, CVMX_QLM_MODE_XLAUI, 0, 0, 2, 2); /* 40G_KR4 */
+ octeon_qlm_tune_v3(0, 2, 103125, 0xd, 0xd0, -1, -1);
+
+ /* For other skews of kilburn, uncomment them */
+
+ printf("Configuring node %d, QLMs 3 for XAUI\n", node);
+ octeon_configure_qlm(3, 103125, CVMX_QLM_MODE_XFI, 0, 0, 2, 2); /* 10G_KR */
+ octeon_qlm_tune_v3(0, 3, 103125, 0xd, 0xd0, -1, -1);
+ }
+ }
+
+int checkboard(void)
+{
+ checkboardinfo();
+
+ return 0;
+}
+
+int early_board_init(void)
+{
+ octeon_board_get_clock_info(KILBURN_DEF_DRAM_FREQ);
+ octeon_board_get_descriptor(CVMX_BOARD_TYPE_CUST_KILBURN, 1, 0);
+
+ /* Even though the CPU ref freq
+ is stored in the clock descriptor, we
+ * don't read it here. The MCU reads it and configures the clock, and
+ * we read how the clock is actually configured.
+ * The bootloader does not need to read the clock descriptor tuple for
+ * normal operation on rev 2 and later boards.
+ */
+ octeon_board_get_mac_addr();
+
+ return 0;
+}
diff -Nru OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn.dts OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/kilburn.dts
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/kilburn.dts 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/kilburn.dts 2016-08-22 11:08:51.849670066 -0700
@@ -0,0 +1,181 @@
+/dts-v1/;
+/*
+ * Cavium Inc. Kilburn 76XX
+ */
+ {
+ model = "cavium,kilburn";
+ compatible = "cavium,kilburn";

```

```

+ #address-cells = <2>;
+ #size-cells = <2>;
+
+ soc@0 {
+ interrupt-parent = <&ciu3>;
+ compatible = "simple-bus";
+ #address-cells = <2>;
+ #size-cells = <2>;
+ ranges; /* Direct
mapping */
+ cavium,node-trim = "0,soc";
+
+ ciu3: interrupt-controller@101000000000 {
+ compatible = "cavium,octeon-7890-ciu3";
+ interrupt-controller;
+ /* Interrupts are specified by two parts:
+ * 1) Source number (20 significant bits)
+ * 2) Trigger type: (4 == level, 1 == edge)
+ */
+ #address-cells = <0>;
+ #interrupt-cells = <2>;
+ reg = <0x10100 0x00000000 0x0 0xb0000000>;
+ };
+
+ bootbus: bootbus@118000000000 {
+ compatible = "cavium,octeon-3860-bootbus";
+ reg = <0x11800 0x00000000 0x0 0x200>;
+ /* The chip select number and offset */
+ #address-cells = <2>;
+ /* The size of the chip select region */
+ #size-cells = <1>;
+ ranges = <0 0 0 0x1ec00000 0x1400000>,
+ <1 0 0x10000 0x10000000 0>,
+ <2 0 0x10000 0x20000000 0>,
+ <3 0 0x10000 0x30000000 0>,
+ <4 0 0x10000 0x40000000 0>,
+ <5 0 0x10000 0x50000000 0>,
+ <6 0 0x10000 0x60000000 0>,
+ <7 0 0x10000 0x90000000 0>;
+
+ cavium,cs-config@0
+ {
+ compatible = "cavium,octeon-3860-bootbus-config";
+ cavium,cs-index = <0>;
+ cavium,t-adr = <10>;
+ cavium,t-ce = <100>;
+ cavium,t-oe = <30>;
+ cavium,t-we = <25>;

```

```

+ cavium,t-rd-hld = <0>;
+ cavium,t-wr-hld = <20>;
+ cavium,t-pause = <10>;
+ cavium,t-wait = <0>;
+ cavium,t-page = <20>;
+ cavium,t-rd-dly = <0>;
+
+ cavium,page-mode = <1>;
+ cavium,pages = <8>;
+ cavium,bus-width = <16>;
+ cavium,ale-mode = <1>;
+ };
+
+ flash0: nor@0,0 {
+ compatible = "cfi-flash";
+ reg = <0 0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <1>;
+
+ partition@0 {
+ label = "bootloader";
+ reg = <0 0x340000>;
+ read-only;
+ };
+ partition@300000 {
+ label = "storage";
+ reg = <0x340000 0xcbe000>;
+ };
+ partition@fe0000 {
+ label = "environment";
+ reg = <0xfe0000 0x20000>;
+ read-only;
+ };
+ };
+ };
+
+ serial@1180000000800 {
+ compatible = "cavium,octeon-3860-uart","ns16550";
+ reg
+ = <0x11800 0x00000800 0x0 0x400>;
+ clock-frequency = <0>;
+ current-speed = <115200>;
+ reg-shift = <3>;
+ interrupts = <0x08000 4>;
+ };
+
+ serial@1180000000c00 {
+ compatible = "cavium,octeon-3860-uart","ns16550";

```

```

+ reg = <0x11800 0x00000c00 0x0 0x400>;
+ clock-frequency = <0>;
+ current-speed = <115200>;
+ reg-shift = <3>;
+ interrupts = <0x08040 4>;
+ };
+
+ gpio0: gpio-controller@1070000000800 {
+ #gpio-cells = <2>;
+ compatible = "cavium,octeon-7890-gpio";
+ reg = <0x10700 0x00000800 0x0 0x100>;
+ gpio-controller;
+ /* Interrupts are specified by two parts:
+ * 1) GPIO pin number (0..15)
+ * 2) Triggering (1 - edge rising
+ * 2 - edge falling
+ * 4 - level active high
+ * 8 - level active low)
+ */
+ interrupt-controller;
+ #interrupt-cells = <2>;
+ /* The GPIO pins connect to 16 consecutive CUI bits */
+ interrupts = <0x03000 4>, <0x03001 4>, <0x03002 4>, <0x03003 4>,
+
+ <0x03004 4>, <0x03005 4>, <0x03006 4>, <0x03007 4>,
+ <0x03008 4>, <0x03009 4>, <0x0300a 4>, <0x0300b 4>,
+ <0x0300c 4>, <0x0300d 4>, <0x0300e 4>, <0x0300f 4>;
+ };
+
+
+
+ /* BGX definitions here must match kilburn_board.c settings. */
+ /* BGX 0*/
+ ethernet-mac-nexus@11800e0000000 {
+ compatible = "cavium,octeon-7890-bgx";
+ reg = <0x11800 0xe0000000 0x0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <0>;
+
+ /* SerDes 0, may differ from PCS Lane/LMAC */
+ ethernet-mac@0 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <0>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "2,xlaui";
+ };
+ };
+

```

```

+ /* BGX 1*/
+ ethernet-mac-nexus@11800e1000000 {
+ compatible = "cavium,octeon-7890-bgx";
+ reg = <0x11800 0xe1000000 0x0 0x1000000>;
+ #address-cells = <1>;
+ #size-cells = <0>;
+ ethernet-mac@0 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <0>;
+ local-mac-address = [00 00 00
00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@1 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <1>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@2 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <2>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+ ethernet-mac@3 {
+ compatible = "cavium,octeon-7890-bgx-port";
+ reg = <3>;
+ local-mac-address = [00 00 00 00 00 00];
+ cavium,qlm-trim = "3,xfi";
+ };
+
+ };
+ };
+ };
diff -Nru OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/Makefile OCTEON-SDK/bootloader/u-
boot/board/octeon/kilburn/Makefile
--- OCTEON-SDK-old/bootloader/u-boot/board/octeon/kilburn/Makefile 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/board/octeon/kilburn/Makefile 2016-08-22 11:09:00.567759719 -0700
@@ -0,0 +1,68 @@
+#
+# (C) Copyright 2003
+# Wolfgang Denk, DENX Software Engineering,
+ wd@denx.de.
+#
+# See file CREDITS for list of people who contributed to this
+# project.
+#

```

```

+# This program is free software; you can redistribute it and/or
+# modify it under the terms of the GNU General Public License as
+# published by the Free Software Foundation; either version 2 of
+# the License, or (at your option) any later version.
+#
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+# You should have received a copy of the GNU General Public License
+# along with this program; if not, write to the Free Software
+# Foundation, Inc., 59 Temple Place, Suite 330, Boston,
+# MA 02111-1307 USA
+#
+#
+include $(TOPDIR)/config.mk
+
+CFLAGS += -I$(TOPDIR)/arch/mips/include/asm/arch-octeon
+
+LIB = $(obj)lib$(BOARD).o
+
+COBJS = $(BOARD)_board.o
+SOBJS =
+COBJS-y =
+#OBS = $(BOARD).o
flash.o
+
+SRCS := $(SOBJS:.o=.S) $(COBJS:.o=.c) $(COBJS-y:.o=.c)
+OBS := $(addprefix $(obj),$(COBJS))
+SOBJS := $(addprefix $(obj),$(SOBJS))
+
+all: $(obj).depend $(LIB)
+
+DTB = $(BOARD).dtb.o
+DTBOBJS := $(addprefix $(obj),$(DTB))
+
+$(BOARD).dtb.o: $(BOARD).dtb.S
+
+$(BOARD).dtb.S: $(BOARD).dtb
+ echo '.section .rodata, "a"' > $@
+ echo '.balign 8' >> $@
+ echo '.global __dtb_begin' >> $@
+ echo '__dtb_begin:' >> $@
+ echo '.incbin "$<"' >> $@
+ echo '__dtb_end:' >> $@
+ echo '.global __dtb_end' >> $@
+ echo '.balign 8' >> $@

```



```

+
+$(BOARD).dtb: $(BOARD).dts
+ $(TOPDIR)/tools/dtc -O dtb -p 1024 -o $@ $<
+
+$(LIB): $(obj).depend $(OBS) $(SOBJS) $(DTBOBJS)
+ $(call cmd_link_o_target, $(OBS) $(SOBJS) $(DTBOBJS))
+
+#####
+include $(SRCTREE)/rules.mk
+
+
+sinclude $(obj).depend
+
+
+clean:
+ -rm -f $(BOARD).dtb $(BOARD).dtb.S
diff -Nru OCTEON-SDK-old/bootloader/u-boot/boards.cfg OCTEON-SDK/bootloader/u-boot/boards.cfg

OCTEON-SDK-old/bootloader/u-boot/boards.cfg 2016-08-22 11:03:25.284816349 -0700
+++ OCTEON-SDK/bootloader/u-boot/boards.cfg 2016-08-22 11:05:15.143742406 -0700
@@ -112,3 +112,4 @@
Target ARCH CPU Board name Vendor SoC Options
#####
#####
octeon_rainier mips octeon rainier octeon -
octeon_rainier:OCTEON_RAINIER,OCTEON_PCI_HOST=1
+octeon_kilburn mips octeon kilburn octeon -
octeon_kilburn:OCTEON_KILBURN,OCTEON_PCI_HOST=0
diff -Nru OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn.h OCTEON-SDK/bootloader/u-
boot/include/configs/octeon_kilburn.h
--- OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn.h 1969-12-31 16:00:00.000000000 -0800
+++ OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn.h 2016-08-22
10:53:45.879796696 -0700
@@ -0,0 +1,280 @@
+/*
+ * (C) Copyright 2003
+ * Wolfgang Denk, DENX Software Engineering, wd@denx.de.
+ *
+ * Copyright (C) 2015 Cavium, Inc. <support@cavium.com>
+ *
+ * SPDX-License-Identifier: GPL-2.0+
+ */
+
+
+/* This file contains the configuration parameters for
+ * Octeon EBB7304 board.
+ */
+
+
+#ifndef __CONFIG_H__
+#define __CONFIG_H__

```

```

+
+/**
+ * Define CONFIG_OCTEON_PCI_HOST = 1 to map the pci devices on the
+ * bus. Define CONFIG_OCTEON_PCI_HOST = 0 for target mode when the
+ * host system performs the pci bus mapping instead. Note that pci
+ * commands are enabled to allow access to configuration space for
+ * both modes.
+ */
+#ifndef CONFIG_OCTEON_PCI_HOST
+# define CONFIG_OCTEON_PCI_HOST 1
+#endif
+
+/** Enable USB support on OCTEON III */
+#define CONFIG_OCTEON_USB_OCTEON3
+
+#define CONFIG_OCTEON_BIG_STACK_SIZE 0x4000 /** 16K stack */
+
+#include "octeon_common.h"
+
+/* CONFIG_OCTEON_KILBURN set by Makefile in include/config.h */
+
+/**
Default DDR clock if tuple doesn't exist in EEPROM */
+#define KILBURN_DEF_DRAM_FREQ 1050
+
+//#define CONFIG_OCTEON_ENABLE_LED_DISPLAY /** 8-char LED support */
+
+#define CONFIG_LBA48 /** 48-bit mode */
+#define CONFIG_SYS_64BIT_LBA /** 64-bit LBA support */
+#define CONFIG_SYS_ATA_BASE_ADDR 0 /** Make compile happy */
+/* Base address of Common memory for Compact flash */
+
+/* eMMC support */
+#define CONFIG_OCTEON_MMC /** Enable MMC support */
+#define CONFIG_MMC_MBLOCK /** Multi-block support */
+#define CONFIG_CMD_MMC /** Enable mmc command */
+#define CONFIG_SYS_MMC_SET_DEV /** Enable multiple MMC devices */
+#define CONFIG_MMC /** Enable MMC support */
+#define CONFIG_OCTEON_MIN_BUS_SPEED_HZ 100000 /** Minimum MMC bus speed */
+
+/** Maximum number of MMC devices needed for API */
+#define CONFIG_SYS_MMC_MAX_DEVICE 2
+
+#if 0
+/** Enable support for native U-Boot application API */
+#define CONFIG_API
+#endif
+

```

```

+
+/* TWSI address for the ispPAC-POWR1220AT8 Voltage
Controller chip */
+#define BOARD_ISP_TWSI_ADDR 0x08
+#define CONFIG_ISPPAC_POWER
+
+/*** Address of MCU on TWSI bus */
+#define BOARD_MCU_TWSI_ADDR 0x60
+
+/*** Address of board EEPROM on TWSI bus */
+#define CONFIG_SYS_I2C_EEPROM_ADDR OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR
+/*** Default EEPROM address */
+#define CONFIG_SYS_DEF_EEPROM_ADDR CONFIG_SYS_I2C_EEPROM_ADDR
+/* These speed up writes to the serial EEPROM by enabling page writes.
+ * Please see the datasheet for details.
+ */
+#define CONFIG_SYS_EEPROM_PAGE_WRITE_BITS 5 /* 32 bytes */
+#define CONFIG_SYS_EEPROM_PAGE_WRITE_DELAY_MS 5
+
+/* Set bootdelay to 0 for immediate boot */
+#define CONFIG_BOOTDELAY 0 /*** autoboot after X seconds */
+
+#undef CONFIG_BOOTARGS
+
+/*** Enable octbootbus command */
+#define CONFIG_CMD_OCTEON_BOOTBUS
+
+/***
+ * The 'mtdids' environment variable is used in conjunction with the 'mtdparts'
+ * variable to define the MTD partitions for u-boot.
+ */
+#define MTDPARTS_DEFAULT \
+ "mtdparts=octeon_nor0:3m(bootloader)ro,"
+ \
+ "2m(kernel)," \
+ "3064k(cramfs)," \
+ "8k(environment)ro;" \
+ "octeon_nand0:6m(bootloader)ro," \
+ "2m(environment)," \
+ "8m(kernel)," \
+ "512m(rootfs)\0"
+
+#define MTDIDS_DEFAULT "nor0=octeon_nor0,nand0=octeon_nand0\0"
+
+/***
+ * End of the NAND boot partition which is searched for a bootloader. Must be
+ * on an erase block boundary. It should be large enough to be able to handle
+ * at least one bad block.

```

```

+ */
+#define CONFIG_OCTEON_NAND_BOOT_END 0x600000
+
+
+/* Define this to enable built-in octeon ethernet support */
+#define CONFIG_OCTEON_SGMII_ENET /** SGMII ethernet support */
+#define CONFIG_OCTEON_XAUI_ENET /** XAUI ethernet support */
+
+/* Enable Octeon built-in networking if RGMII support is enabled */
+#if
 defined(CONFIG_OCTEON_RGMII_ENET) || defined(CONFIG_OCTEON_SGMII_ENET) || \
+ defined(CONFIG_OCTEON_XAUI_ENET) || defined(CONFIG_OCTEON_QSGMII_ENET)
+# define CONFIG_OCTEON_INTERNAL_ENET
+#endif
+#define CONFIG_OCTEON_MGMT_ENET
+
+/* PCI console is supported since oct-remote-boot is supported.
+ * Enable MUX and oct-remote-bootcmd support as well
+ */
+#define CONFIG_SYS_PCI_CONSOLE
+#define CONFIG_CONSOLE_MUX
+#define CONFIG_OCTEON_BOOTCMD
+
+#include "octeon_cmd_conf.h"
+
+#ifdef CONFIG_CMD_NET
+/**
+ * Define available PHYs
+ */
+# define CONFIG_PHY_GIGE /** Gigabit Ethernet PHY support */
+# define CONFIG_PHY_MARVELL /** Marvell PHY support */
+# define CONFIG_PHY_VITESSE /** Vitesse PHY support */
+# define CONFIG_PHY_BROADCOM /** Broadcom PHY support */
+# define CONFIG_PHY_CORTINA /** Cortina PHY support */
+# define CONFIG_PHY_CORTINA_CS4223
+# include "octeon_network.h"
+#endif
+
+
+
+#define CONFIG_CMD_OCTEON_TLVEEPROM /** "tlv_eeeprom" command */
+/* "qlm" command */
+#define
 CONFIG_CMD_QLM /** QLM command */
+
+/* Enable MTD partitioning support */
+#define CONFIG_CMD_MTDPARTS
+

```

```

+/* Enable JFFS2 support */
+#define CONFIG_CMD_JFFS2
+
+/** Enable "date" command to set and get date from RTC */
+#define CONFIG_CMD_DATE
+#define CONFIG_CMD_FLASH /** flinfo, erase, protect */
+#define CONFIG_CMD_EXT2 /** EXT2/3 filesystem support */
+#define CONFIG_CMD_EXT4 /** EXT4 filesystem support */
+#define CONFIG_CMD_FAT /** FAT support */
+#define CONFIG_FAT_WRITE /** FAT write support */
+
+/* SPI NOR flash support */
+#define CONFIG_SF_DEFAULT_BUS 0 /** SPI flash default bus */
+#define CONFIG_SF_DEFAULT_CS 0 /** SPI flash default chip select */
+#define CONFIG_SF_DEFAULT_SPEED 50000000 /** Default speed in MHz */
+
+#define CONFIG_OCTEON_SPI /** Enable OCTEON SPI driver */
+#define CONFIG_SPI_FLASH /** Enable SPI flash driver */
+#define CONFIG_SPI_FLASH_STMICRO /** Enable ST Micro SPI flash */
+#define CONFIG_CMD_SPI /** Enable SPI command */
+#define
CONFIG_CMD_SF /** Enable SPI flash command */
+
+#define CONFIG_OCTEON_SPI_NO_FAILSAFE /** SPI failsafe not yet supported */
+/**
+ * Start of where the SPI bootloader images are placed. Must be on an erase
+ * block boundary.
+ */
+#define CONFIG_OCTEON_SPI_BOOT_START 0x10000
+
+/**
+ * End of the SPI boot partition which is searched for a bootloader. Must be
+ * on an erase block boundary.
+ */
+#define CONFIG_OCTEON_SPI_BOOT_END 0x400000
+
+/* Maximum number of NAND devices */
+#define CONFIG_SYS_MAX_NAND_DEVICE 1
+#define CONFIG_SYS_NAND_FORCE_WIDTH 8 /** Force to 8-bit width */
+/* Not used for Octeon, but must be set */
+#define CONFIG_SYS_NAND_BASE 0
+/* Maximum number of NAND devices */
+#define CONFIG_SYS_NAND_MAX_CHIPS CONFIG_SYS_MAX_NAND_DEVICE
+
+/* Enable multi-bit ECC support */
+#define CONFIG_OCTEON_HW_BCH
+#include "config_octeon_nand.h"
+

```

```

+
+/*
+ * Miscellaneous configurable options
+ */
+/** Environment variables that
+ will be set by default */
+#define CONFIG_EXTRA_ENV_SETTINGS \
+ "burn_app=erase $(flash_unused_addr) +$(filesize);cp.b $(fileaddr) $(flash_unused_addr) $(filesize)\0" \
+ "bf=bootoct $(flash_unused_addr) forceboot numcores=$(numcores)\0" \
+ "linux_mmc=fatload mmc 1 $(loadaddr) vmlinux.64;bootoctlinux $(loadaddr)\0" \
+ "autoload=n\0" \
+ "ls=fatls mmc 0\0" \
+ ""
+
+/*-----
+ * FLASH and environment organization
+ */
+#define CONFIG_SYS_FLASH_SIZE (16*1024*1024) /** Flash size (bytes) */
+#define CONFIG_SYS_MAX_FLASH_BANKS 1 /** max number of memory banks */
+#define CONFIG_SYS_MAX_FLASH_SECT (256) /** max number of sectors on one chip */
+
+/* Width of CFI bus to start scanning */
+#define CONFIG_SYS_FLASH_CFI_WIDTH FLASH_CFI_16BIT
+/* Enable extra elements in CFI driver for storing flash geometry */
+#define CONFIG_SYS_FLASH_CFI 1
+/* Enable CFI flash
+ driver */
+#define CONFIG_FLASH_CFI_DRIVER 1
+#define CONFIG_SYS_FLASH_USE_BUFFER_WRITE
+
+/* We're not RAM_RESIDENT so CONFIG_ENV_IS_IN_FLASH will be set. */
+#if CONFIG_RAM_RESIDENT
+# define CONFIG_ENV_IS_NOWHERE 1
+#else
+# define CONFIG_ENV_IS_IN_FLASH 1
+#endif
+
+
+#define CONFIG_LIBATA /** Needed for SATA */
+#define CONFIG_CMD_SATA /** Enable the sata command */
+#define CONFIG_SYS_SATA_MAX_DEVICE 2 /** Support up to 2 devices */
+#define CONFIG_SATA_AHCI /** Enable AHCI driver */
+#define CONFIG_SATA_AHCI_PLAT /** Enable AHCI platform support */
+#define CONFIG_SATA_AHCI_OCTEON /** Enable Octeon AHCI driver */
+#define CONFIG_AHCI_SETFEATURES_XFER /** Enable setfeature xfer feature */
+
+/* Address and size of Primary Environment Sector */
+#define CONFIG_ENV_SIZE (128*1024) /** Size of environment */
+#define CONFIG_ENV_ADDR (CONFIG_SYS_FLASH_BASE + CONFIG_SYS_FLASH_SIZE -

```

```

CONFIG_ENV_SIZE)
+
+/*-----
+
+ * Cache Configuration (not used)
+ */
+#define CONFIG_SYS_DCACHE_SIZE (32 * 1024) /** L1 Data cache size */
+#define CONFIG_SYS_ICACHE_SIZE (78 * 1024) /** L1 icache size */
+
+#if CONFIG_OCTEON_PCI_HOST
+/** Right now only the Intel E1000 driver has been ported */
+# define CONFIG_E1000
+#endif
+
+/** The CN73XX has two TWSI buses and the EBB7304 board uses both of them */
+#define CONFIG_SYS_MAX_I2C_BUS 1
+
+#define CONFIG_RTC_DS1337 /** RTC clock chip */
+#define CONFIG_SYS_I2C_RTC_ADDR 0x68 /** RTC I2C address */
+#define CONFIG_SYS_RTC_BUS_NUM 0 /** RTC I2C bus */
+
+/** Enable watchdog support */
+#define CONFIG_HW_WATCHDOG
+
+/** Configure QLM support */
+#define CONFIG_OCTEON_QLM
+
+/* Include shared board configuration, consisting mainly of DRAM details. */
+#include "octeon_kilburn_shared.h"
+
+/** Disable DDR2 support */
+#define CONFIG_OCTEON_DISABLE_DDR2
+
+#endif /* __CONFIG_H__ */
diff -Nru OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn_shared.h
OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn_shared.h
--- OCTEON-SDK-old/bootloader/u-boot/include/configs/octeon_kilburn_shared.h 1969-12-31 16:00:00.000000000
-0800
+++ OCTEON-SDK/bootloader/u-boot/include/configs/octeon_kilburn_shared.h 2016-08-22 10:53:46.519732222 -
0700
@@ -0,0 +1,598 @@
+#ifndef __OCTEON_KILBURN_SHARED_H__
+#define __OCTEON_KILBURN_SHARED_H__
+
+
+
+/*
+ * Define this to use SPD data defined in header files instead of
+ * using TWSI to access the SPDs in the DIMMs. This is a workaround

```

```

+ * for boards that lack working TWSI interfaces.
+ */
+
+/* 1-DIMM, 4-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC2(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+#define
OCTEON_DRAM_SOCKET_CONFIG_1DIMM_4LMC3(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}}, \
+ {{0x0, 0x0}, {NULL, NULL}}
+
+/* 2-DIMM, 4-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC2(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_4LMC3(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+
+/* 1-DIMM, 2-LMC */
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC2(spd_pointer)
\
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_1DIMM_2LMC3(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+
+/* 2-DIMM, 2-LMC */

```



```

+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC0(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC1(spd_pointer) \
+ {{0x0, 0x0}, {spd_pointer, NULL}},\
+ {{0x0, 0x0}, {spd_pointer, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC2(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_DRAM_SOCKET_CONFIG_2DIMM_2LMC3(spd_pointer) \
+ {{0x0, 0x0}, {NULL, NULL}},\
+ {{0x0, 0x0}, {NULL, NULL}}
+
+#define MT18ASF1G72AZ_2G1AYESZG_VALUES \
+0x23,0x06,0x0c,0x02,0x84,0x19,0x00,0x00,0x00,0x00,0x00,0x03,0x09,0x0b,0x80,0x00,\
+0x00,0x00,0x08,0x0c,0xf4,0x1b,0x00,0x00,0x6c,0x6c,0x6c,0x11,0x08,0x74,0x20,0x08,\
+0x00,0x05,0x70,0x03,0x00,0xa8,0x1e,0x2b,0x2b,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0xec,0xb5,0xce,0x00,0x00,0x00,0x00,0xc2,0x98,0xf4,\
+0x11,0x11,0x04,0x01,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x2e
+
+#define
MT18ASF1G72AZ_2G1AXESZG_VALUES \
+0x23,0x10,0x0c,0x02,0x84,0x19,0x00,0x00,0x00,0x00,0x00,0x03,0x09,0x0b,0x80,0x00,\
+0x00,0x00,0x08,0x0c,0xf4,0x1b,0x00,0x00,0x6c,0x6c,0x6c,0x11,0x08,0x74,0x20,0x08,\
+0x00,0x05,0x70,0x03,0x00,0xa8,0x1e,0x2b,0x2b,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0xc,0x2c,0x15,0x35,\
+0x15,0x35,0x0b,0x2c,0x15,0x35,0x0b,0x2c,0x0b,0x2c,0x0b,0x35,0x15,0x36,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0xec,0xb5,0xce,0x00,0x00,0x00,0x00,0xc2,0x38,0x93,\
+0x11,0x11,0x04,0x01,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\
+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,\

```

```

+0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x00,0x0c,0x2e
+
+/*
MT18ASF1G72AZ_2G1AYESZG_VALUES ID: 101 DDR4 */
+/* MT18ASF1G72AZ_2G1AXESZG_VALUES ID: 102 DDR4 */
+
+/*-----*/
+/* Configuration 0: gpio 1 = OFF */
+/*-----*/
+/* Select SPD from the list. */
+#define OCTEON_KILBURN_CFG0_SPD_VALUES MT18ASF1G72AZ_2G1AXESZG_VALUES
+
+/* Select socket configuration.
+ * _1DIMM_4LMC _2DIMM_4LMC
+ * _1DIMM_2LMC _2DIMM_2LMC
+
+ */
+#define OCTEON_KILBURN_CFG0_DIMM_LMC_CONFIGURATION _1DIMM_2LMC
+/*-----*/
+
+/*-----*/
+/* Configuration 1: gpio 1 = ON */
+/*-----*/
+/* Select SPD from the list. */
+#define OCTEON_KILBURN_CFG1_SPD_VALUES MT18ASF1G72AZ_2G1AYESZG_VALUES
+
+/* Select socket configuration.
+ * _1DIMM_4LMC _2DIMM_4LMC
+ * _1DIMM_2LMC _2DIMM_2LMC
+ */
+#define OCTEON_KILBURN_CFG1_DIMM_LMC_CONFIGURATION _1DIMM_2LMC
+/*-----*/
+
+
+#define MERGE_AGAIN(X, Y) X ## Y
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION0 {{0x50, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION1 {{0x54, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION2 {{0x0, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+#define OCTEON_KILBURN_DRAM_SOCKET_CONFIGURATION3 {{0x0, 0x0}, {NULL,
NULL}},{{0x0, 0x0}, {NULL, NULL}}
+
+#define OCTEON_KILBURN_BOARD_EEPROM_TWSI_ADDR (0x50)
+
+
+

```

```

+/*
+ * Local copy of these parameters to allow for customization for this
+ * board design. The generic version resides in lib_octeon_shared.h.
+ */
+
+/* LMC0_MODEREG_PARAMS1 */
+#define OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = 0, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01
+ = 0, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = 0, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0, \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_2SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_HiZ, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = 0, \
+ .dic_01 = ddr4_dic_34ohm, \

```

```

+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = ddr4_rttwr_HiZ, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11
= 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0 \
+ } \
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_240ohm, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = 0, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = ddr4_rttwr_240ohm,
\
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = 0, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = 0, \
+ .pasr_11 = 0, \
+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = 0, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = 0, \
+ } \
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_2SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0,

```

```

\
+ .srt_00 = 0, \
+ .rtt_wr_00 = ddr4_rttwr_240ohm, \
+ .dic_00 = ddr4_dic_34ohm, \
+ .rtt_nom_00 = ddr4_rttnom_120ohm, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = ddr4_rttwr_240ohm, \
+ .dic_01 = ddr4_dic_34ohm, \
+ .rtt_nom_01 = ddr4_rttnom_120ohm, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = ddr4_rttwr_240ohm, \
+ .dic_10 = ddr4_dic_34ohm, \
+ .rtt_nom_10 = ddr4_rttnom_120ohm, \
+ .pasr_11 = 0, \
+ .asr_11
= 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = ddr4_rttwr_240ohm, \
+ .dic_11 = ddr4_dic_34ohm, \
+ .rtt_nom_11 = ddr4_rttnom_120ohm, \
+ } \
+ }
+
+#define OCTEON_KILBURN_MODEREG_PARAMS1_4RANK_1SLOT \
+ { .cn78xx = { .pasr_00 = 0, \
+ .asr_00 = 0, \
+ .srt_00 = 0, \
+ .rtt_wr_00 = rttwr_60ohm, \
+ .dic_00 = dic_34ohm, \
+ .rtt_nom_00 = rttnom_20ohm, \
+ .pasr_01 = 0, \
+ .asr_01 = 0, \
+ .srt_01 = 0, \
+ .rtt_wr_01 = rttwr_60ohm, \
+ .dic_01
= dic_34ohm, \
+ .rtt_nom_01 = rttnom_none, \
+ .pasr_10 = 0, \
+ .asr_10 = 0, \
+ .srt_10 = 0, \
+ .rtt_wr_10 = rttwr_60ohm, \
+ .dic_10 = dic_34ohm, \
+ .rtt_nom_10 = rttnom_20ohm, \
+ .pasr_11 = 0, \

```

```

+ .asr_11 = 0, \
+ .srt_11 = 0, \
+ .rtt_wr_11 = rttwr_60ohm, \
+ .dic_11 = dic_34ohm, \
+ .rtt_nom_11 = rttnom_none, \
+ }
+ }
+
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_1SLOT \
+ {
+ \
+ .cn78xx = {
+ \
+ .rtt_park_00 = ddr4_rttpark_60ohm, \
+ .vref_value_00
+ = 0x22, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = 0, \
+ .vref_value_01 = 0, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+ }
+
+ /* FIX */
+ #define OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_2SLOT \
+ {
+ \
+ .cn78xx = {
+ \
+ .rtt_park_00 = ddr4_rttpark_34ohm, \
+ .vref_value_00 = 0x1f, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = 0, \
+ .vref_value_01 = 0, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = ddr4_rttpark_34ohm, \
+ .vref_value_10 = 0x1f, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+ }
+
+ #define OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_1SLOT \

```

```

+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_120ohm, \
+ .vref_value_00 = 0x19, \
+ .vref_range_00 = 0, \
+ .rtt_park_01
 = ddr4_rttpark_120ohm, \
+ .vref_value_01 = 0x19, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_2SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_60ohm, \
+ .vref_value_00 = 0x19, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_60ohm, \
+ .vref_value_01 = 0x19, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = ddr4_rttpark_60ohm, \
+ .vref_value_10 = 0x19, \
+ .vref_range_10 = 0, \
+ .rtt_park_11 = ddr4_rttpark_60ohm, \
+ .vref_value_11 = 0x19, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+#define OCTEON_KILBURN_MODEREG_PARAMS2_4RANK_1SLOT \
+{ \
+ .cn78xx = { \
+ .rtt_park_00 = ddr4_rttpark_80ohm, \
+ .vref_value_00 = 0x1f, \
+ .vref_range_00 = 0, \
+ .rtt_park_01 = ddr4_rttpark_80ohm, \
+ .vref_value_01
 = 0x1f, \
+ .vref_range_01 = 0, \
+ .rtt_park_10 = 0, \
+ .vref_value_10 = 0, \

```

```

+ .vref_range_10 = 0, \
+ .rtt_park_11 = 0, \
+ .vref_value_11 = 0, \
+ .vref_range_11 = 0 \
+ } \
+}
+
+##define OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_1SLOT, ddr4_rodctl_48_ohm, 0x00000000ULL,
0 }, \
+ /* 2 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_1RANK_2SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_1RANK_2SLOT,
ddr4_rodctl_48_ohm, 0x00000000ULL, 0 }
+
+##define OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2 RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x00000000ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_1SLOT, ddr4_rodctl_80_ohm, 0x00000000ULL,
0 }, \
+ /* 2 */ { ddr4_dqx_driver_34_ohm, 0x0c0c0303ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_2RANK_2SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_2RANK_2SLOT, ddr4_rodctl_48_ohm, 0x04080102ULL,
0 }
+
+##define OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION \
+ /* DIMMS DQX_CTL WODT_MASK LMCX_MODEREG_PARAMS1
LMCX_MODEREG_PARAMS2
RODT_CTL RODT_MASK rsv'ed */\
+ /* =====
===== */\
+ /* 1 */ { ddr4_dqx_driver_34_ohm, 0x01030203ULL,
OCTEON_KILBURN_MODEREG_PARAMS1_4RANK_1SLOT,
OCTEON_KILBURN_MODEREG_PARAMS2_4RANK_1SLOT, ddr4_rodctl_48_ohm, 0x01010202ULL,
0 }
+
+/* Construct a static initializer for the ddr_configuration_t variable that holds
+** (almost) all of the information required for DDR initialization.

```



```

+*/
+
+/*
+ The parameters below make up the custom_lmc_config data structure.
+ This structure is used to customize the way that the LMC DRAM
+ Controller is configured for a particular board design.
+
+ Refer to the file lib_octeon_board_table_entry.h for a description
+ of the custom board settings. It is usually kept in the following
+ location... arch/mips/include/asm/arch-octeon/
+
+*/
+
+#define OCTEON_KILBURN_DDR_CONFIGURATION(SPD_CONFIGURATION)
+
+ \
+ /* Interface 0 */
+ {
+ \
+ .custom_lmc_config = {
+ \
+ .min_rtt_nom_idx = 1,
+ \
+ .max_rtt_nom_idx = 7,
+ \
+ .min_rodt_ctl = 1,
+ \
+ .max_rodt_ctl = 7,
+ \
+ .ck_ctl = ddr4_driver_34_ohm,
+ \
+ .cmd_ctl = ddr4_driver_34_ohm,
+ \
+ .ctl_ctl = ddr4_driver_34_ohm,
+ \
+ .min_cas_latency = 0,
+ \
+ .offset_en = 1,
+ \
+ .offset_udimm = 2,
+ \
+ .offset_rdimm =
+ 2,
+ \
+ .ddr_rtt_nom_auto = 0,
+ \
+ .ddr_rodt_ctl_auto = 0,
+ \
+ .rlevel_comp_offset_udimm = 0,
+ \
+ .rlevel_comp_offset_rdimm = 0,
+ \
+ .rlevel_compute = 0,
+ \
+ .ddr2t_udimm = 1,
+ \
+ .ddr2t_rdimm = 1,
+ \
+ .maximum_adjacent_rlevel_delay_increment = 2,
+ \
+ .fprch2 = 2,
+ \
+ .dll_write_offset = NULL,
+ \
+ .dll_read_offset = NULL,
+ \
+ .parity = 0},
+ \
+ .dimm_config_table = {
+ \
+ SPD_CONFIGURATION##0,
+ \
+ \
+ DIMM_CONFIG_TERMINATOR},
+ \
+ .unbuffered = {
+ \
+ .ddr_board_delay = 0,
+ \

```

```

+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION},
+ \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ }, \
+ /* Interface 1 */ \
+ { \
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdim = 2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdim = 0, \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdim = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0}, \
+ .dim = { \
+ SPD_CONFIGURATION##1, \
+ DIMM_CONFIG_TERMINATOR}, \
+ .unbuffered = { \

```

```

+ .ddr_board_delay = 0, \
+ .lmc_delay_clk = 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .registered = { \
+ .ddr_board_delay = 0, \
+ .lmc_delay_clk
= 0, \
+ .lmc_delay_cmd = 0, \
+ .lmc_delay_dq = 0}, \
+ .odt_1rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION}, \
+ .odt_2rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION}, \
+ .odt_4rank_config = { \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION} \
+ }, \
+ /* Interface 2 */ \
+ { \
+ .custom_lmc_config = { \
+ .min_rtt_nom_idx = 1, \
+ \
+ .max_rtt_nom_idx = 7, \
+ .min_rodt_ctl = 1, \
+ .max_rodt_ctl = 7, \
+ .ck_ctl = ddr4_driver_34_ohm, \
+ .cmd_ctl = ddr4_driver_34_ohm, \
+ .ctl_ctl = ddr4_driver_34_ohm, \
+ .min_cas_latency = 0, \
+ .offset_en = 1, \
+ .offset_udimm = 2, \
+ .offset_rdimm = 2, \
+ .ddr_rtt_nom_auto = 0, \
+ .ddr_rodt_ctl_auto = 0, \
+ .rlevel_comp_offset_udimm = 0, \
+ .rlevel_comp_offset_rdimm = 0, \
+ \
+ .rlevel_compute = 0, \
+ .ddr2t_udimm = 1, \
+ .ddr2t_rdimm = 1, \
+ .maximum_adjacent_rlevel_delay_increment = 2, \
+ .fprch2 = 2, \
+ .dll_write_offset = NULL, \
+ .dll_read_offset = NULL, \
+ .parity = 0}, \
+ .dimm_config_table = { \
+ SPD_CONFIGURATION##2, \
+ DIMM_CONFIG_TERMINATOR}, \

```

```

+ .unbuffered = {
+ .ddr_board_delay = 0,
+ .lmc_delay_clk = 0,
+ \
+ .lmc_delay_cmd = 0,
+ .lmc_delay_dq = 0},
+ .registered = {
+ .ddr_board_delay = 0,
+ .lmc_delay_clk = 0,
+ .lmc_delay_cmd = 0,
+ .lmc_delay_dq = 0},
+ .odt_1rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION},
+ .odt_2rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION},
+ .odt_4rank_config = {
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION}
+ },

+ /* Interface 3 */
+ {
+ .custom_lmc_config = {
+ .min_rtt_nom_idx = 1,
+ .max_rtt_nom_idx = 7,
+ .min_rodt_ctl = 1,
+ .max_rodt_ctl = 7,
+ .ck_ctl = ddr4_driver_34_ohm,
+ .cmd_ctl = ddr4_driver_34_ohm,
+ .ctl_ctl = ddr4_driver_34_ohm,
+ .min_cas_latency = 0,
+ .offset_en = 1,
+ .offset_udimm = 2,
+ .offset_rdimm =
+ 2,
+ .ddr_rtt_nom_auto = 0,
+ .ddr_rodt_ctl_auto = 0,
+ .rlevel_comp_offset_udimm = 0,
+ .rlevel_comp_offset_rdimm = 0,
+ .rlevel_compute = 0,
+ .ddr2t_udimm = 1,
+ .ddr2t_rdimm = 1,
+ .maximum_adjacent_rlevel_delay_increment = 2,
+ .fprch2 = 2,
+ .dll_write_offset = NULL,
+ .dll_read_offset = NULL,
+ .parity = 0},
+ .dimm_config_table = {
+ SPD_CONFIGURATION##3,

```

```

+ \
+ DIMM_CONFIG_TERMINATOR},
+ \
+ .unbuffered = {
+ \
+ .ddr_board_delay = 0,
+ \
+ .lmc_delay_clk = 0,
+ \
+ .lmc_delay_cmd = 0,
+ \
+ .lmc_delay_dq = 0},
+ \
+ .registered = {
+ \
+ .ddr_board_delay = 0,
+ \
+ .lmc_delay_clk = 0,
+ \
+ .lmc_delay_cmd = 0,
+ \
+ .lmc_delay_dq = 0},
+ \
+ .odt_1rank_config = {
+ \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_1RANK_CONFIGURATION},
+ \
+ .odt_2rank_config = {
+ \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_2RANK_CONFIGURATION},
+ \
+ .odt_4rank_config = {
+ \
+ OCTEON_KILBURN_CN78XX_DRAM_ODT_4RANK_CONFIGURATION}
+ }
+
+
+ #endif /* __OCTEON_KILBURN_SHARED_H__ */
diff -Nru OCTEON-SDK-old/executive/cvmx-app-init.h OCTEON-SDK/executive/cvmx-app-init.h
--- OCTEON-SDK-old/executive/cvmx-app-init.h 2016-08-22 11:04:03.746732100 -0700
+++ OCTEON-SDK/executive/cvmx-app-init.h 2016-08-22 10:54:58.536678796 -0700
@@ -363,6 +363,7 @@
 ** use any numbers in this range. */
 CVMX_BOARD_TYPE_CUST_PRIVATE_MIN = 20001,
 CVMX_BOARD_TYPE_UBNT_E100 = 20002,
+ CVMX_BOARD_TYPE_CUST_KILBURN = 20003,
 CVMX_BOARD_TYPE_CUST_PRIVATE_MAX = 30000,

 /* Range for IO modules */
 @@ -505,6 +506,7 @@
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_PRIVATE_MIN)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_UBNT_E100)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_CLARK)
+ ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_KILBURN)
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_CUST_PRIVATE_MAX)
 /* Module range */
 ENUM_BRD_TYPE_CASE(CVMX_BOARD_TYPE_MODULE_MIN)

```

Found in path(s):

```

* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/patch6.patch
No license file was found, but licenses were detected in source scan.

```

# Copyright (c) 2016 by Cisco Systems, Inc.

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OCTEON-SDK/bootloader/u-boot/tools/Makefile | 2 +-  
1 files changed, 1 insertions(+), 1 deletions(-)

diff --git a/OCTEON-SDK/bootloader/u-boot/tools/Makefile b/OCTEON-SDK/bootloader/u-boot/tools/Makefile  
index e0ab6ca..e5dc669 100644

--- a/OCTEON-SDK/bootloader/u-boot/tools/Makefile  
+++ b/OCTEON-SDK/bootloader/u-boot/tools/Makefile  
@@ -249,11 +249,11 @@ \$(obj)kwboot\$(SFX): \$(obj)kwboot.o

\$(obj)update\_octeon\_header\$(SFX): update\_octeon\_header.c \$(obj)crc32.o  
\$(HOSTCC) \$(HOSTCFLAGS\_NOPED) -D\_GNU\_SOURCE \$(HOSTLDFLAGS) -o \$@ \$^

\$(obj)dte\$(SFX):  
- CC=\$(HOSTCC) \$(MAKE) -C dtcsrc  
+ CC=\$(HOSTCC) LDFLAGS=\$(HOSTLDFLAGS) \$(MAKE) -C dtcsrc  
mv dtcsrc/dte\$(SFX) \$@

# Some of the tool objects need to be accessed from outside the tools directory  
\$(obj)%.o: \$(SRCTREE)/common/%.c  
\$(HOSTCC) -g \$(HOSTCFLAGS\_NOPED) -c -o \$@ \$<

Found in path(s):

\* /opt/cola/permits/1305654281\_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-  
OCTEON-SDK-bootloader-u-boot-tools-Makefile.patch

No license file was found, but licenses were detected in source scan.

# Copyright (c) 2016 by Cavium, Inc.  
# All rights reserved.

diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h pci-tap/CNNIC-SDK-SRC/modules/pci-  
tap/host-tap-defs.h

--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h 2016-02-24 13:19:45.000000000 +0530  
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/host-tap-defs.h 2016-06-14 19:57:33.000000000 +0530  
@@ -2,7 +2,7 @@

\*

\* CNNIC SDK

\*

- \* Copyright (c) 2015 Cavium Networks. All rights reserved.

+ \* Copyright (c) 2016 Cavium Networks. All rights reserved.

\*

\* This file, which is part of the CNNIC SDK which also includes the

\* CNNIC SDK Package from Cavium Networks, contains proprietary and

@@ -28,8 +28,6 @@

/\* defines \*/

#define cavium\_read\_mem(buf, idx, len, dev) user\_drv\_pci\_read\_mem(buf, idx, len, dev)

#define cavium\_write\_mem(idx, buf, len, dev) user\_drv\_pci\_write\_mem(idx, buf, len, dev)

##define READ\_IDX\_PTR

```

 read_idx_addr
#define WRITE_IDX_PTR write_idx_addr

#define CNXX_GPIO_TX_SET (0x0001070000000888ull)
#define CNXX_GPIO_TX_CLR (0x0001070000000890ull)
@@ -43,10 +41,11 @@
#define RX_BUF_SIZE 2048

/* globals */
-uint64_t write_idx_addr, read_idx_addr;
FILE *cutils_stdout = NULL;
void *oct_pci_dev = NULL;
int tun_fd = 0;
+fd_set readfds;
+struct timeval tv;

/* Functions */
void * user_drv_pci_open(int device);
@@ -73,10 +72,9 @@ uint64_t user_drv_pci_get_scratchreg(voi
* The function waits indefinitely for OCTEON to respond otherwise.
*/

-static void host_oct_handshake(void *user_dev)
+static void host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
 uint64_t scratch_reg_addr, scratch_val = 0;
-
 scratch_reg_addr = user_drv_pci_get_scratchreg(user_dev);

 /* Wait for OCTEON to to send indication of
handshake start */
@@ -96,12 +94,11 @@ static void host_oct_handshake(void *us
 while(scratch_val == HOSTFW_HS_ACK)
 scratch_val = user_drv_pci_read_csr(scratch_reg_addr, user_dev);

- write_idx_addr = scratch_val;
-
- cavium_print_dbg("Received phys addr of NB as %lx\n", write_idx_addr);
-
- /* Set Read address */
- read_idx_addr = write_idx_addr + RX_BUF_SIZE;
+ /*Host read follows host write*/
+ write_buf->base = scratch_val;
+ read_buf->base = write_buf->base + sizeof(nw_buf_t);
+
+ cavium_print_dbg("write: %lx read: %lx\n", write_buf->base, read_buf->base);
 scratch_val = HOSTFW_HS_NB_DONE;
 user_drv_pci_write_csr(scratch_reg_addr, scratch_val, user_dev);

```

```

@@ -109,6 +106,27 @@ static void host_oct_handshake(void *us
 return;
}

+static int host_wait_on_fds()
+{
+ int nfds = 0;
+ int ret = -1;
+
+ nfds = tun_fd;
+ tv.tv_sec = 0;
+ tv.tv_usec = 0;
+ FD_ZERO(&readfds);
+ FD_SET(tun_fd, &readfds);
+
+ ret = select(nfds + 1, &readfds, NULL, NULL, &tv);
+
+ if (ret == -1)
+ {
+ cavium_print_msg("Select failed with error %d\n", errno);
+ return 0;
+ }
+ return 1;
+
+}
+
+/*
+ * Interrupt Octeon after data has been written to BAR1 memory.
+ * Octeon upon receiving this interrupt would wake up the process
@@ -137,7 +155,7 @@ static inline int host_verify_data_valid
 cavium_read_mem((uint8_t*)read_buf, addr, 64, oct_pci_dev);

 if (memcmp((void *)read_buf, (void *)buffer, 64))
- return FAILED;
+ return FAILED;

 return SUCCESS;
}
@@ -161,34 +179,31 @@ static inline int host_oct_init()
}

/*
- * Forever data loop for host to read/write data from/to BAR1/TAP.
+ * Forever data loop for host.
+ * Read from Host TAP and write to BAR1.
+ * Read from BAR1 and write to Host TAP.
+ */

```



```

* @param read and write buffers.
*/
static void host_oct_data_loop(struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
- int nread
= 0;
while (1)
{
- /* Tx: Read data from tap and write to OCTEON memory */
-
- nread = read(tun_fd, write_buf->data, NW_BUF_SIZE);
-
- /* Proceed only when data is available from tap interface*/
- if(nread > 0)
+
+ if(host_wait_on_fds())
{
- host_oct_network_to_pci(write_buf, nread);
-
- /* Inform Octeon */
- host_interrupt_octeon(oct_pci_dev);
+ if (FD_ISSET(tun_fd, &readfds))
+ {
+ if(host_oct_network_to_pci(write_buf, tun_fd)){
+ /* Inform Octeon */
+ host_interrupt_octeon(oct_pci_dev);
+ }
+ }
}

/* Rx: Read data from OCTEON memory and write to tap */
-
- host_oct_pci_to_network(read_buf);

- /* Yield the CPU */
- nsleep(5);
}

user_drv_pci_close(oct_pci_dev);
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/gpio_tap.c pci-tap/CNNIC-SDK-
SRC/modules/pci-tap/kernel/drv/gpio_tap.c

pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/gpio_tap.c 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/gpio_tap.c 2016-06-14 19:57:33.000000000 +0530
@@ -1,3 +1,26 @@
+/*
+ *
+ * CNNIC SDK

```

```

+ *
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+ *
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+ *
+ *
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+ *
+ * Contact Cavium Networks at info@caviumnetworks.com for more information.
+ *
+ */
#include <linux/module.h>
#include <linux/wait.h>
#include <linux/rculist.h>
@@ -68,11 +91,19 @@ static unsigned int gpio_dev_poll(struct
static irqreturn_t octeon_gpio_handler(int irq, void * data)
{
 unsigned long flags;
- cvmx_gpio_tx_clr_t tx_clr;
-
+
+ #ifdef CONFIGURE_GPIO_OUTPUT
+ cvmx_gpio_tx_clr_t tx_clr;
+ tx_clr.u64 = cvmx_read_csr(CVMX_GPIO_TX_CLR);
+ tx_clr.s.clr |= GPIO_TX_CLEAR_MASK;
+ cvmx_write_csr(CVMX_GPIO_TX_CLR, tx_clr.u64);
+ #else
+ cvmx_gpio_intrx_t intrx;
+ /*Clear any previous or Stale interrupt*/
+ intrx.u64 = cvmx_read_csr(CVMX_GPIO_INTRX(GPIO_CIU3_BASE_IRQ));
+ intrx.cn73xx.intr = 0x1; /*Clear*/
+ cvmx_write_csr(CVMX_GPIO_INTRX(GPIO_CIU3_BASE_IRQ), intrx.u64);
+ #endif

 spin_lock_irqsave(&gpio_lock, flags);
 wakeup_fd = 1;
@@ -91,9 +122,34 @@ int octeon_gpio_ciu3_init(void)

```

```

 irq_handler_t isr;
 unsigned long irqflags;
 int error;
- cvmx_gpio_bit_cfgx_t gpio_cfg;
+ #ifdef CONFIGURE_GPIO_OUTPUT
+ cvmx_gpio_bit_cfgx_t cfg;
+ #endif
 const char *desc = "gpio_tap";

+ #ifdef DISABLE_INITIAL_SPURIOUS_INTR /* Not tested*/
+
+ /*Code to remove initial Spurious interrupt*/
+ cvmx_gpio_bit_cfgx_t gpio_cfg;
+ cvmx_gpio_intrx_t intrx;
+
+ /*Errata CN73XX GPIO_BIT_CFGX[int_en]=1->0 does not make any affect*/
+ if(OCTEON_IS_MODEL(OCTEON_CN73XX_PASS1_X)){
+ cvmx_write_csr(CVMX_CIU3_ISCX_W1C(0x3000 + GPIO_CIU3_BASE_IRQ), 1);
+ cvmx_write_csr(CVMX_CIU3_ISCX_W1C(0x3008), 1);
+ }
+
+ /*Disable interrupt. Let user process enables it*/
+ gpio_cfg.u64 = cvmx_read_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
+ gpio_cfg.s.int_en = 0;
+ cvmx_write_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ), gpio_cfg.u64);
+
+ /*Clear any previous or Stale interrupt*/
+ intrx.u64=cvmx_read_csr(CVMX_GPIO_INTRX(GPIO_CIU3_BASE_IRQ));
+
+ intrx.cn73xx.intr = 0x1; /*Clear*/
+ cvmx_write_csr(CVMX_GPIO_INTRX(GPIO_CIU3_BASE_IRQ), intrx.u64);
+ #endif
+
 error = register_chrdev(GPIO_TAP_DEV_MAJOR, DRIVER_NAME, &octeon_gpio_fops);
 if(error < 0) {
 panic("OCTEON: Device Registration failed, error:%d\n", error);
@@ -124,11 +180,12 @@ int octeon_gpio_ciu3_init(void)
 irq, error);
 goto fail;
}
-
- gpio_cfg.u64 = cvmx_read_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
- gpio_cfg.s.tx_oe = 1;
- gpio_cfg.s.int_type = 1;
- cvmx_write_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ), gpio_cfg.u64);
+ #ifdef CONFIGURE_GPIO_OUTPUT
+ cfg.u64 = cvmx_read_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
+ cfg.s.tx_oe = 1;

```

```

+ cfg.s.int_type = 1;
+ cvmx_write_csr(CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ), cfg.u64);
+#endif
 init_waitqueue_head(&gpio_dev_waitqueue);

 return 0;
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/Makefile pci-tap/CNNIC-SDK-
SRC/modules/pci-tap/kernel/drv/Makefile

pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/Makefile 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/kernel/drv/Makefile 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
#
CNNIC SDK
#
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#
This file, which is part of the CNNIC SDK which also includes the
CNNIC SDK Package from Cavium Networks, contains proprietary and
@@ -32,7 +32,7 @@
#

#CFLAGS_LOCAL = -O2 -W -Wall -Wno-unused-parameter -fno-strict-aliasing
-CFLAGS_LOCAL = -O2 -DGPIO_CIU3_BASE_IRQ=1
+CFLAGS_LOCAL = -O2 -DGPIO_CIU3_BASE_IRQ=1 -DCONFIGURE_GPIO_OUTPUT

EXTRA_CFLAGS = $(CFLAGS_LOCAL)

diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/Makefile pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/Makefile
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/Makefile 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/Makefile 2016-06-14
19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
#
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#
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@@ -35,7 +35,7 @@ CFLAGS_LOCAL = -O2 -W -Wall -Werror -Wno

EXTRA_CFLAGS = $(CFLAGS_LOCAL) -DGPIO_CIU3_BASE_IRQ=1
-

```

```

+export NUMBER_OF_BAR_DESC=1
OCT_PCI_TAP=pci-tap
HOST_TAP_BIN=host-pci-tap

@@ -43,7 +43,7 @@ all: hostapp octapp

hostapp: CC=gcc
hostapp: AR=ar
-hostapp: EXTRA_CFLAGS += -Wno-error=declaration-after-statement
+hostapp: EXTRA_CFLAGS += -Wno-error=declaration-after-statement -
DN_DESC=$(NUMBER_OF_BAR_DESC)
hostapp: EXTRA_LDFLAGS = -L $(CNNIC_ROOT)/modules/pci-tap/ -lpthread -lnutils
hostapp: $(OCT_PCI_TAP)

@@ -64,9 +64,9 @@ octapp : check_octeon_sdk build_oct
OCTEON_TARGET = linux_64
TARGET
 = oct-pci-tap

-build_oct: EXTRA_CFLAGS += -DOCTEON_LE -DENABLE_OCTEON=0 -DENABLE_OCTEON_PLUS=0
+build_oct: EXTRA_CFLAGS += -DOCTEON_LE -DENABLE_OCTEON=0 -DENABLE_OCTEON_PLUS=0 -
DN_DESC=$(NUMBER_OF_BAR_DESC)
build_oct: BINDIR := $(CNNIC_ROOT)/modules/driver/bin
-build_oct: COMPILE += -Wa,-a=$@.list -DOCT_TAP $(EXTRA_CFLAGS)
+build_oct: COMPILE += -Wa,-a=$@.list -DOCT_TAP -DCONFIGURE_GPIO_OUTPUT $(EXTRA_CFLAGS)
build_oct: $(TARGET)
build_fs: build_oct
 cp $(TARGET) $(TARGET).strip;
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/oct-tap-defs.h
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/oct-tap-defs.h 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
*
* CNNIC SDK
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*
* This file, which is
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@@ -34,10 +34,8 @@
/* defines */
#define cast64(val) ((unsigned long long)val)
#define CVM_DRV_GET_PHYS(ptr) (cast64(cvmx_ptr_to_phys(ptr)))
-#define cavium_read_mem(dest, src, len,dev) memcpy(dest, src, len)
+#define cavium_read_mem(dest, src, len,dev) memcpy(dest,src, len)

```

```

#define cavium_write_mem(dest, src, len, dev) memcpy(dest, src, len)
-#define READ_IDX_PTR read_idx_ptr
-#define WRITE_IDX_PTR write_idx_ptr

#define HOSTFW_HS_IND 0x1501
#define HOSTFW_HS_ACK 0x1502
@@ -61,7 +59,7 @@
#define max(a, b) ((a) > (b) ? (a) : (b))

/* globals */
-uint8_t *write_idx_ptr = NULL, *read_idx_ptr= NULL;
+
int gpio_fd = -1;
int tun_fd = -1;
fd_set readfds;
@@ -81,33 +79,25 @@ void *oct_pci_dev
= NULL;
* return Setup Read and write addresses for Host and return.
* The function waits indefinitely for HOST to respond otherwise.
*/
-static void host_oct_handshake(void *user_dev)
+static void
+host_oct_handshake(void *user_dev, struct nw_buffer *read_buf, struct nw_buffer *write_buf)
{
 uint64_t read_idx_addr, scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1, scratch_val;
 const cvmx_bootmem_named_block_desc_t *desc;
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(),
CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
+ scratch_val |= 0x1; /* Enable Output bit */
+
+ cvmx_write_csr_node(cvmx_get_node_num(), CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ),
scratch_val);
+
+ scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), CVMX_GPIO_TX_SET);
+ if (scratch_val & 0x1) {
+ cavium_print_msg("TX_SET configured for GPIO 0.\n");
+ } else {
+ scratch_val |= 0x1;
+ cvmx_write_csr_node(cvmx_get_node_num(),
CVMX_GPIO_TX_SET, scratch_val);
+ }
-
- /* Setup interrupt for GPIO 0: Assumes kernel module is loaded. */
- scratch_val = cvmx_read_csr_node(cvmx_get_node_num(),
CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ));
-
- scratch_val |= 0x1; /* Enable Output bit */

```

```

-
- cvmx_write_csr_node(cvmx_get_node_num(), CVMX_GPIO_BIT_CFGX(GPIO_CIU3_BASE_IRQ),
scratch_val);
-
- scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), CVMX_GPIO_TX_SET);
-
- if (scratch_val & 0x1)
- {
- cavium_print_msg("TX_SET configured for GPIO 0.\n");
- }
- else
- {
- scratch_val |= 0x1;
- cvmx_write_csr_node(cvmx_get_node_num(), CVMX_GPIO_TX_SET, scratch_val);
- }
-
-
- /* Handshake */
+ /* Handshake */
scratch_val = HOSTFW_HS_IND;
cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);

@@ -117,28 +107,31 @@ static void host_oct_handshake(void *use

/* Fetch the named block allocated for pci-tap buffers*/
desc = cvmx_bootmem_find_named_block(BLOCK_NAME);
-
+
if(!desc)
{
/* Allocate named block for write index */
- read_idx_ptr = cvmx_bootmem_alloc_named_range(BLOCK_SIZE, NB_MIN_ADDR, NB_MAX_ADDR,
NB_4KB_ALIGNED, BLOCK_NAME);
+ read_buf->base = cvmx_bootmem_alloc_named_range(N_DESC*BLOCK_SIZE,
+ NB_MIN_ADDR, NB_MAX_ADDR, NB_4KB_ALIGNED, BLOCK_NAME);
}
else
- read_idx_ptr = cvmx_phys_to_ptr(desc->base_addr);
-
- if(read_idx_ptr == (void *)-1)
+ read_buf->base = cvmx_phys_to_ptr(desc->base_addr);
+ if(read_buf->base == (void *)-1)
{
cavium_print_msg("Failed to allocate named block\n");
exit(1);
}

- cavium_print_dbg("read_idx_ptr: %p\n",read_idx_ptr);

```

```

+ cavium_print_dbg("read_idx_ptr: %p\n",read_buf->base);
+
+ /*memset complete block including w/r all desc*/
+ memset(read_buf->base, 0, N_DESC*BLOCK_SIZE);

- memset(read_idx_ptr, 0, BLOCK_SIZE);
- write_idx_ptr = (uint8_t*)((uint8_t *)read_idx_ptr + RX_BUF_SIZE);
+ /*OCTEON write
follows OCTEON read*/
+ write_buf->base = read_buf->base + sizeof(nw_buf_t);

- read_idx_addr = CVM_DRV_GET_PHYS(read_idx_ptr);
- cavium_print_dbg("write_idx_ptr:%p\n", write_idx_ptr);
+ read_idx_addr = CVM_DRV_GET_PHYS(read_buf->base);
+ cavium_print_dbg("write_idx_ptr:%p\n", write_buf->base);

/* Write the phys address of named block to scratch reg */
scratch_val = read_idx_addr;
@@ -152,7 +145,6 @@ static void host_oct_handshake(void *use
{
 scratch_val = cvmx_read_csr_node(cvmx_get_node_num(), scratch_reg_addr);
}
-
cavium_print_msg("Host-Octeon Handshake Completed\n");
cavium_print_msg("OCT-PCI-TAP running as daemon\n");
}
@@ -209,30 +201,27 @@ static inline int host_oct_init()
}

/*
- * Forever data loop for Octeon to read/write data from/to BAR1/TAP.
+ * Forever data loop for OCTEON.
+ * Read from OCTEON TAP and write to OCTEON DRAM.
+ * Read from OCTEON DRAM and write to OCTEON TAP.
+ *
+ * @param read and write buffers.
+ */
static void host_oct_data_loop(nw_buf_t *read_buf, nw_buf_t *write_buf)
{
- int nread = 0;
while (1)
{
 oct_wait_on_fds();

 /* Data available from Host */
 if (FD_ISSET(gpio_fd, &readfds))
- {
+ {

```



```

 host_oct_pci_to_network(read_buf);
 }
-
+
 if (FD_ISSET(tun_fd, &readfds))
 {
- nread = read(tun_fd, write_buf->data, NW_BUF_SIZE);
- if(nread > 0)
- {
- host_oct_network_to_pci(write_buf, nread);
- }
+ host_oct_network_to_pci(write_buf, tun_fd);
 }
}
}
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/pci-tap.c
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.c 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
*
* CNNIC SDK
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*
* This file, which is part of the CNNIC SDK which also includes the
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@@ -33,7 +33,6 @@
#include <stdint.h>
#include <time.h>
#include <unistd.h>
-
#include "pci-tap.h"

/*
@@ -81,41 +80,81 @@ int host_oct_tun_alloc(char *dev, int fl
 return fd;
}

+static inline int buf_isempty(nw_buf_t *pbuf,void *dev)
+{
+ int ret =0;
+ cavium_read_mem(pbuf ,pbuf->base, 2*sizeof(uint64_t), oct_pci_dev);
+
+ if(pbuf->ridx == pbuf->widx){
+ ret =1;

```

```

+ } else {
+ ret=0;
+ if(N_DESC == pbuf->ridx){
+ pbuf->ridx =0;
+ pbuf->flag =1;
+ }
+ }
+ return(ret);
+}
+
+static inline int buf_isfull(nw_buf_t *pbuf,void *dev)
+{
+ int ret=0;
+ cavium_read_mem(pbuf ,pbuf->base, 2*sizeof(uint64_t), oct_pci_dev);
+ if(pbuf->ridx == PREDEC_IDX(pbuf->widx)){
+
+ ret =1;
+ } else {
+ ret =0;
+ if(N_DESC == pbuf->widx){
+ pbuf->widx=0;
+ pbuf->flag=1;
+ }
+ }
+ return(ret);
+}
+
+/*
+ * Read data from BAR1 memory and write it to the tap device.
+ *
+ * @param network buffer.
+ *
+ */
-void host_oct_pci_to_network(void *buf)
+int host_oct_pci_to_network(void *pbuf)
+{
- nw_buf_t *read_buf = (nw_buf_t *)buf;
-
- /* Rx: Read data from OCTEON memory and write to tap */
- cavium_read_mem((uint8_t*)read_buf, READ_IDX_PTR, 2*sizeof(uint64_t), oct_pci_dev);
-
- /* Wait until we have read access and a valid buffer to read */
- if(!read_buf->rw_idx) && (read_buf->len > 0)
- {
- cavium_print_dbg("Read %ld bytes from named block\n",read_buf->len);
- cavium_read_mem(read_buf, READ_IDX_PTR, NW_BUF_SIZE, oct_pci_dev);
- read_buf->rw_idx = 1;
-
- /* Update the index to allow peer to perform next write */

```

```

- cavium_write_mem(READ_IDX_PTR,(uint8_t*)read_buf,
sizeof(uint64_t), oct_pci_dev);
-
- /* Send the buffer to the linux stack */
- write(tun_fd, read_buf->data, read_buf->len);
- cavium_print_dbg("Wrote %ld bytes to /dev/%s\n", read_buf->len, TAP_IF_NAME);
- }
+ nw_buf_t *preadbuf = (nw_buf_t *)pbuf;
+ desc_t local_desc;
+ int actual_read = 0, skipidx;
+
+ if(!buf_isempty(preadbuf, oct_pci_dev)){
+
+ /*Go to the ridx data offset and read the data*/
+ memset(&local_desc, 0 , sizeof(desc_t));
+
+ /*Calculate of amount of descriptor index to be skipped if ndescs > 1*/
+ skipidx = (preadbuf->ridx)?(preadbuf->ridx -1):0;
+
+ /*Read complete descriptor data into local variable*/
+ cavium_read_mem(&local_desc, DESC_BASE_OFFF(preadbuf) +
+ skipidx * sizeof(desc_t), sizeof(desc_t), oct_pci_dev);
+
+ actual_read = (local_desc.len > MAX_ETH_FRAM_SZ) ? MAX_ETH_FRAM_SZ : local_desc.len;
+ cavium_print_dbg("Read
%d bytes from named block\n",actual_read);
+ if(actual_read){
+ /* Send the buffer to the linux stack */
+ if(write(tun_fd, local_desc.data, actual_read)<0){
+ cavium_print_msg("Tunfd write error [%d, %s]\n", errno, strerror(errno));
+ } else {
+ cavium_print_dbg("Wrote %ld bytes to /dev/%s\n", actual_read, TAP_IF_NAME);
+ }
+ BUF_INC_IDX(preadbuf, ridx);
+ cavium_write_mem(preadbuf->base + OFFSETOF(nw_buf_t, ridx), &(preadbuf->ridx), sizeof(uint64_t),
oct_pci_dev);
+ }
#ifdef OCT_TAP
- else
- {
- cavium_print_msg("Invalid interrupt from Host\n");
+ else {
+ cavium_print_msg("Invalid interrupt from host with Packet len: 0\n");
+ }
+ }
+ #endif
- #endif /* OCT_TAP */
-

```

```

- return;
+ return actual_read;
}

/*
@@ -124,35 +163,33 @@ void host_oct_pci_to_network(void *buf)
* @param network buffer.
*
*/
-void
host_oct_network_to_pci(void * buf, int nread)
-{
- nw_buf_t *write_buf = (nw_buf_t *)buf;
-
- /* Octeon will read the data after clearing the interrupt, wait for write access. */
- while(!write_buf->rw_idx)
- {
- cavium_read_mem(write_buf, WRITE_IDX_PTR, sizeof(uint64_t), oct_pci_dev);
- }
-
- write_buf->len = nread;
-
- /* Reset the rw_idx to allow peer to perform next write */
- write_buf->rw_idx = 0;
-
- /* Writing data before index ensures data integrity. */
- cavium_write_mem(WRITE_IDX_PTR + 2*sizeof(uint64_t), (uint8_t*)write_buf + 2*sizeof(uint64_t), nread,
oct_pci_dev);
- cavium_write_mem(WRITE_IDX_PTR, (uint8_t*)write_buf, 2*sizeof(uint64_t), oct_pci_dev);
-
-#ifndef HOST_TAP
- /* Read the data back */
- if (host_verify_data_valid(WRITE_IDX_PTR + 2*sizeof(uint64_t),
- (uint8_t*)write_buf + 2*sizeof(uint64_t)) == FAILED)
- {
- cavium_print_msg("Data integrity check failed\n");
+int host_oct_network_to_pci(void
* buf, int tun_fd)
+{
+ int nread = 0, skipidx;
+ nw_buf_t *pwbuff = (nw_buf_t *)buf;
+
+ if(!buf_isfull(pwbuff, oct_pci_dev)){
+
+ /*Calculate of amount of descriptor index to be skipped if ndescs > 1*/
+ skipidx = (pwbuff->widx)?(pwbuff->widx -1):0;
+ if((nread = read(tun_fd, pwbuff->desc[pwbuff->widx].data, MAX_ETH_FRAM_SZ))<0){
+ cavium_print_msg("TUNFD read error [%d, %s]\n", errno, strerror(errno));

```

```

+ return -1;
+ }
+ /*Udate Length field and update the write index*/
+ if(nread){
+ cavium_write_mem((DESC_BASE_OFF(pwbuf) + (skipidx * sizeof(desc_t))
+ + OFFSETOF(desc_t, len)), &nread, sizeof(uint64_t), oct_pci_dev);
+ cavium_write_mem(DESC_BASE_OFF(pwbuf) + (skipidx * sizeof(desc_t)) +
+ OFFSETOF(desc_t, data), &(pwbuf->desc[pwbuf->widx].data),
+ nread, oct_pci_dev);
+
+ BUF_INC_IDX(pwbuf, widx);
+
+ cavium_write_mem(pwbuf->base + OFFSETOF(nw_buf_t, widx),
+ &(pwbuf->widx), sizeof(uint64_t), oct_pci_dev);
+ }
+ }
-#endif /* HOST_TAP */
-
- return;
+ return nread;
+ }

/*
@@ -195,14 +232,19 @@ int main(int argc, char *argv[])
 memset(read_buf, 0x0, NW_BUF_SIZE);
 memset(write_buf, 0x0, NW_BUF_SIZE);

- host_oct_handshake(oct_pci_dev);
-
- /* Set indices to allow both sides to begin transfer */
- read_buf->rw_idx = write_buf->rw_idx = 1;
-
- cavium_write_mem(WRITE_IDX_PTR,(uint8_t*)write_buf, sizeof(uint64_t), oct_pci_dev);
- cavium_write_mem(READ_IDX_PTR,(uint8_t*)read_buf, sizeof(uint64_t), oct_pci_dev);
+ host_oct_handshake(oct_pci_dev,read_buf,write_buf);

+ /*Double check: Reset indexes after handshake*/
+ read_buf->ridx = 0;
+ read_buf->widx = 0;
+ write_buf->ridx = 0;
+ write_buf->widx = 0;
+
+ /*Write widx/ridx values to nw_buf_t on PCI BAR*/
+ cavium_write_mem(write_buf->base,
+ &(write_buf->widx),
+ 2*sizeof(uint64_t), oct_pci_dev);
+ cavium_write_mem(read_buf->base, &(read_buf->widx),
+ 2*sizeof(uint64_t), oct_pci_dev);

```

```

daemon(0,0);

host_oct_data_loop(read_buf, write_buf);
diff -purN pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.h pci-tap/CNNIC-SDK-SRC/modules/pci-
tap/pci-tap.h
--- pci-tap-335/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.h 2016-02-24 13:19:45.000000000 +0530
+++ pci-tap/CNNIC-SDK-SRC/modules/pci-tap/pci-tap.h 2016-06-14 19:57:33.000000000 +0530
@@ -2,7 +2,7 @@
*
* CNNIC SDK
*
- * Copyright (c) 2015 Cavium Networks. All rights reserved.
+ * Copyright (c) 2016 Cavium Networks. All rights reserved.
*
* This file, which is part of the CNNIC SDK which also includes the
* CNNIC SDK Package from Cavium Networks, contains proprietary and
@@ -25,7 +25,10 @@
#ifndef _PCI_TAP_H
#define _PCI_TAP_H
#include <syslog.h>
#include <stdio.h>
#include <errno.h>

extern int errno;
#ifndef
TAP_IF_NAME
#define TAP_IF_NAME "tap0"
#endif /* TAP_IF_NAME */
@@ -34,8 +37,7 @@
#define TUN_DEV_FILE "/dev/net/tun"
#endif /* TUN_DEV_FILE */

#define MAX_ETH_FRAM_SZ 1518
-
+ #define MAX_ETH_FRAM_SZ 1536
#define cavium_print_msg(...) syslog(LOG_INFO,__VA_ARGS__);
#ifdef PRINT_DBG
#define cavium_print_dbg(...) syslog(LOG_DEBUG,__VA_ARGS__);
@@ -46,26 +48,52 @@
#define cavium_openlog()
#define SUCCESS 0
#define FAILED 1
+
+ // #define OFFSETOF(TYPE, ELE) ((size_t)&(((TYPE*)0)->ELE) - (size_t)&((TYPE *)0))
+ #define OFFSETOF(TYPE, ELE) __builtin_offsetof(TYPE, ELE)
+ #ifdef HOST_TAP
+ #define DESC_BASE_OFF(buf) ((buf->base) + OFFSETOF(nw_buf_t, desc))
+ #else

```

```

+#define DESC_BASE_OFF(buf) ((uint8_t*)((buf->base) + OFFSETOF(nw_buf_t, desc)))
+#endif
+#define BUF_INC_IDX(buf, idx) ((buf->flag)? (buf->flag)--
:(buf->idx)++)
+#define PREDEC_IDX(idx) ((idx)? idx-1: N_DESC)
+
+
+
+/*
+ * Common definition for network buffer
+ * @param rw_idx, to sync read/write between OCTEON and host
+ * @param len, len of read/write data.
+ * @param data, L2 network Buffer
+ */
-#typedef struct nw_buffer
-#
-# uint64_t rw_idx;
+typedef struct desc_t
+{
+ uint64_t len;
+ char data[MAX_ETH_FRAM_SZ];
-} nw_buf_t;
+} desc_t;

+typedef struct nw_buffer
+{
+ /*First two variables ridx and widx
+ * offsets should not change*/
+ uint64_t widx;
+ uint64_t ridx;
+#ifdef OCT_TAP
+ uint8_t * base;
+#else
+ uint64_t base;
+#endif
+ desc_t desc[N_DESC];
+ uint64_t flag;
+} nw_buf_t;
#define NW_BUF_SIZE (sizeof(nw_buf_t))

/* Functions */

int host_oct_tun_alloc(char *dev, int flags);
-void host_oct_pci_to_network(void *buf);
-void host_oct_network_to_pci(void * buf, int nread);
+int host_oct_pci_to_network(void *buf);
+int

```

```
host_oct_network_to_pci(void * buf, int tun_fd);
```

```
/*
 * Wrapper over nanosleep
 @@ -81,6 +109,7 @@ static inline void nsleep(long ns)
 nanosleep(&wait, NULL);
 }

 +
 #ifdef OCT_TAP
 #include "oct-tap-defs.h"
 #else
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/OCTEON-PCI-TAP-3.3.5-01_bidirectional.patch
```

No license file was found, but licenses were detected in source scan.

```
Copyright (c) 2017 by Cisco Systems, Inc.
```

```
All rights reserved.
```

```
diff -aur a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c
```

```
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c 2017-03-28
```

```
12:52:41.619920142 -0400
```

```
+++ a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c 2017-03-28
```

```
12:52:04.561296273 -0400
```

```
@@ -567,9 +567,11 @@
```

```
/* 5: Rad the cfg details */
```

```
memcpy(&oct_cfg,block_ptr,sizeof(oct_cfg));
```

```
+ /* Remove output to limit exposure */
```

```
+#ifdef CAVIUM_DEBUG
```

```
printf(" Received Firmware Configuration From Host(oct_cfg size: %lu) \n", sizeof(oct_cfg));
```

```
-
```

```
 cvmcs_dump_hostfw_config();
```

```
+#endif
```

```
ctrlq_num = CFG_GET_CTRL_Q_NO(&oct_cfg);
```

```
link_query_interval = CFG_GET_OCT_LINK_QUERY_INTERVAL(&oct_cfg);
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/suppress-hostfw-config-dump.patch
```

No license file was found, but licenses were detected in source scan.

```
Copyright (c) 2016 by Cisco Systems, Inc.
```



# All rights reserved.

```
CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h | 16 ++++++++
.../modules/driver/src/common/cn73xx_pf_regs.h | 6 +---
.../modules/driver/src/common/cn73xx_vf_regs.h | 6 +---
.../driver/src/firmware/apps/common/cvmcs-common.c | 8 +----
.../src/firmware/apps/common/cvmcs-common.mk | 2 +-
.../driver/src/firmware/apps/ioq/cvmcs-ioq-main.c | 41 ++++++++
.../modules/driver/src/firmware/core/core-driv.mk | 2 +-
.../driver/src/firmware/core/cvm-driver-defs.h | 13 +++++-
.../modules/driver/src/firmware/core/cvm-driv.c | 4 +--
.../modules/driver/src/firmware/core/cvm-pci-pko.c | 6 +++++
.../modules/driver/src/firmware/core/cvm-pci-pko.h | 3 +-
.../driver/src/host/linux/user/drv/octeon_ioif.c | 2 +-
12 files changed, 77 insertions(+), 32 deletions(-)
```

```
diff --git a/CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h
b/CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h
index 975002f..cb3da3f 100644
--- a/CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h
+++ b/CNNIC-SDK-SRC/modules/driver/inc/cavium_defs.h
@@ -198,11 +198,10 @@
#error Undefined __CAVIUM_BYTE_ORDER
#endif /*#if __CAVIUM_BYTE_ORDER == __CAVIUM_BIG_ENDIAN */
```

```
-
/** Swap 8B blocks */
#ifdef OCTEON_SWAP_DATA_BYTES
static __inline void
octeon_swap_8B_data(uint64_t *data, uint32_t blocks)
{
@@ -217,10 +216,25 @@ octeon_swap_8B_data(uint64_t *data UNUSED, uint32_t blocks UNUSED)
{
 return;
}
#endif

+static __inline void
+octeon_swap_info(uint64_t *data, uint32_t blocks)
+{
+ /*In OCTEON to host, Data + Response HDR is correct but resp_hdr->len seems
+ * to be swapped*/
+#ifdef OCTEON_LE
+ while(blocks) {
+ *data = ENDIAN_SWAP_8_BYTE(*data);
+ blocks--; data++;
```

```

+ }
+ #else
+ octeon_swap_8B_data(data, blocks);
+ #endif
+ }
+

#ifdef OCTEON_SWAP_DATA_BYTES
/** Swap 4B blocks */
static
__inline void
octeon_swap_4B_data(uint32_t *data, uint32_t blocks)
diff --git a/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_pf_regs.h b/CNNIC-SDK-
SRC/modules/driver/src/common/cn73xx_pf_regs.h
index 84d6a17..b833dc0 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_pf_regs.h
+++ b/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_pf_regs.h
@@ -50,18 +50,14 @@
#define CN73XX_CONFIG_PCIE_FLTMSK 0x720

-
-#ifndef OCTEON_LE
-#define CN73XX_ENDIAN_SWAP 0
-#else
+#ifndef CN73XX_ENDIAN_SWAP
#define CN73XX_ENDIAN_SWAP 1
#endif

-
-/*
-##### BAR0 Registers #####
-*/

#define CN73XX_SLI_CTL_PORT_START 0x286E0
diff --git a/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_vf_regs.h b/CNNIC-SDK-
SRC/modules/driver/src/common/cn73xx_vf_regs.h
index 4f1e4a7..5b3edeb 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_vf_regs.h
+++ b/CNNIC-SDK-SRC/modules/driver/src/common/cn73xx_vf_regs.h
@@ -45,15 +45,11 @@

#define
CN73XX_CONFIG_PCIE_FLTMSK 0x720

```

```

-
-
-#ifndef OCTEON_LE
-#define CN73XX_ENDIAN_SWAP 0
-#else
+#ifndef CN73XX_ENDIAN_SWAP
#define CN73XX_ENDIAN_SWAP 1
#endif

diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c
index 123fc00..a86ff04 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.c
@@ -359,11 +359,11 @@ cvmcs_app_check_mode()

```

```

-
+#if 0
/** MAIN */
int cvmcs_app_bringup()
{
//int i;

@@ -404,11 +404,11 @@ int cvmcs_app_bringup()
void
cvmcs_app_barrier()
{
cvmx_coremask_barrier_sync(&coremask_all);
}
-
+#endif

int
is_control_core(uint32_t core_id)
{
return (core_id == control_core);
@@ -509,11 +509,11 @@ cvmcs_common_hostfw_handshake(uint32_t app_mode, uint32_t hs_support)
uint32_t num_vfs = 0, rpvf = 0, num_pfs = 0, pvf_val
= 0;

uint64_t num_if = 0ULL;

```

```

uint64_t phys_addr = 0ULL, scratch_val= 0ULL;
uint8_t *block_ptr = NULL, counter = NB_ALLOC_MAX_TRIES;
- uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+ uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
int node = cvmx_get_node_num();
if(OCTEON_IS_MODEL(OCTEON_CN56XX))
 scratch_reg_addr = CVMX_PEXP_NPEI_SCRATCH_1;

CFG_GET_APP_MODE(&oct_cfg) = app_mode;
@@ -641,11 +641,11 @@ cvmcs_common_hostfw_handshake(uint32_t app_mode, uint32_t hs_support)

int cvmcs_common_send_active_indication()
{
 int node = cvmx_get_node_num();
 uint64_t scratch_val = 0LL, rst_boot =0;
- uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+ uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
 uint64_t coproc_clk =0, core_clk =0;

 /* Get pkind for DPI and send it to host */
 if(OCTEON_IS_MODEL(OCTEON_CN78XX) || OCTEON_IS_MODEL(OCTEON_CN73XX))
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.mk
b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.mk
index b798e26..3ad71c1 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.mk
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/common/cvmcs-common.mk
@@ -38,11 +38,11 @@ d := $(dir)
LIBRARY := $(OBJ_DIR)/libcvmcscommon.a

OBJS_$(d) := $(OBJ_DIR)/cvmcs-common.o
OBJS_$(d) += $(OBJ_DIR)/cvm-app.o

-$(OBJS_$(d)): CFLAGS_LOCAL := -I$(d) -I$(d)/../core/ -I$(CNNIC_ROOT)/modules/driver/src/common -O2 -
-g -Werror
+$(OBJS_$(d)): CFLAGS_LOCAL := -I$(d) -I$(d)/../core/ -I$(CNNIC_ROOT)/modules/driver/src/common -
I$(OCTEON_ROOT)/executive -O2 -g -Werror

standard component Makefile rules

DEPS_$(d) := $(OBJS_$(d):.o=.d)

diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
index a98b19e..7fb8234 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
+++

```

```

b/CNNIC-SDK-SRC/modules/driver/src/firmware/apps/ioq/cvmcs-ioq-main.c
@@ -96,11 +96,11 @@ cvmcs_ioq_hostfw_handshake()
 uint64_t scratch_reg_addr;

 if(OCTEON_IS_MODEL(OCTEON_CN56XX))
 scratch_reg_addr = CVMX_PEXP_NPEI_SCRATCH_1;
 else
- scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+ scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;

 /* 1: Send Indication to host about Host Firmware support. */
 scratch_val = HOSTFW_HS_SUPPORT_INDICATION;

 cvmx_write_csr_node(cvmx_get_node_num(), scratch_reg_addr, scratch_val);
@@ -347,11 +347,15 @@ cvmcs_ioq_send_to_pko3(cvmx_wqe_t *wqe, int dir, int port, int queue, int offl
 cvmcs_print("%d:%s", __LINE__, __FUNCTION__);
 return -1;
}

hdr_s = desc.hdr_s;
+#if 0
hdr_s->s.df = 0;
+#else
+ hdr_s->s.df = 1; //for LINA
+#endif
hdr_s->s.ii = 1;

if(offload) {

 uint8_t ip0, l4_proto = 0, ipoff = offload
- 1;
@@ -379,10 +383,19 @@ cvmcs_ioq_send_to_pko3(cvmx_wqe_t *wqe, int dir, int port, int queue, int offl
 else if (l4_proto == 132)
 hdr_s->s.ckl4 = /* SCTP */ 3;
 else
 hdr_s->s.ckl4 = /* Unknown */ 0;
}
+#if 1 //for LINA
+ /* Free buffer when finished with the packet */
+ cvmx_pko_send_free_t *send_free;
+ send_free = (void *) &desc.word[desc.num_words++];
+
+ send_free->u64 = 0;
+ send_free->s.subdc4 = CVMX_PKO_SENDSUBDC_FREE;
+ send_free->s.addr = (uint64_t)CVM_DRV_GET_PHYS(wqe);
+#endif

return cvmx_pko3_pdsc_transmit(&desc, queue, NULL);

```

```

}

@@ -478,11 +491,11 @@ cvmcs_ioq_forward_packet_to_host(cvmx_wqe_t *wqe, int rxq)
cvmx_buf_ptr_t *lptr;
cvmx_resp_hdr_t *resp_ptr;
cvmx_buf_ptr_pki_t *pki_lptr;
int port, queue;

-
+#if 0 //for LINA: as WQE and data share the same buffer.
 cvmx_wqe_set_bufs(wqe, cvmx_wqe_get_bufs(wqe) + 1);
 cvmx_wqe_set_len(wqe,
(cvmx_wqe_get_len(wqe) + CVMX_RESP_HDR_SIZE));

/* We use the space in WQE starting at byte offset 40 for the response
header information. This allows the WQE to be used for data and let
@@ -490,15 +503,26 @@ cvmcs_ioq_forward_packet_to_host(cvmx_wqe_t *wqe, int rxq)
lptr = (cvmx_buf_ptr_t *)&wqe->packet_data[48];
lptr->u64 = wqe->packet_ptr.u64;

resp_ptr = (cvmx_resp_hdr_t *)&wqe->packet_data[56];
#else
+ cvmx_wqe_set_len(wqe, (cvmx_wqe_get_len(wqe) + CVMX_RESP_HDR_SIZE));
+ resp_ptr = (cvmx_resp_hdr_t *) cvmx_phys_to_ptr(cvmx_wqe_get_pki_pkt_ptr(wqe).addr - 8);
#endif
resp_ptr->u64 = 0;
resp_ptr->s.opcode = CORE_NW_DATA_OP;
resp_ptr->s.destqport = rxq;
resp_ptr->s.csum_verified = 1; /* checksum notification to Host */

+#if 1 //for LINA
+ pki_lptr = (cvmx_buf_ptr_pki_t *)&wqe->packet_ptr;
+ pki_lptr->addr = CVM_DRV_GET_PHYS(resp_ptr);
+ pki_lptr->size += 8;
+
+
#else
if(OCTEON_IS_MODEL(OCTEON_CN78XX)
|| OCTEON_IS_MODEL(OCTEON_CN73XX)) {
/* lptr is reused */
pki_lptr = (cvmx_buf_ptr_pki_t *)&wqe->packet_ptr;
pki_lptr->u64 = 0;
pki_lptr->addr = CVM_DRV_GET_PHYS(resp_ptr);
@@ -510,11 +534,11 @@ cvmcs_ioq_forward_packet_to_host(cvmx_wqe_t *wqe, int rxq)
lptr->u64 = 0;
lptr->s.addr = CVM_DRV_GET_PHYS(resp_ptr);

```

```

lptr->s.size = 8;
lptr->s.pool = CVMX_FPA_WQE_POOL;
}
-
+#endif
DBG("wqe @ %p wqe bufs: %d len: %d pkt_ptr @ %lx\n", wqe,
 cvmx_wqe_get_bufs(wqe), cvmx_wqe_get_len(wqe), (unsigned long)(cvmx_phys_to_ptr(wqe-
>packet_ptr.s.addr)));

port = cvm_pci_get_oq_pkoport(rxq);
queue = cvm_pci_get_oq_pkoqueue(rxq);
@@ -693,11 +717,11 @@ int cvmcs_ioq_init_pci_drv()
* Share the PKI config number with host. This number will be updated
* in the instruction header of every frame sent by host to OCTEON.
*/
void cvmcs_ioq_send_pkind_to_host(void)
{
- uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+
uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
uint64_t scratch_val = 0ULL;
int node = -1;

if(!OCTEON_IS_MODEL(OCTEON_CN78XX))
return;
@@ -816,11 +840,11 @@ cvmcs_ioq_print_stats(void)
* Update the PKO ready state to host. This allows host to
* configure Input and Output queues for data transfer.
*/
void cvmcs_ioq_send_pko_ready(void)
{
- uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_1;
+ uint64_t scratch_reg_addr = CVMX_PEXP_SLI_SCRATCH_2;
uint64_t scratch_val = 0ULL;
uint32_t indication = 0;
int node = -1;

/* wait for ACK from HOST */
@@ -887,12 +911,15 @@ cvmcs_ioq_init_global()
cvmx_helper_pki_get_dflt_style(0, &style_cfg);

style_cfg.parm_cfg.first_skip = CVMX_HELPER_FIRST_MBUFF_SKIP;
style_cfg.parm_cfg.later_skip = CVMX_HELPER_NOT_FIRST_MBUFF_SKIP;
style_cfg.parm_cfg.dis_wq_dat = 1; /* use distinct buffers for WQE and packet data */
-
- cvmx_helper_pki_set_dflt_style(0, &style_cfg);
+ style_cfg.parm_cfg.fcs_strip

```

```

= 0;
+#ifdef OCTEON_LE
+ style_cfg.parm_cfg.pkt_lend = 1;
+#endif
+ cvmx_helper_pki_set_dflt_style(0, &style_cfg);

}
#endif
if (cvmx_helper_initialize_packet_io_global() == -1) {
printf("# cvmcs: Failed to initialize input ports\n");
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/core-drv.mk b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/core-drv.mk
index 6c14b39..fdda72d 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/core-drv.mk
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/core-drv.mk
@@ -52,11 +52,11 @@ OBJ_${d} += $(OBJ_DIR)/cn56xx_ep_comm.o
endif

-$(OBJ_${d}): CFLAGS_LOCAL := -I$(d) -I$(d)/../common -O2 -g -Werror
+$(OBJ_${d}): CFLAGS_LOCAL := -I$(d) -I$(d)/../common -I$(OCTEON_ROOT)/executive -O2 -g -Werror

standard component Makefile rules

DEPS_${d} := $(OBJ_${d}).o=.d

diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-driver-defs.h b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-driver-defs.h
index
6dc0d08..98dec30 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-driver-defs.h
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-driver-defs.h
@@ -46,24 +46,29 @@
#include "cvmx-bootmem.h"
#include "cvmx-spinlock.h"
#include "cvmx-version.h"
#include "cvmx-helper.h"
#include "cvmx-helper-util.h"
-#include "cvm-common-wqe.h"
-#include "cvm-common-defs.h"
-#include "cvm-common-misc.h"
-#include "cvm-common-fpa.h"
+//#include "cvm-common-wqe.h"
+//#include "cvm-common-defs.h"
+//#include "cvm-common-misc.h"
+//#include "cvm-common-fpa.h"

```



```

#include "cvm-drv-debug.h"

#ifndef __CAVIUM_LITTLE_ENDIAN
#define __CAVIUM_LITTLE_ENDIAN 1234
#endif

#ifndef __CAVIUM_BIG_ENDIAN
#define __CAVIUM_BIG_ENDIAN 4321
#endif

+#ifdef CVMX_FPA_WQE_POOL
+#undef CVMX_FPA_WQE_POOL
+#endif
+#define CVMX_FPA_WQE_POOL CVMX_FPA_PACKET_POOL
+
#define MAX_WQE_SIZE 128
/* OCTEON - III Supports Max of 48 cores */
#define MAX_DRV_CORES 48

/* In order to use PCIe port #1, set this macro to 1 */
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-drv.c
index 58e6998..2878be4 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-drv.c
@@ -66,21 +66,21 @@ cvm_drv_wait(int time_in_us)

void
cvm_drv_local_init()
{
+#if 0 //no used in LINA
cvmx_scratch_write64(CVM_DRV_SCR_PACKET_BUF_PTR, CVM_COMMON_ALLOC_INIT);
CVMX_SYNCW;
cvmx_fpa_async_alloc(CVM_DRV_SCR_PACKET_BUF_PTR, CVMX_FPA_PACKET_POOL);

cvmx_scratch_write64(CVM_DRV_SCR_WQE_BUF_PTR, CVM_COMMON_ALLOC_INIT);
CVMX_SYNCW;
cvmx_fpa_async_alloc(CVM_DRV_SCR_WQE_BUF_PTR, CVMX_FPA_WQE_POOL);

cvm_drv_core_id = cvmx_get_core_num();
-
-
+#endif
}

void

```

```

setup_pci_input_ports(void)
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c b/CNNIC-SDK-
SRC/modules/driver/src/firmware/core/cvm-pci-pko.c
index
12ec09c..dc25848 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.c
@@ -219,10 +219,14 @@ __cvm_post_pko3_command(cvmx_buf_ptr_t lptr,

hdr_s->u64 = 0;
hdr_s->s.df = 0; // H/W will free the buffers by default
hdr_s->s.ii = 0;

#ifdef OCTEON_LE
+ hdr_s->s.le = 1;
#endif
+
if(pko_ptr_type == PKO_GATHER_DATA) {
/* Process legacy gather list */
uint32_t i;
cvmx_buf_ptr_t *pPtr;
cvmx_buf_ptr_t blk;
@@ -464,10 +468,11 @@ cvmx_pko_send_data(cvmx_resp_hdr_t *user_resp_hdr,
cvm_ptr_type_t ptr_type,
uint32_t total_bytes,
uint32_t segs,
uint32_t port)
{
#ifdef 0 //not used in LINA, commented out to avoid dependence on cvm-drv-resources.h
cvmx_buf_ptr_t lptr;
cvmx_resp_hdr_t *resp_hdr;
uint16_t
gcount=0;
cvmx_buf_ptr_t *glist,*send_buf;

@@ -539,10 +544,11 @@ cvmx_pko_send_data(cvmx_resp_hdr_t *user_resp_hdr,
lptr.s.pool = CVM_PCI_PKO_FPA_POOL;
lptr.s.i = 1; /* PKO SHOULD FREE THIS BUFFER */
CVMX_SYNCWS;
cvm_pko_send_direct(lptr, PKO_GATHER_DATA, gcount, total_bytes, port);
}
#endif
return 0;
}

```

```
diff --git a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.h b/CNNIC-SDK-
```

```

SRC/modules/driver/src/firmware/core/cvm-pci-pko.h
index 8914fbc..67bf122 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.h
+++ b/CNNIC-SDK-SRC/modules/driver/src/firmware/core/cvm-pci-pko.h
@@ -41,13 +41,14 @@

#define CVM_USE_HW_CKSUM_OFFLOAD 1

#define CVM_PCI_PKO_FPA_POOL CVMX_FPA_PACKET_POOL
#define CVM_PCI_PKO_SCRATCHPAD CVM_DRV_SCR_PACKET_BUF_PTR
-
+##if 0 //not used in LINA, commented out to avoid dependence on cvm-drv-resources.h
#define CVM_PCI_PKO_ALLOC_BUFFER(ptr) \
 ptr = cvm_drv_fpa_alloc_async(CVM_PCI_PKO_SCRATCHPAD, CVM_PCI_PKO_FPA_POOL)
+##endif

#define CVM_PCI_PKO_FREE_BUFFER(ptr) \
 cvm_drv_fpa_free(ptr, CVM_PCI_PKO_FPA_POOL, 0)

#define CVM_PCI_PKO_SET_FPA_POOL(lptr) \
diff --git a/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/octeon_ioif.c b/CNNIC-SDK-
SRC/modules/driver/src/host/linux/user/drv/octeon_ioif.c
index af1299a..4a17d2d 100644
--- a/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/octeon_ioif.c
+++ b/CNNIC-SDK-SRC/modules/driver/src/host/linux/user/drv/octeon_ioif.c
@@ -310,11 +310,11 @@ Block *cnioq_get_quick(void *rxq, octeon_rx_info_t *rx_info)
 return NULL;
 }
 }
 info = &(droq->info_list[droq->host_read_index]);

- octeon_swap_8B_data((uint64_t *)info, 2);
+ octeon_swap_info((uint64_t *)info, 2);
 if(!info->length)
 {
 cavium_error("OCTEON:DROQ[%d] idx: %d len:0\n", droq->q_no, droq->host_read_index);
 return NULL;
 }

Found in
path(s):
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-rsync-
from-istanbul.patch
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```

```
OCTEON-SDK/host/coredump-util/Makefile | 9 +++++-----
OCTEON-SDK/host/debug-agent/Makefile | 9 +++++-----
OCTEON-SDK/host/remote-lib/Makefile | 7 +++++---
OCTEON-SDK/host/remote-utils/Makefile | 9 +++++-----
4 files changed, 19 insertions(+), 15 deletions(-)
```

```
diff --git a/OCTEON-SDK/host/coredump-util/Makefile b/OCTEON-SDK/host/coredump-util/Makefile
index f386adc..e965900 100644
```

```
--- a/OCTEON-SDK/host/coredump-util/Makefile
+++ b/OCTEON-SDK/host/coredump-util/Makefile
@@ -22,14 +22,15 @@ CFLAGS+= -static
LIBS_FLAGS += -lc -lnss_files -lnss_dns -lresolv
LIBS_CXX += -Wl,-lstdc++, -lc, -lnss_files, -lnss_dns, -lresolv
endif
endif
```

```
-CROSS=
-CC=$(CROSS)gcc
-CXX=$(CROSS)g++
-STRIP=$(CROSS)strip
+#CISCO_MODIFIED - changed from = to ?=
+CROSS?=
+CC?=$(CROSS)gcc
+CXX?=$(CROSS)g++
+STRIP?=$(CROSS)strip
```

```
BINS=$(DIR)/oct-remote-coredump
```

```
all: $(BINS) $(LIBS)
- $(STRIP) $(BINS)
```

```
diff --git a/OCTEON-SDK/host/debug-agent/Makefile b/OCTEON-SDK/host/debug-agent/Makefile
index 63dad9a..c1d157e 100644
```

```
--- a/OCTEON-SDK/host/debug-agent/Makefile
+++ b/OCTEON-SDK/host/debug-agent/Makefile
@@ -19,14 +19,15 @@ CFLAGS+= -static
LIBS_FLAGS += -lc -lnss_files -lnss_dns -lresolv
LIBS_CXX += -Wl,-lstdc++, -lc, -lnss_files, -lnss_dns, -lresolv
endif
endif
```

```
-CROSS=
-CC=$(CROSS)gcc
-CXX=$(CROSS)g++
-STRIP=$(CROSS)strip
+#CISCO_MODIFIED - changed from = to ?=
+CROSS?=
+CC?=$(CROSS)gcc
+CXX?=$(CROSS)g++
```

+STRIP?=\$(CROSS)strip

BINS=\$(DIR)/oct-debug-agent

all: \$(BINS) \$(LIBS)

- \$(STRIP) \$(BINS)

diff --git a/OCTEON-SDK/host/remote-lib/Makefile b/OCTEON-SDK/host/remote-lib/Makefile  
index 25ca60c..3a83c67 100644

--- a/OCTEON-SDK/host/remote-lib/Makefile

+++ b/OCTEON-SDK/host/remote-lib/Makefile

@@ -5,13 +5,14 @@ BOOTLOADER\_DEFINES =

CFLAGS = -g -O3 -W -Wall -D\_GNU\_SOURCE -DUSE\_RUNTIME\_MODEL\_CHECKS=1 -

DCVMX\_BUILD\_FOR\_LINUX\_HOST=1 \

-I

\$(OCTEON\_ROOT)/target/include \

\$(BOOTLOADER\_HEADERS) \$(EXTRA\_CFLAGS) \$(BOOTLOADER\_DEFINES)

DIR=.

-CROSS=

-CC=\$(CROSS)gcc

-AR=\$(CROSS)ar

+#CISCO\_MODIFIED - changed from = to ?=

+CROSS?=

+CC?=\$(CROSS)gcc

+AR?=\$(CROSS)ar

.PHONY: all

all: \$(DIR)/libocteon-remote.a

OBJ = \$(DIR)/octeon-remote.o \

diff --git a/OCTEON-SDK/host/remote-utils/Makefile b/OCTEON-SDK/host/remote-utils/Makefile  
index 11e4742..b41da23 100644

--- a/OCTEON-SDK/host/remote-utils/Makefile

+++ b/OCTEON-SDK/host/remote-utils/Makefile

@@ -28,14 +28,15 @@ CFLAGS+= -static

LIBS\_FLAGS += -lc -lnss\_files -lnss\_dns -lresolv

LIBS\_CXX += -Wl,-lstdc++,-lc,-lnss\_files,-lnss\_dns,-lresolv

endif

endif

-CROSS=

-CC=\$(CROSS)gcc

-CXX=\$(CROSS)g++

-STRIP=\$(CROSS)strip

+#CISCO\_MODIFIED - changed from = to ?=

+CROSS?=

+CC?=\$(CROSS)gcc

+CXX?=\$(CROSS)g++

```
+STRIP?=$(CROSS)strip
```

```
BINS = \
$(DIR)/oct-remote-boot \
$(DIR)/oct-remote-bootcmd \
$(DIR)/oct-remote-console \

```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cavium-fixup-
OCTEON-SDK-host-CROSS-CC-AR-STRIP-variables.patch
```

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```

```
OCTEON-SDK/application.mk | 7 ++++---
1 file changed, 4 insertions(+), 3 deletions(-)
```

```
diff --git a/OCTEON-SDK/application.mk b/OCTEON-SDK/application.mk
```

```
index 4e91c40..7b69d89 100644
```

```
--- a/OCTEON-SDK/application.mk
```

```
+++ b/OCTEON-SDK/application.mk
```

```
@@ -76,11 +76,12 @@ ifeq (${MATCH}, DUSE_RUNTIME_MODEL_CHECKS=1)
```

```
MADE_WITH_OCTEON_MODEL = "runtime"-${CVMX_USE_1_TO_1_TLB_MAPPINGS}
```

```
else
```

```
MADE_WITH_OCTEON_MODEL = $(OCTEON_MODEL)-$(CVMX_USE_1_TO_1_TLB_MAPPINGS)
```

```
endif
```

```
set special filename for target and change any spaces in it to commas
```

```
-MADE_WITH = $(shell echo "$(OBJ_DIR)/made_with-
```

```
OCTEON_MODEL=$(MADE_WITH_OCTEON_MODEL)=-ISA=${OCTEON_ISA}=-
```

```
OCTEON_CPPFLAGS_GLOBAL_ADD=$(OCTEON_CPPFLAGS_GLOBAL_ADD)=" | sed 's\ /,/g')
```

```
+MADE_WITH_CPPFLAGS_GLOBAL_ADD = $(shell echo "$(OCTEON_CPPFLAGS_GLOBAL_ADD)" | sed
's/-I.*\(\.*\)/-I...1/g')
```

```
+MADE_WITH = $(shell
```

```
echo "$(OBJ_DIR)/made_with-OCTEON_MODEL=$(MADE_WITH_OCTEON_MODEL)=-
```

```
ISA=${OCTEON_ISA}=-
```

```
OCTEON_CPPFLAGS_GLOBAL_ADD=$(MADE_WITH_CPPFLAGS_GLOBAL_ADD)=" | sed 's\ /,/g')
```

```
MADE_WITH_ALL = $(OBJ_DIR)/made_with-*
```

```
MADE_WITH_DIR = $(subst /,-,.cstamp-$(OBJ_DIR))
```

```
MADE_WITH_DIR_ALL = .cstamp-*
```

```
@@ -101,15 +102,15 @@ endif
```

```
$(MADE_WITH):
```

```
mkdir -p $(OBJ_DIR)
```

```
- rm -f $(MADE_WITH_ALL)
+ -rm -f $(MADE_WITH_ALL)
touch "$(MADE_WITH)"
```

```
$(MADE_WITH_DIR):
- rm -f $(MADE_WITH_DIR_ALL)
+ -rm -f $(MADE_WITH_DIR_ALL)
touch $(MADE_WITH_DIR)
```

```
$(OBJ_DIR)/cvmx-config-init.o: $(OCTEON_ROOT)/executive/cvmx-config-init.c
$(CC) $(CFLAGS_CONFIG_INIT) $(CFLAGS_GLOBAL) $(CFLAGS_LOCAL) -MMD -c -o $$@ $<
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/cvmx-lib-cavium-
fixup-OCTEON-SDK-application-mk.patch
```

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```
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```

```
OCTEON-SDK/host/remote-utils/oct-remote-boot.c | 2 +-
1 files changed, 1 insertions(+), 1 deletions(-)
```

```
diff --git a/OCTEON-SDK/host/remote-utils/oct-remote-boot.c b/OCTEON-SDK/host/remote-utils/oct-remote-
boot.c
```

```
index dfb3006..948ba90 100644
```

```
--- a/OCTEON-SDK/host/remote-utils/oct-remote-boot.c
```

```
+++ b/OCTEON-SDK/host/remote-utils/oct-remote-boot.c
```

```
@@ -653,11 +653,11 @@ int main(int argc, const char *argv[])
```

```
 if (octeon_remote_open(OCTEON_REMOTE_DEFAULT, 0))
 return -1;
```

```
 if (OCTEON_IS_OCTEON3() && !OCTEON_IS_MODEL(OCTEON_CN70XX)) {
```

```
- load_cache_flag = 1;
```

```
+ load_cache_flag = 0;
```

```
 printf("CN7XXX: Loading from cache automatically\n");
```

```
 }
```

```
 if (load_cache_flag)
```

```
 {
```

```
 if (OCTEON_IS_MODEL(OCTEON_CN3XXX) || OCTEON_IS_MODEL(OCTEON_CN5XXX))
```

Found in path(s):

```
* /opt/cola/permits/1305654281_1687300191.1584306/0/octeon-sdk-tar-gz/octeon-sdk/patches/oct-remote-boot-
c.patch
```

# 1.305 openssl 1.1.1o

## 1.305.1 Available under license :

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```
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```

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```
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```

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```
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```

<signature of Ty Coon>, 1 April 1989

Ty Coon, President of Vice

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The End

# 1.306 libnsl 1.2.0+git0+37c5ffe303

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Version 2.1, February 1999

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## 1.307 sysfsutils 2.1.0

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## 1.308 zlib 1.2.11

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/\* zlib.h -- interface of the 'zlib' general purpose compression library  
version 1.2.11, January 15th, 2017

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The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://tools.ietf.org/html/rfc1950> (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

\*/

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## 1.310 pyyaml 3.13

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