## cisco

To Whom It May Concern

A conformance review of Cisco IOS XE version 17.15 ("the Product") deployed on the following devices:

- Cisco C8500, C8500L Series Edge Platforms
- Cisco C8300, C8200, C8200L Series Edge Platforms
- Cisco Aggregation Services Router (ASR) 1000 series
- Cisco Integrated Services Router (ISR) 4000 series
- Cisco Integrated Services Router (ISR) 1000 series
- Cisco C8000V Edge Software Router

- Cisco IR 1100, 1800, 8100, 8300 Series Industrial Routers
- Cisco ESR 6300 Series Embedded Services
  Routers
- Cisco Aggregation Services Router (ASR) 920
- Cisco VG400 Series Voice Gateways
- Cisco Unified Border Element (CUBE)

was completed and found that the Product incorporates the following FIPS 140-2 validated cryptographic module:

- Cisco IOS Common Cryptographic Module IC2Mrel5a ((FIPS 140-2 Cert. <u>#4222</u>)
- FIPS Object Module (FOM) 7.2a (FIPS 140-2 Cert. <u>#4036</u>)

Cisco confirms that the cryptographic module listed above provides cryptographic services for the following as applicable:

- IPsec<sup>1</sup>
- TLS
- SSH
- SNMPv3
- SRTP

The review/testing confirmed that:

- 1. The cryptographic module (mentioned above) initializes in a way compliant with its Security Policy.
- 2. All applicable cryptographic algorithms used for session establishment are handled within the cryptographic module.
- 3. All applicable underlying cryptographic algorithms support each service's key derivation function.

This letter has been generated in accordance with guidance provided by the Cryptographic Module Validation Program (<u>CMVP</u>). The CMVP has not independently reviewed this analysis, testing, or the results.

Any questions regarding these statements may be directed via e-mail to the Cisco Global Certification Team (GCT) at <u>certteam@cisco.com</u>.

Sincerely,

Edward D Parades

Ed Paradise Cisco Senior Vice President Foundational & Government Security

<sup>&</sup>lt;sup>1</sup> IPsec is validated for control plane only. The ModP calculation for Diffie-Hellman is offloaded to hardware, and then used in the software implementation. The hardware acceleration chip used in the ModP calculation is not validated as part of this compliance review.