



## UCS Server BIOS Tokens

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### Server BIOS Tokens in Release 4.1(3h)

The platforms supported in this release 4.1(3h) are:

- B200 M5
- B480 M5
- C220 M5
- C240 M5
- C240 SD M5
- C480 M5
- S3260 M5
- C125 M5
- C480 M5 ML

The following table lists the newly added BIOS token names, including the defaults and values for each supported M5 platform in this release. The BIOS tokens listed in the [Server BIOS Tokens in Release 4.1\(1a\)](#) , [Server BIOS Tokens in Release 4.1\(2\) , on page 4](#) , [Server BIOS Tokens in Release 4.1\(3a\), on page 2](#) , and [Server BIOS Tokens in Release 4.1\(3e\), on page 2](#) table are also supported in this release.

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>SHA-1 PCR Bank</b>	Enabled	Enabled, Disabled	All M5 Servers	
<b>SHA-256 PCR Bank</b>	Enabled	Enabled, Disabled	All M5 Servers	

## Server BIOS Tokens in Release 4.1(3e)

The platforms supported in this release 4.1(3e) are:

- B200 M5
- B480 M5
- C220 M5
- C240 M5
- C240 SD M5
- C480 M5
- S3260 M5
- C125 M5
- C480 M5 ML

The following table lists the newly added BIOS token names, including the defaults and values for each supported M5 platform in this release. The BIOS tokens listed in the [Server BIOS Tokens in Release 4.1\(1a\)](#), [Server BIOS Tokens in Release 4.1\(2\)](#), on page 4, and [Server BIOS Tokens in Release 4.1\(3a\)](#), on page 2 table are also supported in this release.

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
Burst and Postponed Refresh	Disabled	Enabled, Disabled	All M5 Servers	

## Server BIOS Tokens in Release 4.1(3a)

The platforms supported in this release 4.1(3a) are:

- B200 M5
- B480 M5
- C220 M5
- C240 M5
- C240 SD M5
- C480 M5
- S3260 M5
- C125 M5
- C480 M5 ML

The following table lists the newly added BIOS token names, including the defaults and values for each supported M5 platform in this release. The BIOS tokens listed in the [Server BIOS Tokens in Release 4.1\(1a\)](#) and [Server BIOS Tokens in Release 4.1\(2\)](#), on page 4 table are also supported in this release.

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
Memory Thermal Throttling Mode	Closed Loop Thermal Throttling (CLTT) with Platform Environment Control Interface (PECI)	Closed Loop Thermal Throttling (CLTT) with Platform Environment Control Interface (PECI), Disabled	All M5 Servers <b>Note</b> This token is not supported on C125 M5 servers.	
Advanced Memory Test	Disabled	Enabled, Disabled	All M5 Servers <b>Note</b> This token is not supported on C125 M5 servers.	
Transparent Secure Memory Encryption (TSME)	Auto	Enabled, Disabled, Auto	C125 M5 servers	
Secure Encrypted Virtualization (SEV)	Auto	253 ASIDs, 509 ASIDs, Auto	C125 M5 servers	
DRAM SW Thermal Throttling	Disabled	Enabled, Disabled	C125 M5 servers	
Memory Refresh Rate	1x Refresh	1x Refresh, 2x Refresh	All M5 Servers	
Panic and High Watermark	Low	High, Low	All M5 Servers	
PCIe PLL SSC	Disabled	Auto, Disabled, ZeroPointFive	All M5 Servers	
Configurable TDP Level	Normal	Normal, Level 1, Level 2	All M5 Servers	
Uncore Frequency Scaling	Enabled	Enabled, Disabled	All M5 Servers	
UPI Link Frequency Select	Auto	Auto, 9.6GT/s, 10.4GT/s	All M5 Servers	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
External SSC Enable	Disabled for blade and Cisco UCS S3260 M5 Storage servers  Enabled for rack servers (except for Cisco UCS S3260 M5 Storage servers)	Enabled, Disabled	All M5 Servers	
CR QoS	Disabled	Recipe 1, Recipe 2, Recipe 3, Disabled	All M5 Servers	
NVM Performance Setting	BW Optimized	BW Optimized, Latency Optimized, Balanced Profile	All M5 Servers	
CR FastGo Config	Auto	Default, Option 1, Option 2, Option 3, Option 4, Option 5, Auto	All M5 Servers	
Snoopy mode for 2LM	Disabled	Enabled, Disabled	All M5 Servers	
Snoopy mode for AD	Disabled	Enabled, Disabled	All M5 Servers	

## Server BIOS Tokens in Release 4.1(2)

Cisco UCS Manager, Release 4.1(2) introduces the Cisco UCS C240 SD M5 Server to the list of supported platforms. The platforms supported in this release are:

- B200 M5
- B480 M5
- C220 M5
- C240 M5
- C240 SD M5
- C480 M5
- S3260 M5
- C125 M5
- C480 M5 ML

The following table lists the newly added BIOS token names, including the defaults and values for each supported M5 platform in this release. The BIOS tokens listed in the [Server BIOS Tokens in Release 4.1\(1a\)](#) table are also supported in this release.

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
Memory Refresh Rate	2x Refresh	1x Refresh, 2x Refresh	All M5 Servers	
Panic and High Watermark	Low	High, Low	All M5 Servers	
PCIe PLL SSC	Disabled	Auto, Disabled, ZeroPointFive	All M5 Servers	
Configurable TDP Level	Normal	Normal, Level 1, Level 2	All M5 Servers	
Uncore Frequency Scaling	Enabled	Enabled, Disabled	All M5 Servers	
UPI Link Frequency Select	Auto	Auto, 9.6GT/s, 10.4GT/s	All M5 Servers	
External SSC Enable	Disabled for blade and Cisco UCS S3260 M5 Storage servers  Enabled for rack servers (except for Cisco UCS S3260 M5 Storage servers)	Enabled, Disabled	All M5 Servers	
CR QoS	Disabled	Recipe 1, Recipe 2, Recipe 3, Disabled	All M5 Servers	
NVM Performance Setting	BW Optimized	BW Optimized, Latency Optimized, Balanced Profile	All M5 Servers	
CR FastGo Config	Auto	Default, Option 1, Option 2, Option 3, Option 4, Option 5, Auto	All M5 Servers	
Snoopy mode for 2LM	Disabled	Enabled, Disabled	All M5 Servers	
Snoopy mode for AD	Disabled	Enabled, Disabled	All M5 Servers	

## Server BIOS Tokens in Release 4.1(1a)

The UCS M5 platforms supported in this release are:

- B200 M5
- B480 M5
- C220 M5
- C240 M5
- C480 M5
- S3260 M5
- C125 M5
- C480 M5 ML

The following table lists the BIOS token names, including the defaults and values for each supported M5 platform in this release.

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Legacy USB Support</b>	Enabled	Disabled , Enabled, Auto	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>FRB-2 Timer</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>OS Boot Watchdog Timer Policy</b>	Power Off	Power Off, Reset	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	Applicable only when <b>OS Boot Watchdog Timer</b> is <b>Enabled</b> .
<b>OS Boot Watchdog Timer Timeout</b>	10 minutes	5 minutes, 10 minutes, 15 minutes, 20 minutes	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	Applicable only when <b>OS Boot Watchdog Timer</b> is <b>Enabled</b> .
<b>OS Boot Watchdog Timer</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Flow Control</b>	None	None, RTS-CTS	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	Applicable only when <b>Console Redirection</b> is <b>COM 0</b> .

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Baud Rate</b>	115.2k	9.6k, 19.2k, 38.4k, 57.6k, 115.2k	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	Applicable only when <b>Console Redirection</b> is <b>COM 0</b> .
<b>Terminal Type</b>	VT100	PC-ANSI, VT100, VT100-PLUS, VT-UTF8	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	Applicable only when <b>Console Redirection</b> is <b>COM 0</b> .
<b>Console Redirection</b>	Disabled	COM 0, COM 1, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Number of Retries</b>	13	Infinite, 5, 13	B200 M5 B480 M5 C480 M5	Applicable only when <b>Boot Option Retry</b> is <b>Enabled</b> .
<b>Cool Down Time (sec)</b>	90	15, 45, 90	B200 M5 B480 M5 C480 M5	Applicable only when <b>Boot Option Retry</b> is <b>Enabled</b> .
<b>Boot Option Retry</b>	Disabled	Enabled, Disabled	B200 M5 B480 M5 C480 M5	
<b>Trusted Platform Module (TPM)</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Intel Virtualization Technology</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Intel VT for directed IO</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Intel Trusted Execution Technology Support</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	Can be <b>Enabled</b> only when <b>Trusted Platform Module (TPM)</b> is <b>Enabled</b> .  Enabling <b>Intel Trusted Execution Technology Support</b> auto enables <b>Intel VT for directed IO</b> and <b>Intel Virtualization Technology</b> .
<b>Local X2 Apic</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	Enabling <b>Local X2 Apic</b> auto enables <b>Intel VT for directed IO</b> .
<b>Intel VTD Coherency Support</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Intel VTD ATS support</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>UEFI Boot Mode</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>NUMA Optimized</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Select Memory RAS configuration</b>	ADDDC Sparing	Mirror Mode 1LM, Partial Mirror mode 1LM, Maximum Performance, ADDDC Sparing	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
Select PPR Type	Hard PPR	Hard PPR, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Memory Mapped IO Above 4GB</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	



Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Hardware Prefetcher</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Adjacent Cache Line Prefetcher</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>DCU Streamer Prefetch</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>DCU IP Prefetcher</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Processor C1E</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Processor C6 Report</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Execute Disable Bit</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Intel Turbo Boost Tech</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>P STATE Coordination</b>	HW ALL	HW ALL, SW ALL, SW ANY	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Boot Performance Mode</b>	Max Performance	Max Performance, Max Efficient, Set by Intel NM	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Enhanced Intel SpeedStep Tech</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	Disabling <b>Enhanced Intel SpeedStep Tech</b> auto disables <b>Intel Turbo Boost Tech</b> .
<b>Intel HyperThreading Tech</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	

<b>Name</b>	<b>Default Value</b>	<b>M5 Server Supported Values</b>	<b>Platform</b>	<b>Dependencies</b>
<b>CPU Performance</b>	Custom	Enterprise, HPC, High Throughput, Custom	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
				<p>If set as <b>Enterprise</b> then:</p> <ul style="list-style-type: none"> <li>• <b>Hardware Prefetcher is Enabled</b></li> <li>• <b>Adjacent Cache Line Prefetcher is Enabled</b></li> <li>• <b>DCU Streamer Prefetch is Enabled</b></li> <li>• <b>DCU IP Prefetcher is Enabled</b></li> </ul> <p>If set as <b>High Throughput</b> then:</p> <ul style="list-style-type: none"> <li>• <b>Hardware Prefetcher is Disabled</b></li> <li>• <b>Adjacent Cache Line Prefetcher is Disabled</b></li> <li>• <b>DCU Streamer Prefetch is Disabled</b></li> <li>• <b>DCU IP Prefetcher is Enabled</b></li> </ul> <p>If set as <b>HPC</b> then:</p> <ul style="list-style-type: none"> <li>• <b>Hardware Prefetcher is Enabled</b></li> <li>• <b>Adjacent Cache Line Prefetcher is Enabled</b></li> <li>• <b>DCU Streamer Prefetch is</b></li> </ul>

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
				<b>Enabled</b> <ul style="list-style-type: none"> <li>• <b>DCU IP Prefetcher</b> is <b>Enabled</b></li> </ul>
<b>CDN Control</b>	Enabled	Enabled, Disabled	B480 M5, B200 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	By default, CDN Control is <b>Disabled</b> in the UCSM Manager. (It is <b>Enabled</b> by default in Standalone mode.)
<b>Processor CMCI</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Adaptive Memory Training</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C480 M5 ML	
<b>OptionROM Launch Optimization</b>	Enabled	Enabled, Disabled	All M5 Servers	
<b>BIOS Techlog Level</b>	Minimum	Maximum, Normal, Minimum	All M5 Servers	
<b>All Onboard LOM</b>	Enabled	Enabled, Disabled	C220 M5, C240 M5, C480 M5	If set as <b>Disabled</b> , then <b>LOM Port 0 OptionROM</b> and <b>LOM Port 1 OptionROM</b> are <b>Disabled</b> .  If set as <b>Enabled</b> , then <b>LOM Port 0 OptionROM</b> and <b>LOM Port 1 OptionROM</b> are <b>Enabled</b> .
<b>VMD Enable</b>	Disabled	Enabled, Disabled	C220 M5, C240 M5, B200 M5, B480 M5	
<b>PCIe RAS Support</b>	Enabled	Enabled, Disabled	All M5 Servers	
<b>ACS Control SLOT-11</b>	Enabled	Enabled, Disabled	C480 M5	

<b>Name</b>	<b>Default Value</b>	<b>M5 Server Supported Values</b>	<b>Platform</b>	<b>Dependencies</b>
<b>ACS Control SLOT-12</b>	Enabled	Enabled, Disabled	C480 M5	
<b>ACS Control SLOT-13</b>	Enabled	Enabled, Disabled	C480 M5	
<b>ACS Control SLOT-14</b>	Enabled	Enabled, Disabled	C480 M5	
<b>ACS Control GPU-1</b>	Disabled	Enabled, Disabled	C480 M5	
<b>ACS Control GPU-2</b>	Disabled	Enabled, Disabled	C480 M5	
<b>ACS Control GPU-3</b>	Disabled	Enabled, Disabled	C480 M5	
<b>ACS Control GPU-4</b>	Disabled	Enabled, Disabled	C480 M5	
<b>ACS Control GPU-5</b>	Disabled	Enabled, Disabled	C480 M5	
<b>ACS Control GPU-6</b>	Disabled	Enabled, Disabled	C480 M5	
<b>ACS Control GPU-7</b>	Disabled	Enabled, Disabled	C480 M5	
<b>ACS Control GPU-8</b>	Disabled	Enabled, Disabled	C480 M5	
<b>LOM Port 1 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5 C480 M5	
<b>LOM Port 2 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5 C480 M5	
<b>SBNVMe1 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>SBNVMe2 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>IOEMezz1 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>IOENVMe1 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>IOENVMe2 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>IOESlot1 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>IOESlot2 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>SBMezz1 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>SBMezz2 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>SIOC1 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>SIOC2 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>SIOCNvme1 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>SIOCNvme2 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>SBLom1 OptionROM</b>	Enabled	Disabled, Enabled	S3260 M5	
<b>SBNVMe1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>SBNVMe2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>SIOCNVMe1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>SIOCNVMe2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>SIOC1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>SIOC2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>SBMezz1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>SBMezz2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>IOESlot1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>IOESlot2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>IOEMezz1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	

<b>Name</b>	<b>Default Value</b>	<b>M5 Server Supported Values</b>	<b>Platform</b>	<b>Dependencies</b>
<b>IOENVMe1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>IOENVMe2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	S3260 M5	
<b>PCIe Slot 1 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5 C480 M5	
<b>PCIe Slot 2 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5 C480 M5	
<b>PCIe Slot 3 OptionROM</b>	Enabled	Disabled, Enabled	C240 M5 C480 M5	
<b>PCIe Slot 4 OptionROM</b>	Enabled	Disabled, Enabled	C240 M5 C480 M5	
<b>PCIe Slot 5 OptionROM</b>	Enabled	Disabled, Enabled	C240 M5 C480 M5	
<b>PCIe Slot 6 OptionROM</b>	Enabled	Disabled, Enabled	C240 M5 C480 M5	
<b>PCIe Slot 7 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot 8 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot 9 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot 10 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot 11 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot 12 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot 13 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot 14 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>PCIe Slot MLOM OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5	
<b>PCIe Slot MRAID OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5	
<b>PCIe Slot RAID OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N1 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5, C480 M5	
<b>PCIe Slot N2 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5, C480 M5	
<b>PCIe Slot N3 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5, C480 M5	
<b>PCIe Slot N4 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5, C480 M5	
<b>PCIe Slot N5 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5, C480 M5	
<b>PCIe Slot N6 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5, C480 M5	
<b>PCIe Slot N7 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5, C480 M5	
<b>PCIe Slot N8 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C240 M5, C480 M5	
<b>PCIe Slot N9 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C480 M5	
<b>PCIe Slot N10 OptionROM</b>	Enabled	Disabled, Enabled	C220 M5, C480 M5	
<b>PCIe Slot N11 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N12 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N13 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N14 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N15 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	



<b>Name</b>	<b>Default Value</b>	<b>M5 Server Supported Values</b>	<b>Platform</b>	<b>Dependencies</b>
<b>PCIe Slot N16 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N17 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N18 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N19 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N20 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N21 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N22 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N23 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot N24 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot Rear NVMe1 OptionROM</b>	Enabled	Disabled, Enabled	C240 M5, C480 M5	
<b>PCIe Slot Rear NVMe2 OptionROM</b>	Enabled	Disabled, Enabled	C240 M5, C480 M5	
<b>PCIe Slot Rear NVMe3 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot Rear NVMe4 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot Rear NVMe5 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot Rear NVMe6 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>PCIe Slot Rear NVMe7 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>PCIe Slot Rear NVMe8 OptionROM</b>	Enabled	Disabled, Enabled	C480 M5	
<b>MRAID Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C220 M5, C240 M5	
<b>RAID Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C480 M5	
<b>MLOM Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C220 M5, C240 M5	
<b>PCIe Slot 1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C220 M5, C240 M5 C480 M5 C125 M5	
<b>PCIe Slot 2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C220 M5, C240 M5 C480 M5 C125 M5	
<b>PCIe Slot 3 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C240 M5 C480 M5 C125 M5	
<b>PCIe Slot 4 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C240 M5 C480 M5 C125 M5	
<b>PCIe Slot 5 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C240 M5 C480 M5 C125 M5	
<b>PCIe Slot 6 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C240 M5 C480 M5 C125 M5	
<b>PCIe Slot 7 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C480 M5 C125 M5	
<b>PCIe Slot 8 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C480 M5 C125 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>PCIe Slot 9 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C480 M5 C125 M5	
<b>PCIe Slot 10 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C480 M5 C125 M5	
<b>PCIe Slot 11 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C480 M5 C125 M5	
<b>PCIe Slot 12 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C480 M5 C125 M5	
<b>Front NVME1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C220 M5, C240 M5	
<b>Front NVME2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C220 M5, C240 M5	
<b>Rear NVME1 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C240 M5	
<b>Rear NVME2 Link Speed</b>	Auto	Disabled, Auto, GEN1, GEN2, GEN3	C240 M5	
<b>Power On Password</b>	Disabled	Enabled, Disabled	C220 M5, C240 M5 C480 M5 C125 M5	
<b>P-SATA mode</b>	For S3260 M5: AHCI  For all other servers: LSI SW RAID	AHCI, LSI SW RAID, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Processor Prefetch Config</b>	Auto	Enabled, Disabled, Auto	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>VGA Priority</b>	OnBoard	OnBoard, OffBoard, OnBoard VGA Disabled	C220 M5, C240 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Core Multi Processing</b>	All	All, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>USB Port Front</b>	Enabled	Enabled, Disabled	C220 M5, C240 M5 C480 M5	
<b>USB Port Rear</b>	Enabled	Enabled, Disabled	C220 M5, C240 M5 C480 M5	
<b>USB Port KVM</b>	Enabled	Enabled, Disabled	C220 M5, C240 M5 C480 M5	
<b>USB Port VMedia</b>	Enabled	Enabled, Disabled	C480 M5	
<b>USB Port Internal</b>	Enabled	Enabled, Disabled	C220 M5, C240 M5 C480 M5	
<b>USB Port SD Card</b>	Enabled	Enabled, Disabled	C220 M5, C240 M5 C480 M5	
<b>Workload Configuration</b>	IO Sensitive	Balanced, IO Sensitive	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>IMC Interleave</b>	Auto	1-way Interleave, 2-way Interleave, Auto	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Sub NUMA Clustering</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>KTI Prefetch</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>XPT Prefetch</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Energy Performance Tuning</b>	OS	OS, BIOS	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	If set as <b>OS</b> then <b>Energy Performance</b> is <b>Balanced Performance</b> .  If set as <b>BIOS</b> then, <b>Energy Performance</b> is <b>Performance, Balanced Performance, Balanced Power, or Power</b> .
<b>Energy Performance</b>	Balanced Performance	Performance, Balanced Performance, Balanced Power, Power	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Package C State Limit</b>	C0 C1 State	No Limit, Auto, C0 C1 State, C2, C6 Non Retention, C6 Retention	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>LLC Prefetch</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>CPU Hardware Power Management</b>	HWPM Native Mode	Disabled, HWPM Native Mode, HWPM OOB Mode, Native Mode with no Legacy	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	If set as <b>HWPM OOB Mode</b> then, <b>ProcessorEppProfile</b> is set as <b>Performance, Balanced Performance, or Balanced Power, Power</b> .
<b>IPV6 PXE Support</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Energy Efficient Turbo</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Autonomous Core C-state</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>ProcessorEppProfile</b>	Balanced Performance	Performance, Balanced Performance, Balanced Power, Power	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Patrol Scrub</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>Intel Speed Select</b>	Base	Base, Option 1, Option 2	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5	
<b>BME DMA Mitigation</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>CDN Support for LOMs</b>	Disabled	Enabled, Disabled	C480 M5	
<b>DCPMM Firmware Downgrade</b>	Disabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Core Performance Boost</b>	Auto	Auto, Disabled	C125 M5	
<b>Global C-state Control</b>	Auto	Auto, Enabled, Disabled	C125 M5	
<b>L1 Stream HW Prefetcher</b>	Auto	Auto, Enabled, Disabled	C125 M5	
<b>L2 Stream HW Prefetcher</b>	Auto	Auto, Enabled, Disabled	C125 M5	
<b>AMD Memory Interleaving</b>	Auto	Auto, None, Channel, Die, Socket	C125 M5	
<b>AMD Memory Interleaving Size</b>	Auto	Auto, 256 Bytes, 512 Bytes, 1 KB, 2 KB	C125 M5	
<b>Chipselect Interleaving</b>	Auto	Auto, Disabled	C125 M5	

Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Bank Group Swap</b>	Auto	Auto, Enabled, Disabled	C125 M5	
<b>Determinism Slider</b>	Auto	Auto, Power, Performance	C125 M5	
<b>IOMMU</b>	Auto	Auto, Enabled, Disabled	C125 M5	
<b>Downcore control</b>	Auto	Auto, TWO (1 + 1), TWO (2 + 0), THREE (3 + 0), FOUR (2 + 2), FOUR (4 + 0), SIX (3 + 3)	C125 M5	
<b>SMT Mode</b>	Auto	Auto, Off	C125 M5	
<b>SVM Mode</b>	Enabled	Enabled, Disabled	C125 M5	
<b>SMEE</b>	Enabled	Enabled, Disabled	C125 M5	
<b>PCIe Slot GPU1 OptionROM</b>	Enabled	Enabled, Disabled	C480 M5 ML	
<b>PCIe Slot GPU2 OptionROM</b>	Enabled	Enabled, Disabled	C480 M5 ML	
<b>PCIe Slot GPU3 OptionROM</b>	Enabled	Enabled, Disabled	C480 M5 ML	
<b>PCIe Slot GPU4 OptionROM</b>	Enabled	Enabled, Disabled	C480 M5 ML	
<b>PCIe Slot GPU5 OptionROM</b>	Enabled	Enabled, Disabled	C480 M5 ML	
<b>PCIe Slot GPU6 OptionROM</b>	Enabled	Enabled, Disabled	C480 M5 ML	
<b>PCIe Slot GPU7 OptionROM</b>	Enabled	Enabled, Disabled	C480 M5 ML	
<b>PCIe Slot GPU8 OptionROM</b>	Enabled	Enabled, Disabled	C480 M5 ML	
<b>Slot 1 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 2 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	

<b>Name</b>	<b>Default Value</b>	<b>M5 Server Supported Values</b>	<b>Platform</b>	<b>Dependencies</b>
<b>Slot 3 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 4 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 5 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 6 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 7 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 8 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 9 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 10 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 11 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 12 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 13 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	
<b>Slot 14 state</b>	Enabled	Enabled, Disabled, UEFI Only, Legacy Only	C480 M5 ML	



Name	Default Value	M5 Server Supported Values	Platform	Dependencies
<b>Partial Memory Mirror Mode</b>	Disabled	Percentage, Value in GB, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Partial Mirror percentage</b>	0.00	0.00 - 50.00 with a step size of 0.01	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Partial Mirror1 Size in GB</b>	0	0 - 65535 with a step size of 1	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Partial Mirror2 Size in GB</b>	0	0 - 65535 with a step size of 1	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Partial Mirror3 Size in GB</b>	0	0 - 65535 with a step size of 1	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Partial Mirror4 Size in GB</b>	0	0 - 65535 with a step size of 1	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Memory Size Limit in GB</b>	0	0 - 65535 with a step size of 1	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>Network Stack</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	
<b>IPV4 PXE Support</b>	Enabled	Enabled, Disabled	B200 M5, B480 M5, C220 M5, C240 M5, C480 M5, S3260 M5, C125 M5	

